Session Program

12-16 Sept 2005

11th Workshop on Electronics for LHC and future Experiments

Parallel session A1

Heidelberg
Germany

Tuesday 13 September

11:00

Parallel session A1

Session | **Location:** Heidelberg, Germany

11:00–11:25

A VLSI Full Custom ASIC Front End for the Optical Module of NEMO Underwater Neutrino Detector

Speaker

Dr Domenico Lo Presti

11:25–11:50 **Delay25, an ASIC for timing adjustment in LHC**

Speaker

Mr Hugo Furtado

11:50–12:15 **The 0.25um Token Bit Manager for CMS Pixel Readout**

Speaker

Mr Edward Bartz

12:15–12:40

A current-based readout ASIC with on chip pedestal subtraction and zero suppression for a fast readout at a future collider

Speaker

Mr Marcel Trimpl

13:05