Session Program

12-16 Sept 2005

11th Workshop on Electronics for LHC and future Experiments

Parallel session B4

Heidelberg Germany

Thursday 15 September

11:	00-11:25 ATLAS DAQ/HLT infrastructure
Spe a Dr Yi	aker uri ERMOLINE
	25-11:50
	VMEbus processor hardware and software infrastructure in ATLAS
Spea Mr M	aker 1arkus Joos
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11:	50-12:15
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