

Session Program

12-16 Sept 2005

**11th Workshop on Electronics for LHC and
future Experiments**

Parallel session A3

Heidelberg
Germany

Tuesday 13 September

16:25

Parallel session A3

Session | Location: Heidelberg, Germany

16:25-16:50

Mass production testing of front-end ASICs for ALICE SDD system

Speaker

Dr Luca Toscano

16:50-17:15

Production Testing and Quality Assurance of the CMS Preshower Front-end Chips - PACE3

Speaker

Mr Nikolaos Manthos

17:15-17:40

Production of the LHCb Silicon Tracker Readout Electronics

Speaker

Dr Achim Vollhardt

17:40-18:40

Microelectronic WG meeting

Speaker

Alesandro Marchioro

19:00