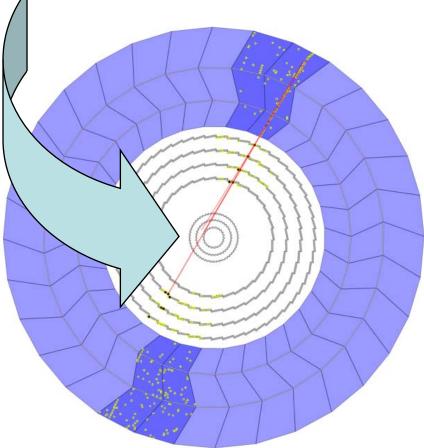




Commissioning of the ATLAS Pixel Detector with Cosmic Data



- Overview
 - LHC and ATLAS
 - Pixel Detector
- Commissioning
- Readout
- Calibration
- Cosmic Runs

Evgeny Galyaev (University of Texas at Dallas) On behalf of the ATLAS Pixel Collaboration

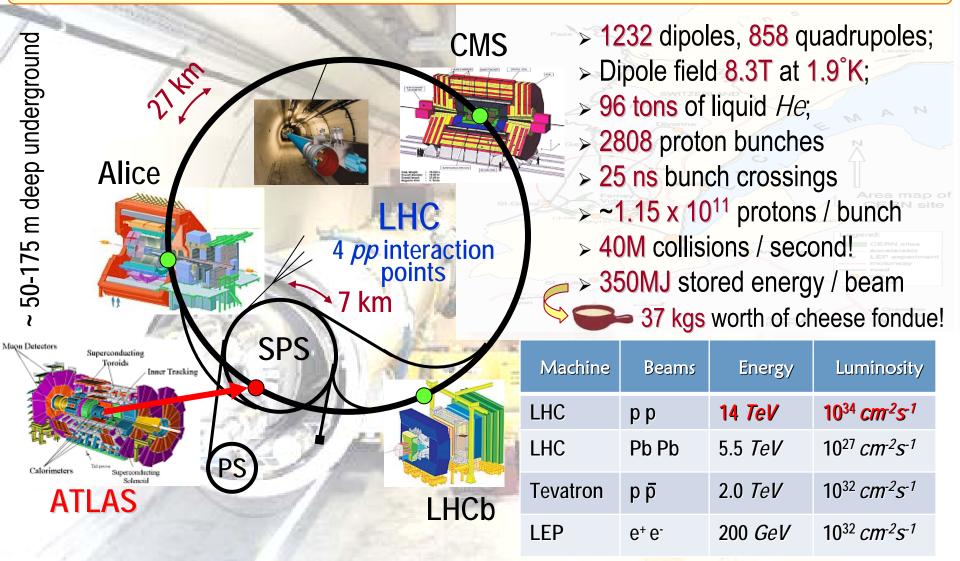


2009 Meeting of the Division of Particles and Fields of the American Physical Society



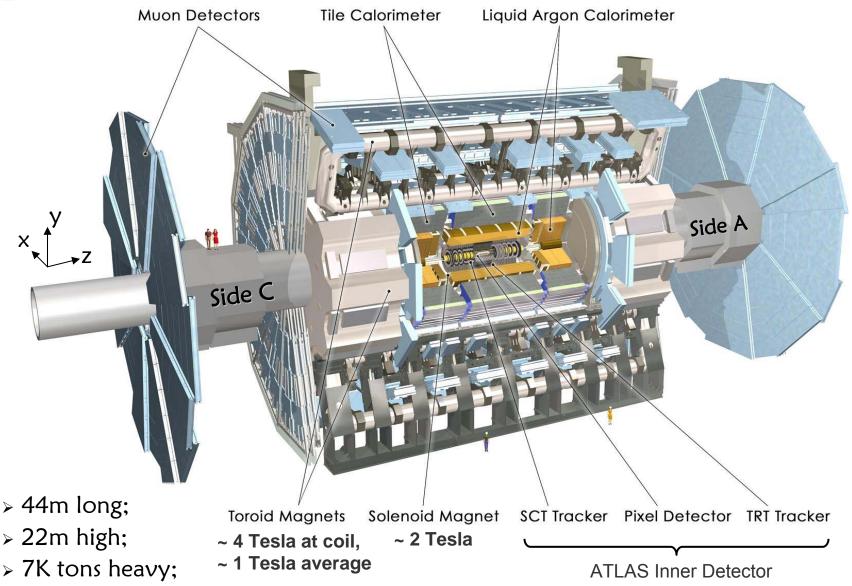


MOTIVATION: To find Higgs Boson and discover New Physics beyond Standard Model





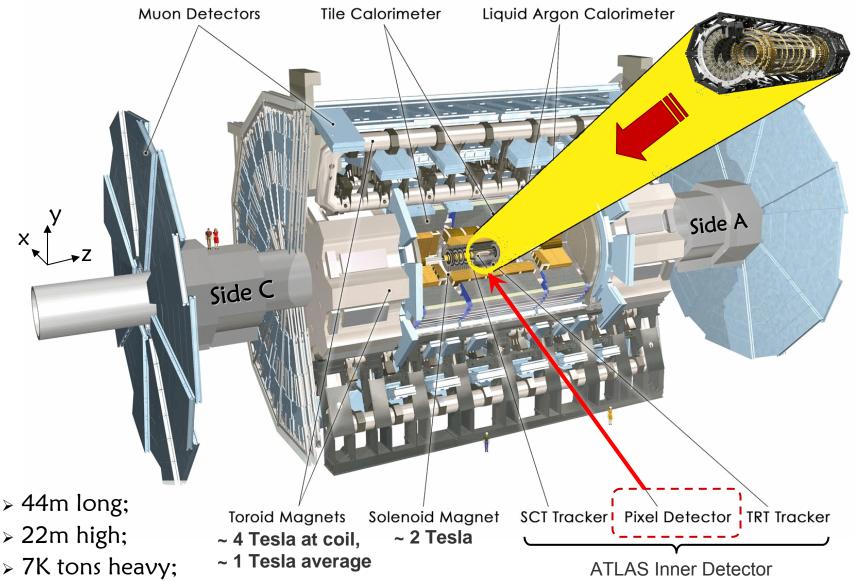
ATLAS: A Toroidal LHC ApparatuS





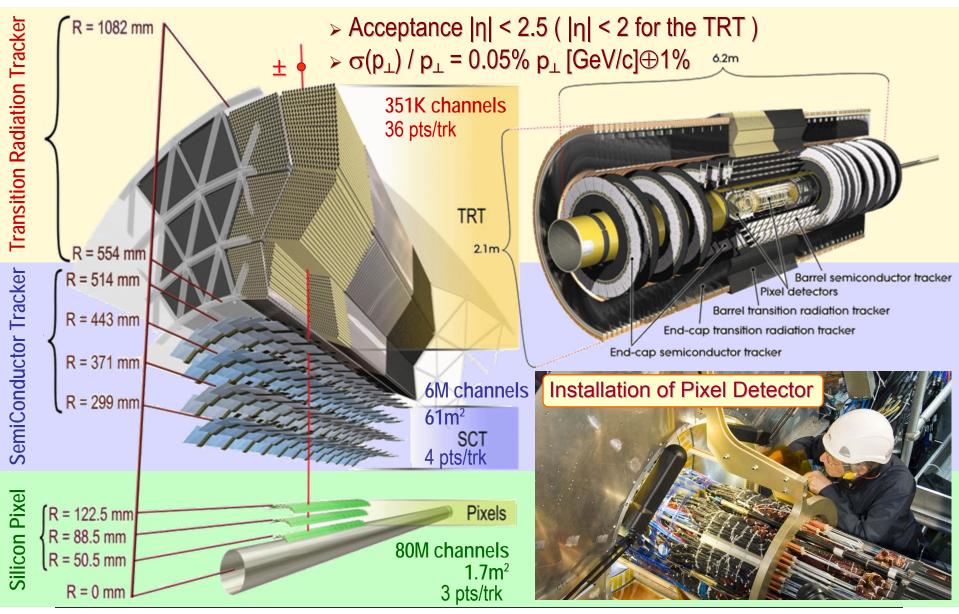
ATLAS: A Toroidal LHC ApparatuS







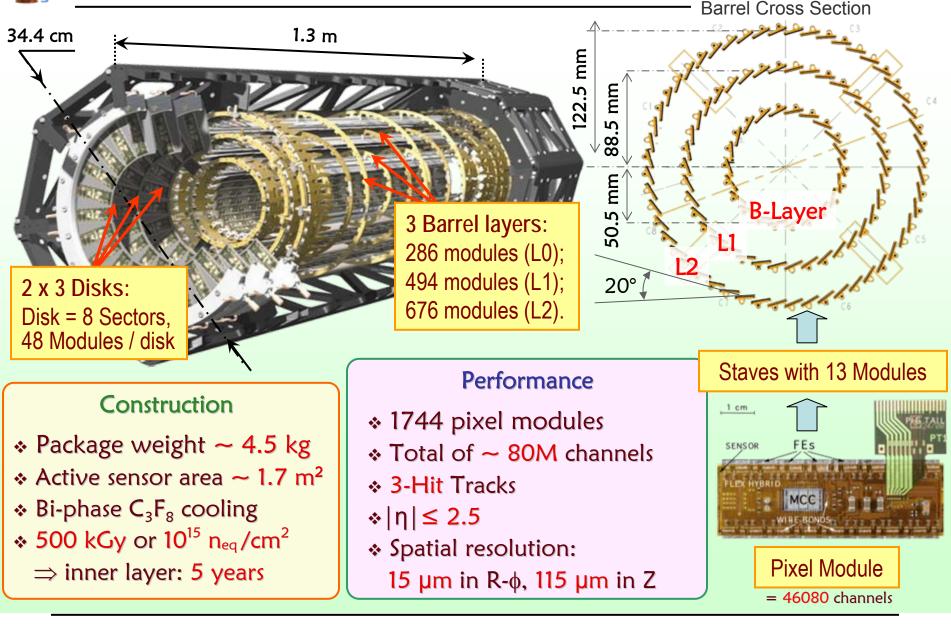






The Pixel Detector Overview

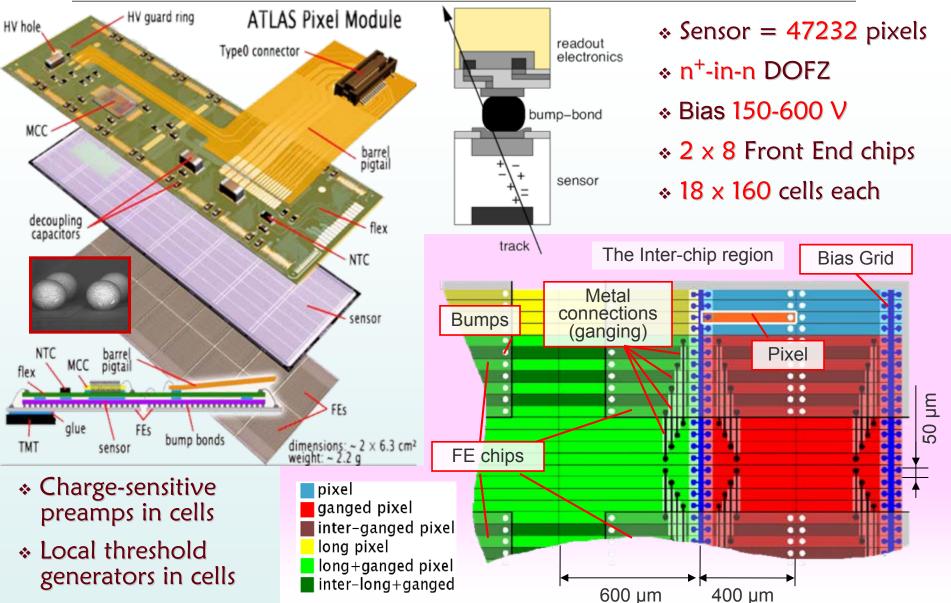






The Pixel Module









2007

Insertion into the ATLAS Inner Detector ***** June 22-27 ★ Pixel detector is disconnected, with no access to it

2008

February - April	Initial connection, connectivity tests
May - August	and pixel detector sign-off Environmental systems commissioning, Final integration in ATLAS DCS
July	Beampipe bakeout: successful, on-time Pixels are on the beampipe, cooled!
\checkmark	•••
August	First pass calibration (communication links)
End of August	Detector is cooled, modules are powered
September 14 th	Combined cosmic data taking: continuous

l, modules are powered Combined cosmic data taking: continuous calibration adjustments, modules recovery

LHC accident in sectors 3-4

Cosmic running becomes the top priority

to November

September 19th



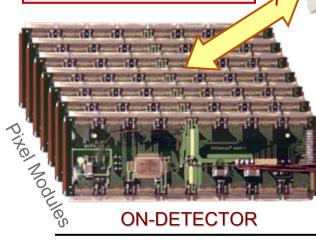


1. Opto-Link Tuning

Goal: Reliable communication between the modules and off-detector Back-Of-Crate readout cards via optical links.

- Find optimal conditions for modules
- There are 6 or 7 modules on each 288 boards, 40/80/160 mb/s $link \Rightarrow$ not an easy task!

Data readout: Data-push Receiving: Cell control logic signals: Thresh, ToT, test charge...



OFF-DETECTOR READOUT



-100m long optical cables 2. Module Tuning

annu.m

Opto-boards Motivation: Initial homogenous detector response; account for degradation due to irradiation in the future.

Threshold – level to tell signal from noise

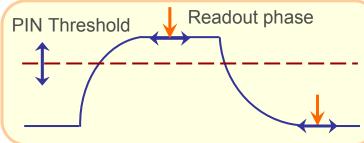
Time-Over-Threshold – ToT, indirect measurement of the deposited charge from the above-threshold signal amplitude

For every pixel cell

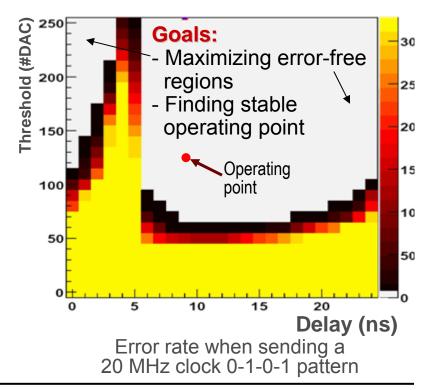




- The optical communication uplink is tuned by adjusting
 - The power of the on-detector lasers
 - Power of VCSEL lasers is temperature-dependent
 - One laser power setting per opto-board (6/7 channels)
 - The delay of the off-detector sampling clock
 - The PiN current threshold of the off-detector receiver diode



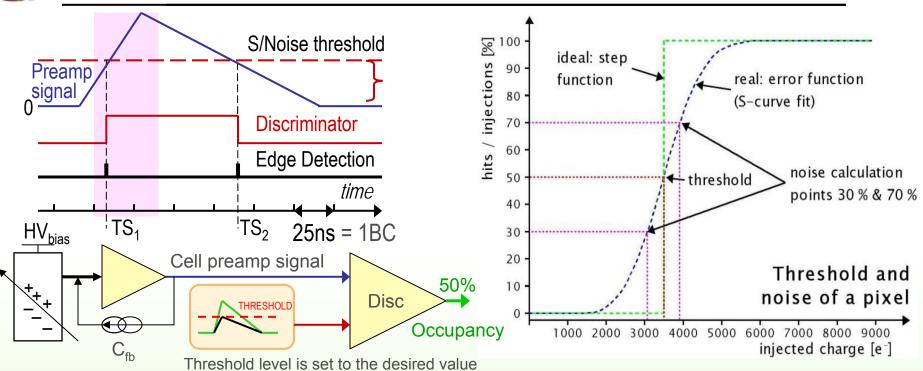
- Modules cannot be operated without good optical tuning
- Scanning 2-D parameter space
- Common stable operating point for 6/7 links has to be found
- Tuning takes ~15 min (all links)





Threshold Tuning





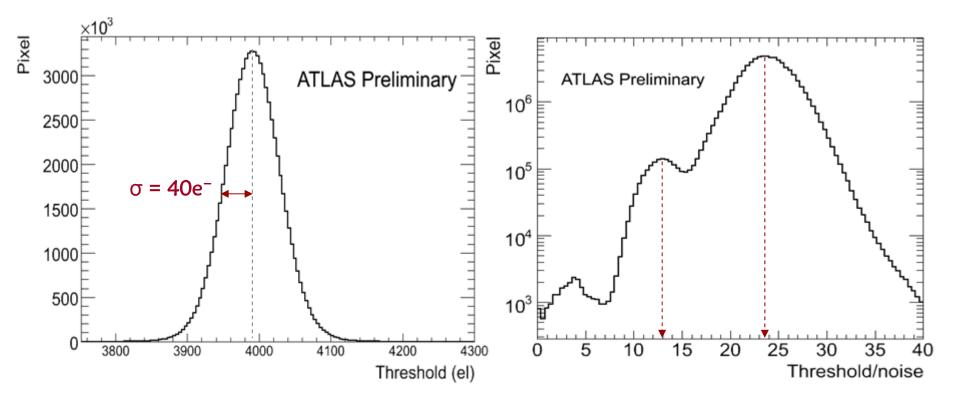
* Threshold is adjusted on pixel cell level

- Varying charge injections in the preamp of each pixel cell via integrated in-cell calibration circuitry;
- Fit error function to number of events vs. charge;
- **1.5 hrs** to tune the entire detector.
- * September cosmic runs \Rightarrow production tuning
- \diamond November runs \Rightarrow fully tuned configuration





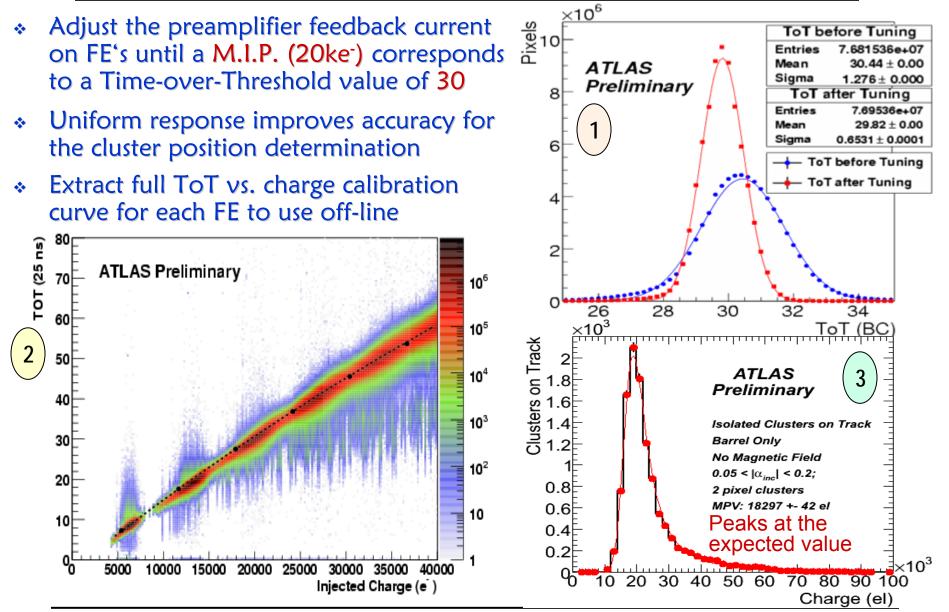
- * Thresholds are tuned to 4000 e⁻ in November
- - Threshold over noise is ~ 24 for most pixels
 - Threshold over noise is ~ 13 for few special inter-chip pixels





ToT (Time-over-Threshold) Tuning





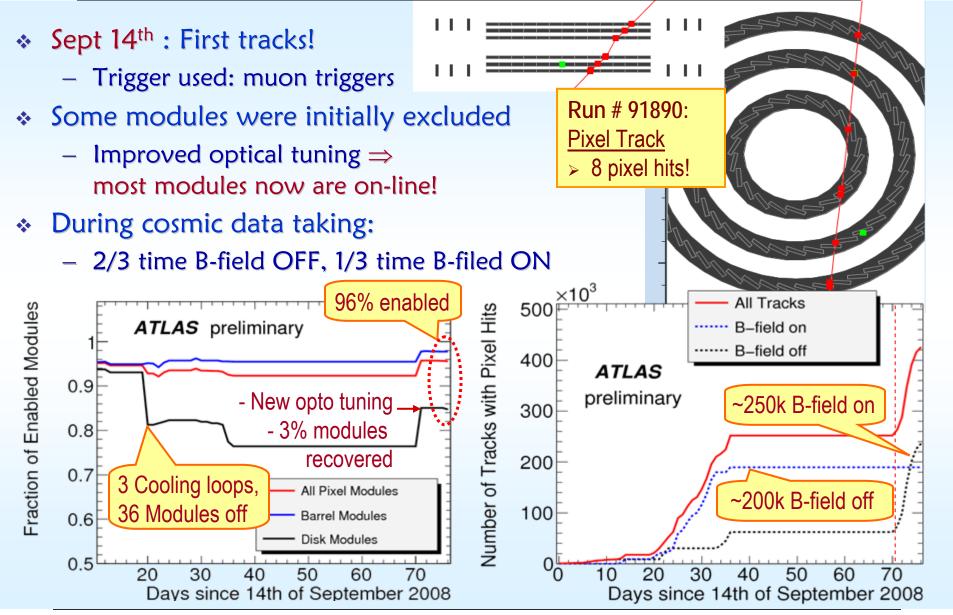
DPF'2009, July 28, 2009

Evgeny Galyaev, UT Dallas



Cosmic Data Taking





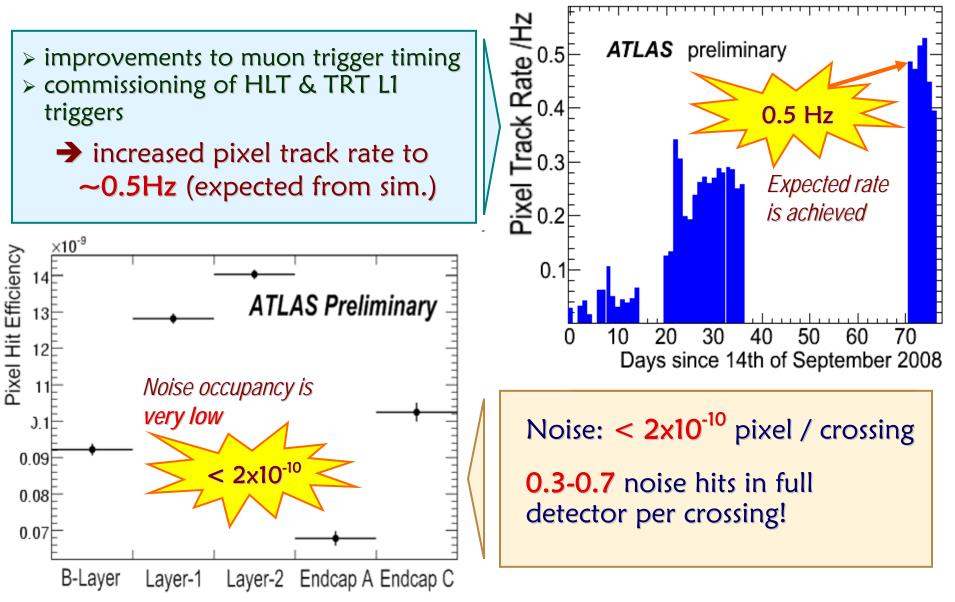
DPF'2009, July 28, 2009

Evgeny Galyaev, UT Dallas



Running With the Cosmic Data

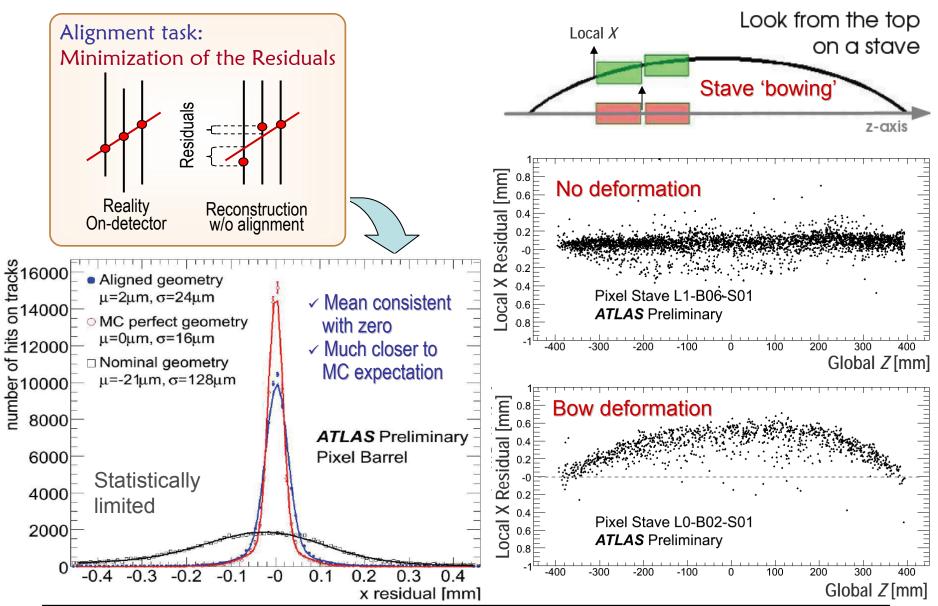






Detector Alignment with Cosmic Data





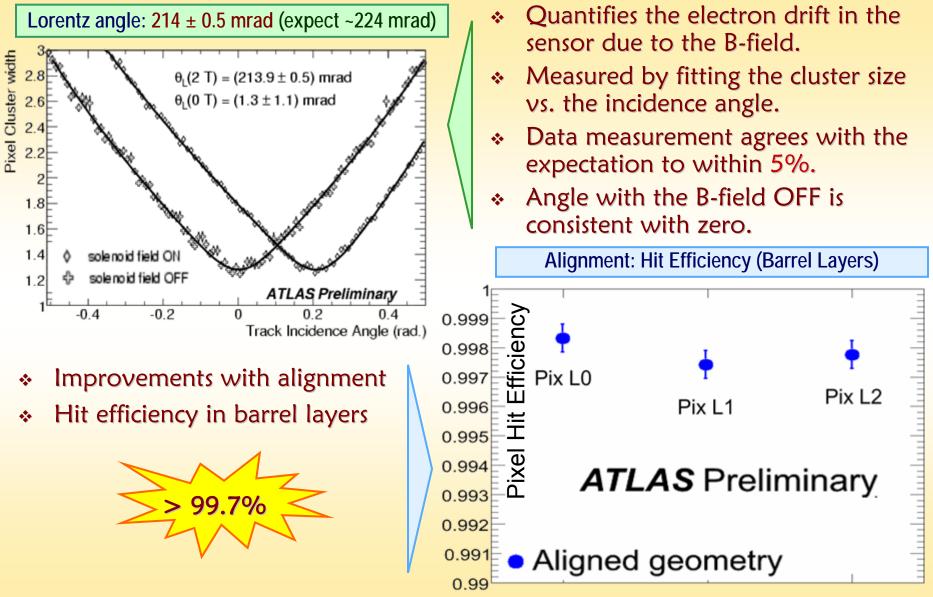
DPF'2009, July 28, 2009

Evgeny Galyaev, UT Dallas



Results from Cosmic Runs: Some of the Studies

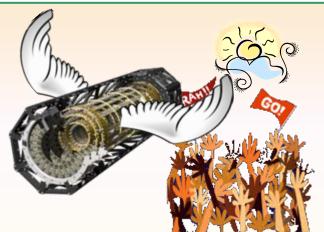








- Extremely tight commissioning schedule was successfully met.
- * 96% of all modules are included in data taking.
- * 2% were disabled due to problematic cooling loops (all in the disks):
 - \Rightarrow Year 2009: all cooling loops are operating;
 - \Rightarrow June 2009: Modules on these loops are qualified, tuned, and operate!
- Hit efficiency in the enabled modules (barrel layers) is above 99.7%.
- ♦ Noise occupancy is $< 10^{-10} \Rightarrow$ Well below one noise hit per event.
- * Resolution after recent alignment with available cosmics $\sim 24 \ \mu m$.
- Pixel detector is performing very well.



We are ready to take data in 2009!







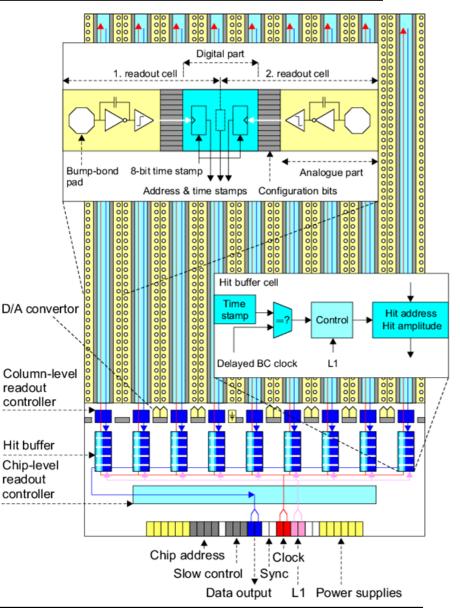
PIXEL DETECTOR

Back-up Slides Follow



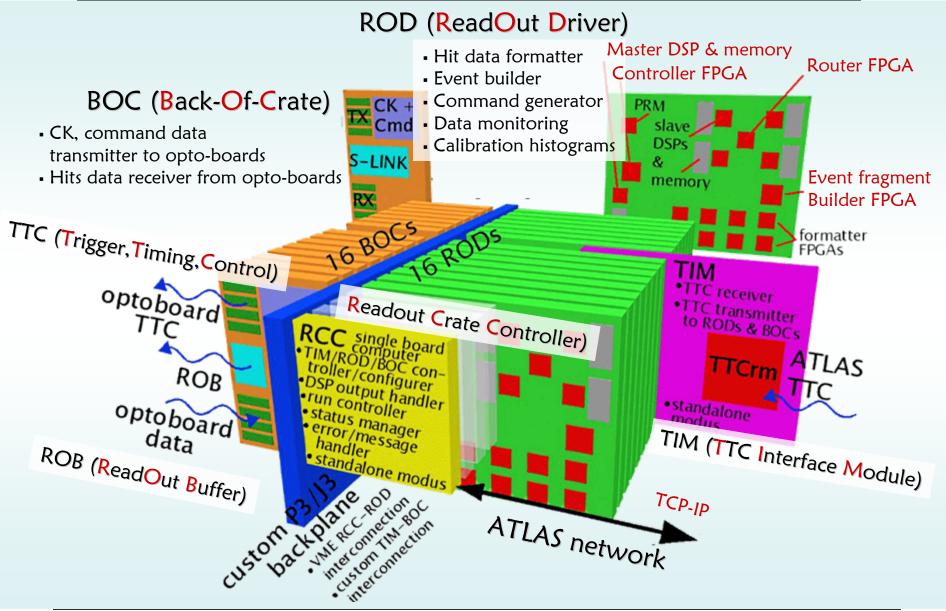


- One front end chip contains 2880 pixel cells organised in 18 columns with 160 rows
- Each pixel cell in the matrix contains preamplifier, discriminator and readout logic, which transfers hits to buffers at the bottom of the chip
- Peripheral region contains 64 hit buffers per double-column, logic for trigger coincidence and data serialisation
- Data is sent to the MCC, which builds the full event and sends it out



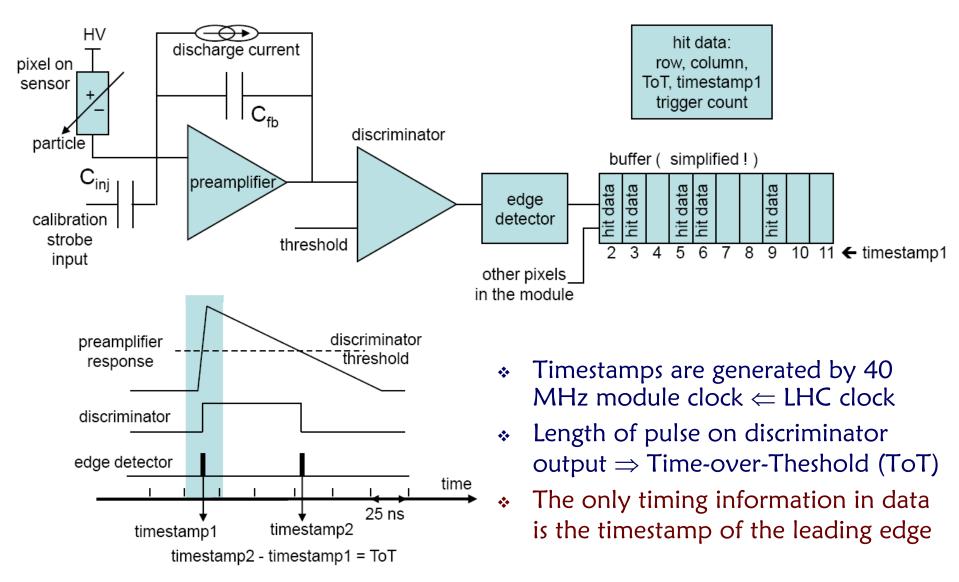
















- Noise mask is created off-line and applied on-line
- ♦ Noisy pixels are defined having $\geq 10^{-5}$ hits / event
- * ~ 5K pixels are masked \Rightarrow only 0.006% of all pixels

