

Wednesday 15 October

14:00 **Electronics and read-out (I)** Session | Location: IPN Lyon 14:00-14:30 Update of the SAM chip performances measured on a USB powered board Eric Delagnes / Dominique Breton 14:30-15:00 10ps Timing with the BLAB series ASICs Speaker **Gary Varner** 15:00-15:30 First results from the DRS4 fast sampling chip Speaker Stefan Ritt 15:30-16:00 First results from the CERN TDC chip Speaker Christian Mester 16:00