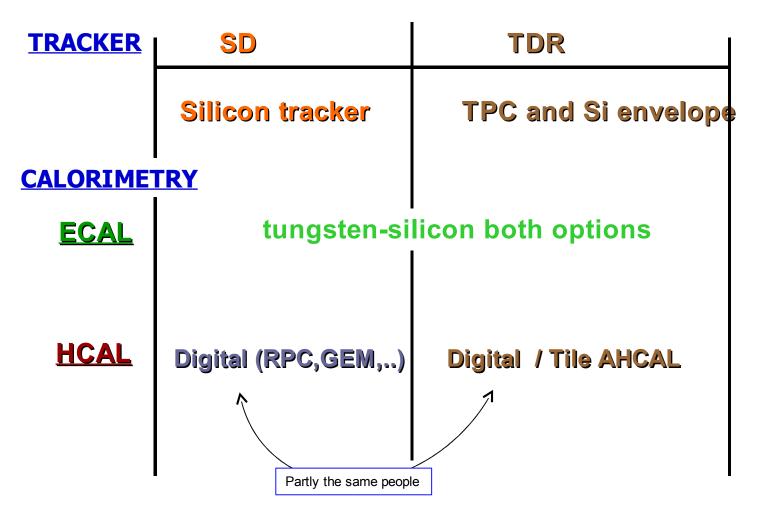
Two detector options today SD vs TDR



The 2 options following J.Jaros

Silicon area TDR ~ 2.6 Silicon area SiD

The only(main) justification

for the **SD** detector ??!!

JLC TESLA SD LD TPC TPC Silicon Jet-cell drift Tracker type **ECAL** R_{min} barrel (m) 1.68 1.27 2.00 1.60 Si pad/W Type Si pad/W scint. tile/Pb scint. tile/Pb $\frac{2^{\circ}}{2}$ < $0.71X_0$ $30 \times 0.4 X_0$ $30 \times 0.71 X_0$ $40 \times 0.71 X_0$ Sampling $+10 \times 1.2 X_0$ Gaps (active) (mm) 2.5 (0.5 Si) 2.5 (0.3 Si) 1 (scint.) 1 (scint.) Long. readouts 30 40 10 144 Trans. seg. (cm) ≈ 1 0.5 5.2 Channels (×10²) 50000 135 Z_{min} endcap (m) 2.8 1.7 3.0 1.9 **HCAL** 1.91 1.43 2.50 2.0 R_{\min} (m) barrel T: scint. tile/S.Steel digital scint, tile/Pb scint. tile/Pb Type D: digital/S.Steel Sampling $38 \times 0.12\lambda$ (B), $120 \times 0.047\lambda$ $1^{\alpha\alpha} \times 0.047\lambda$ $34 \times 0.12\lambda$ $53 \times 0.12\lambda$ (EC) T: 6.5 (5 scint.) Gaps (active) (mm) 1 (TBD) 2 (scint.) 2 (scint.) D: 6.5 (TBD) Longitudinal T: 9(B), 12(EC) 34 3 D: 38(B), 53(EC) readouts T: 5-25 Transverse 19 14 segmentation (cm) D: 1 80 θ_{\min} endcap 50 2° 20 Coil R_{\min} (m) 3.0 2.5 3.7 3.7 B(T)5 3 3 scint. strip (1 cm) option: Si pad shower max. det. shower max. det. (2 layers)



Radius, length, size, ...

Tracker size

Reason for 2.50m for the TPC length

Covering at low angle? but the FTD is doing it with FCH
Reason for the TPC radius of 1.60m
Single track resolution?
Separability?

- ECAL size

 Reason for 1.70m for the ECAL radius

 TPC radius + 10cm

 Compact ECAL to save space for HCAL inside coil

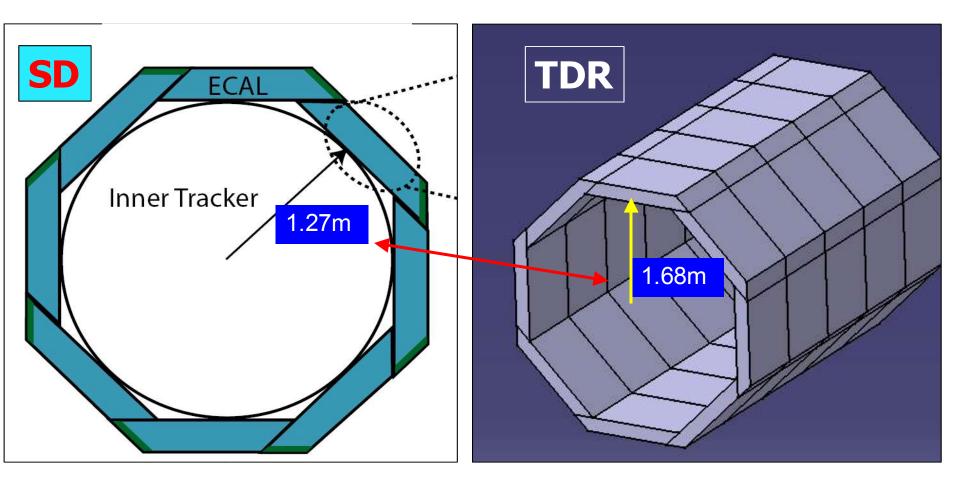
Reducing the external radius of the TPC (reduce the cost of the overall detector)

- Impact on the momentum resolution?
- if needed a precise point outside TPC can be added ??
- what about the charged-neutral separation ??

Geometry of the calorimeter

ECAL-SiD- ALCPG

ECAL-TDR- CALICE

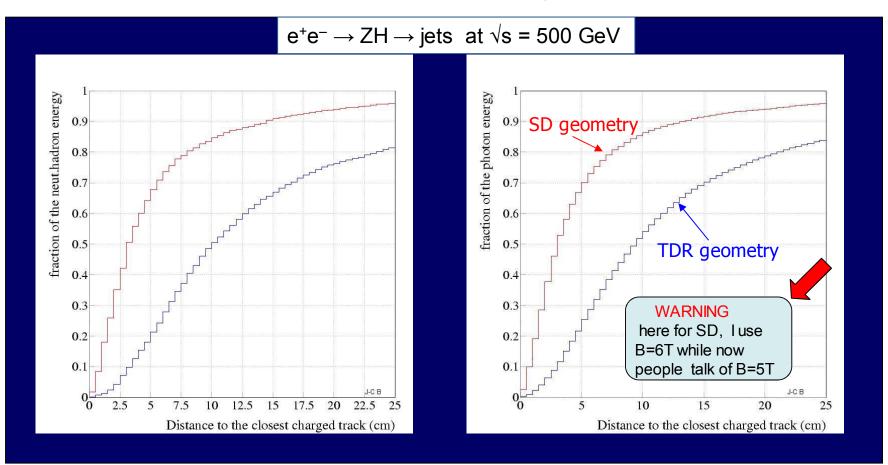


Is it so different?

At least, there is a good agreement on the global geometry

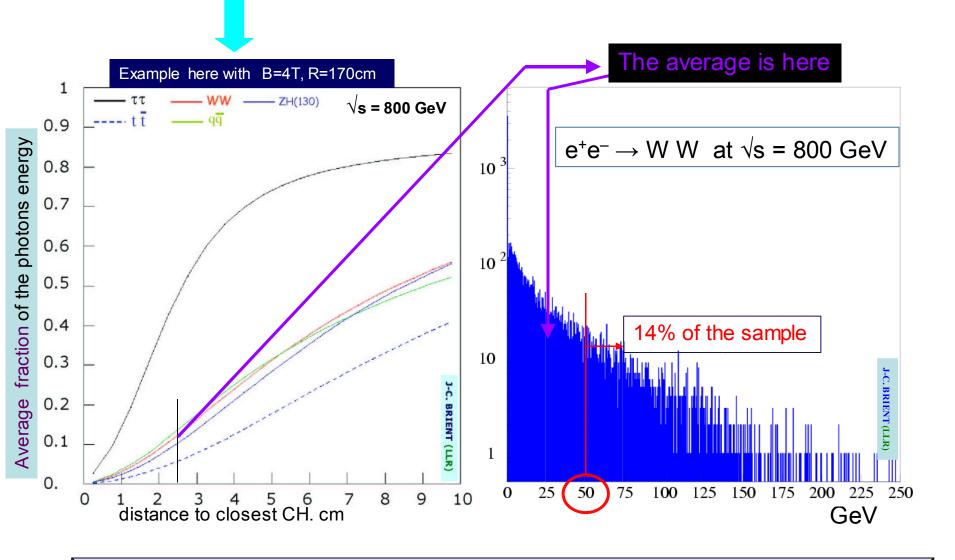
The ECAL internal radius

impact on EFL Presentation JCB at LBL 2000 - ALC meeting



For SD geometry, there is an average of ~65GeV of photons closer than 2.5 cm versus ~20 GeV for the TDR geometry



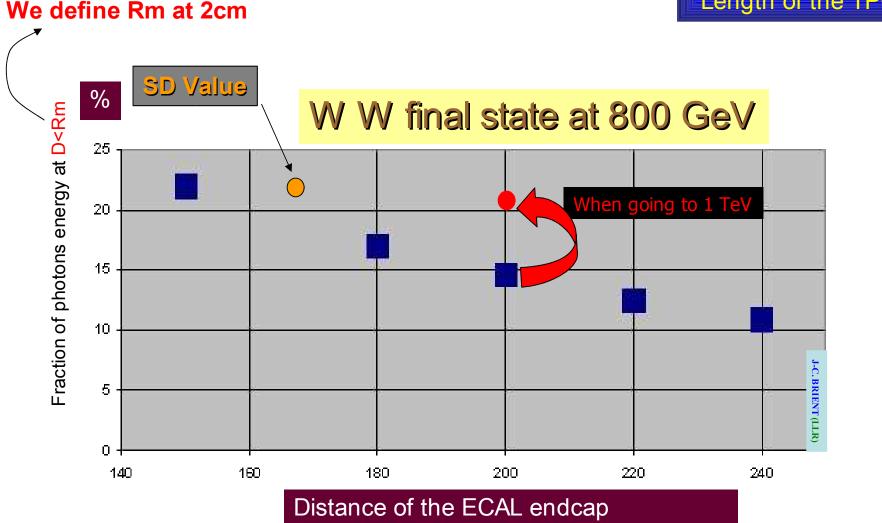


Efficiency of reconstructing photons close to ch. track (D<Rm) is <<100%

Variation with the ECAL endcap entrance

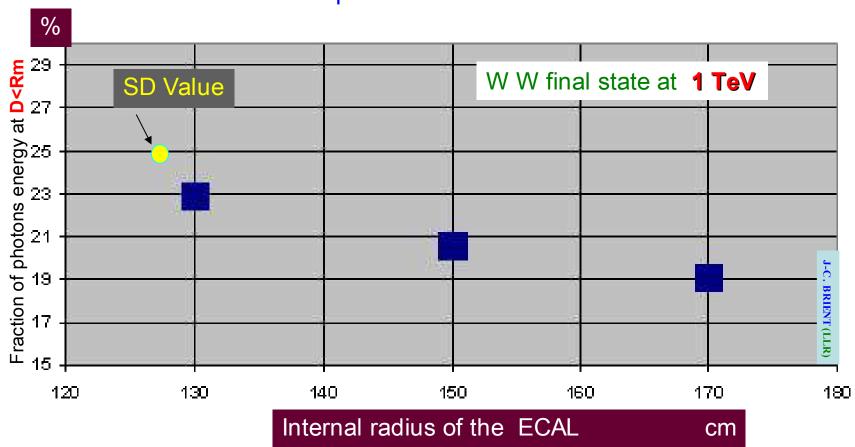
Internal radius fixed at 1.50 m and B=4T





Variation with the internal ECAL radius

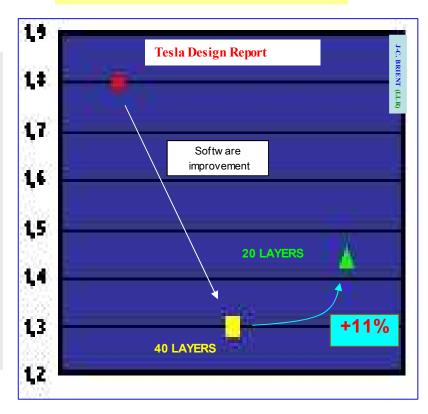




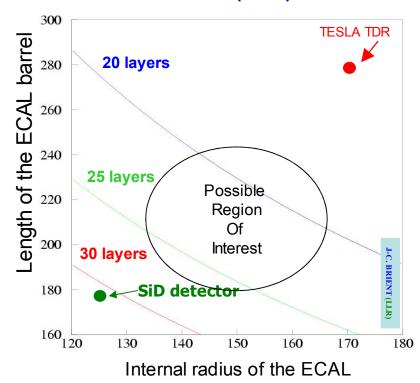
SD Values Rint=125, Zec=170 and B=5T

Is it possible reducing the calor. cost ? **AND** saving the EFLOW performances

ECFA Krakow Sept. 2001



Curves ISOCOST(area) versus SiD



GeV

ΔE(? component in jet)

WW at 800 GeV

For the TDR type of detector (R=170cm and 4T)

14% of the events have more than 50 GeV in the difficult region

For the SiD detector

(R=125cm and 5T)

32% of the events have more than 50 GeV in the difficult region

Due to the large value of the WW cross section, Any signal in jets could be overflowed?!

For the photon(s) reconstruction, the ECAL radius and Z endcap is much more important !!!

Impact on the jets to be quantified?

To reduce the ECAL cost,

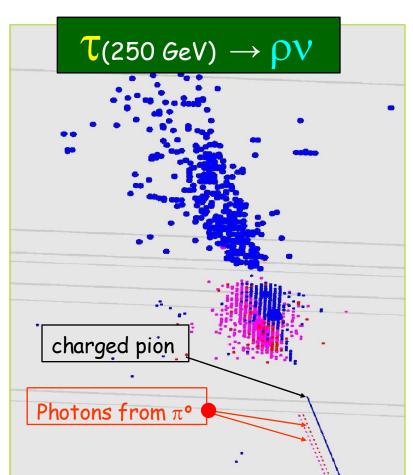
Playing with layers number is more efficient and less penalizing for the performances on jet, τ ,...?!

A new detector proposal

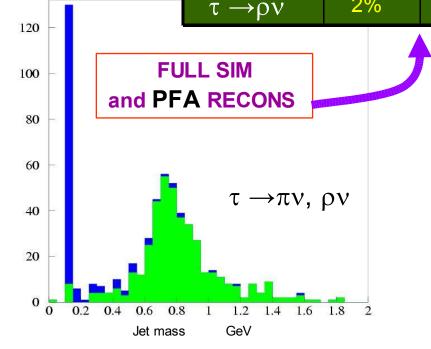
~ 20-25 layers ECAL at R≈1.55m ?? Z_{ECAL} ??

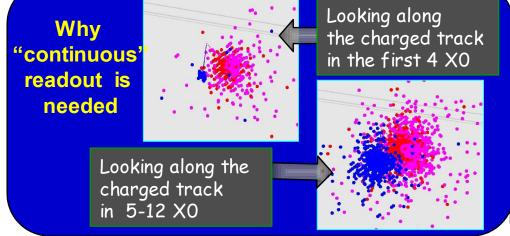
JER INDORIANI

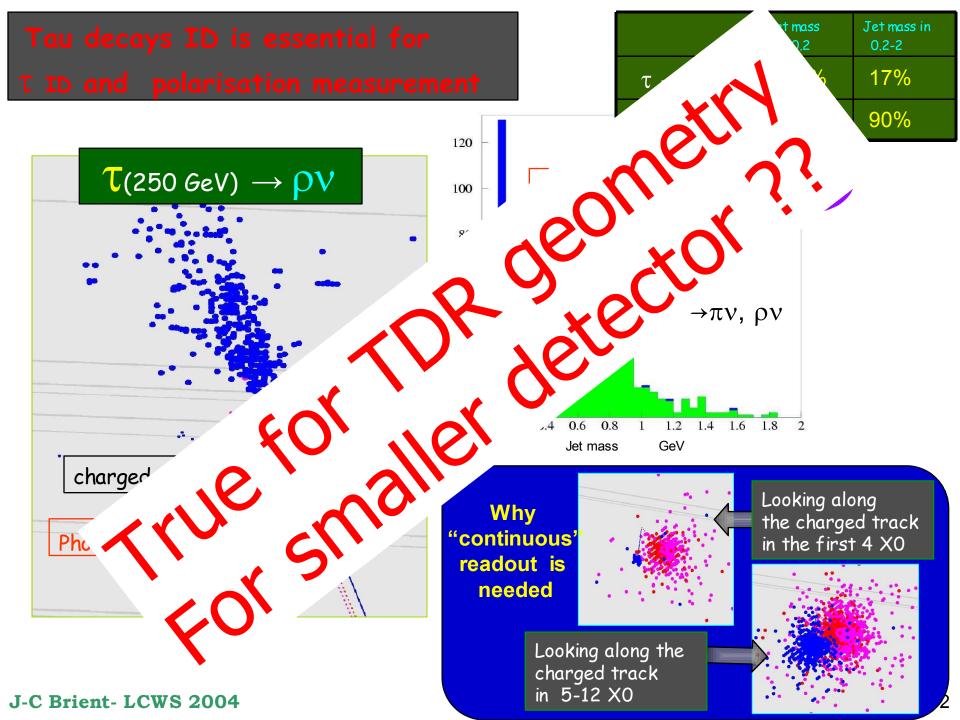
Tau decays ID is essential for



	Jet mass < 0.2	Jet mass in 0.2-2
$\tau \rightarrow \pi \nu$	82%	17%
τ →ρν	2%	90%





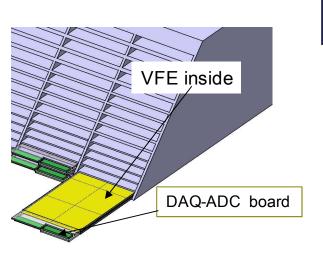


Summary of the ECAL change vs TDR

- ► VFE inside for the ECAL, alveoli thinner, better eff. Molière radius
- ► For the simulation, I propose to use 30 layers to be consistent with the SiD ECAL and with the prototype in construction

Changing the general geometry

- ► VFE inside for the HCAL (Si-PM, or digital readout for DHCAL)
 - → NO SPACE for fibbers in overlap !!!
 - → NEW distance TPC-ECAL in endcap !!!!



New way of the ECAL readout

VFE (with ADC?) send each BX to DAQ board (with/without ADC) DAQ-ADC board digitise, store in digital memory, MUX to optical link



- VFE time occupancy is about 1/200 for TESLA
- VFE On-Off take about 100 μs
- ⇒ Simulation gives ~100µW/channel !!! (source CdlT)



Passive cooling would be sufficient (source JB)

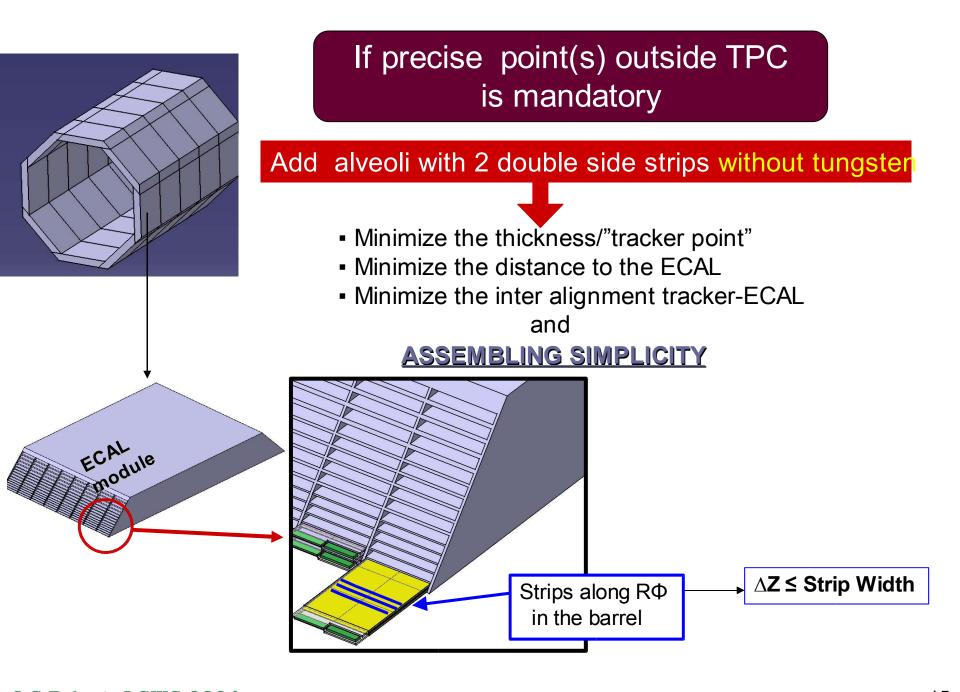
R&D in CALICE ECAL (IN2P3,KNU,MSU) to quantify this passive cooling limit

Modify Simulation (better R_m^{eff})

Other open questions

- Quantitative variation of performances on jet(s)
 (and impact on physics program) with TPC size
- ▶ Is there a way to avoid the hole between Forward CAL and ECAL together with the possibility to open the detector?
- A dedicated study of the CALOR. endcap geometry

- ▶ Using ECAL to seed the high Pt track in the SiD tracker ? a kind of substitute for the large number of points in a TPC
- ► FCH (SET?) in silicon device inserted in ECAL CFi frame ? See next slide
- ► What is the **number of X0** of the endplate and readout electronics ? what is the distance TPC-ECAL ?



A lot of questions, Just few answers/guess

I propose you my preliminary **personal conclusions**

- For CALOR. geometry , the TDR detector is not so different from the SD detector, but the size
- The PFLOW is very probably more difficult with the SD detector (to be quantified)
- The impact on the performances from different TPC size, with/without precise points, etc... has to be QUANTIFY

May be it is time to begin the second round of detector optimisation

- ▲ Inter-regional proposal would be VERY WELCOME !!
- a proposal at the next LCWS?