



# 10th Workshop on Electronics for LHC and future Experiments, Boston USA

## Tuesday, 14 September 2004

### Parallel session A3 (16:10 - 18:00)

time	[id] title	presenter
16:10	[88] PACE3 : A large dynamic range analog memory ASIC assembly designed for the readout of silicon sensors in the LHC CMS Preshower.	PAUL ASPELL
16:35	[89] SEU robustness, total dose radiation hardness and analog performance of the Beetle chip	ULRICH TRUNK
17:00	[90] Production testing and quality assurance of CMS silicon microstrip tracker readout chip	PIERRE BARILLON
17:25	[91] Cancelled	ADRIANO LAI
	- [1] (The DIALOG and SYNC chip in the front-end electronics of the LHCb muon detector. )	