

**Session Program**

**13-17 Sept 2004**

**10th Workshop on Electronics for LHC and  
future Experiments, Boston USA**

***Plenary P6***

Boston Marriott Hotel, Boston USA

# Thursday 16 September

14:00

## Plenary P6

Session | Location: Boston Marriott Hotel, Boston USA

14:00-15:00

**Fundamental discontinuities in silicon technology; Examples, consequences, and outlook for the future**

**Speaker**

Bernard S Meyerson

15:30

15:00-15:30

**Coffee and poster session**