

Session Program

13-17 Sept 2004

**10th Workshop on Electronics for LHC and
future Experiments, Boston USA**

Parallel session B6

Boston Marriott Hotel, Boston USA

Wednesday 15 September

16:10

Parallel session B6

Session | **Location:** Boston Marriott Hotel, Boston USA

16:10-16:35

Novel bridges for PC-based VME control

Speaker

Nicola Paoli

16:35-17:00

The LHC PC Rack Project

Speaker

Fred Wickens

17:00-17:25

The Final prototype of the Fast Merging Module (FMM) for readout status processing in CMS DAQ

Speaker

Attila Racz

17:25-17:50

TTC Interface Module for ATLAS Read Out Electronics : Final production version based on Xilinx FPGA devices

Speaker

Matt Warren

17:50