

LLRF05**Tuesday, 11 October 2005****Working Group 4: Platforms (L. Doolittle) - Bldg. 40, Room 40-S2-C01 (14:00 - 14:45)****-Conveners: Brian Chase****Working Group 4: ADC/DAC (C. Hovater) - 40-S2-C01 (14:45 - 15:30)****-Conveners: Brian Chase**

time	[id] title	presenter
14:45	[39] Adaptive Resonance Control for LANSCE-R	Mr KWON, Sungil

Working Group 4: DSP topics (B. Chase) - Bldg. 40, Room 40-S2-C01 (16:00 - 16:45)**-Conveners: Brian Chase**

time	[id] title	presenter
16:07	[50] Software development for FPGA based cavity controller and simulator SIMCON	Mr SZEWINSKI, Jaroslaw Mr PUCYK, Piotr

Working Group 4: Summary - 40-S2-C01 (16:45 - 17:30)**-Conveners: Brian Chase**

Wednesday, 12 October 2005

Working Group 4: Discussion - 40-S2-C01 (14:00 - 14:30)

-Conveners: Brian Chase

Working Group 4: Receiver Technology (F. Pedersen) - Bldg. 40, Room 40-S2-C01 (14:30 - 15:00)

-Conveners: Brian Chase

time	[id] title	presenter
14:30	[84] Femtosecond optical synchronization systems for XFELs	Mr WINTER, Axel
14:37	[85] SNS LLRF Reference System	Mr PILLER, Maurice

Working Group 4: Master Oscillator / Distribution (F. Pedersen) - 40-S2-C01 (15:00 - 15:30)

Working Group 4: Control of ferrite shifters (Y. Kang) - Bldg. 40, Room 40-S2-C01 (16:00 - 16:45)

-Conveners: Brian Chase

time	[id] title	presenter
16:00	[42] Vector Modulation of High RF Power using Ferrite Phase Shifters	Dr KANG, Yoon

Working Group 4: System Software / Verification (P. Joireman) - 40-S2-C01 (16:45 - 17:30)

-Conveners: Brian Chase