Session Program

10-13 Oct 2005

LLRF05

Working Group 3

CERN

Tuesday 11 October

14:00

Working Group 3: Session 1a: RF Modelling
Session | **Location:** CERN, Bldg. 40, Room 40-4-C01 | **Convener:** Dr Stefan Simrock

14:00–14:07 **Superconductive cavity driving with FPGA controller**

Speaker
Mr Tomasz Czarski

14:07–14:14 **LANSCE-R Low Level RF Control System**

Speaker
Mark Prokop

14:14–14:21

VHDL programming and software for LLRF control system SIMCON and results from tests

Speaker
Mr waldemar koprek

14:21–14:28 **Dynamic Modeling and Simulation of LER-HER PEP II Rings**

Speaker
Dr Claudio Rivetta

14:49–14:56 **Digital Low Level RF Control System for the DESY TTF VUV-FEL Linac**

Speakers
Dr Stefan Simrock, Dr Valeri Ayvazyan

15:30

16:00

Working Group 3: Session 1b: RF Software concepts (Control Systems, Architecture of LLRF, Subsystems, etc.)
Session | **Location:** CERN, Bldg. 40, Room 40-4-C01 | **Convener:** Dr Stefan Simrock

16:00–16:20 **Measurement Receiver for LLRF Control System**

Speaker
Mr Tomasz Filipek

17:30

Wednesday 12 October

14:00

Working Group 3: Session 2a: RF Software Feedback and Applications
Session | **Location:** CERN, Bldg. 40, Room 40-4-C01 | **Convener:** Dr Stefan Simrock

14:00–14:20

Low-Level RF Station Operation for the VUV-FEL at DESY and other Linear Accelerators

Speaker
Mr Wojciech Cichalewski

14:20–14:40 **Internal Interface**

Speaker
Mr Wojciech Jalmuzna

15:30

16:00

Working Group 3: Session 2b: RF Control Operation (Automation, Procedures, Reliability)
Session | **Location:** CERN, Bldg. 40, Room 40-4-C01 | **Convener:** Dr Stefan Simrock

17:30