

## New Single-sided 2d and (2+3)d Stripixel Detectors: Concept, Simulation, and Design\*

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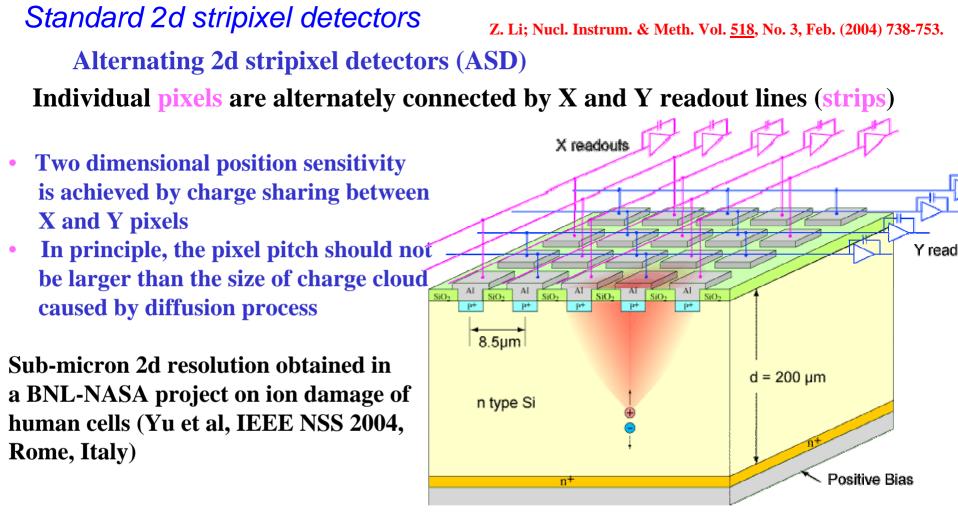
June 2005

6<sup>th</sup> CERN RD50 Workshop, Helsinki, Finland

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# Outline

- Introduction
  - Standard 2d stripixel detectors
    Alternating stripixel detectors (ASD)
    Interleaved stripixel detectors (ISD)
    Standard 3d detectors
    Single-Type-Column 3d detectors (3DSTC)
- Novel 1-sided 2d and (2+3)d Stripixel Detector Concept
  - Planar single-sided 2d stripixel detectors
  - Planar+3d single-sided 2d stripixel detectors with single column
  - Planar+3d single-sided 2d stripixel detectors with dual columns
- Simulation and Design of 1<sup>st</sup> Prototype detectors
- Summary



- Can not be made in large pitches (  $\leq 20 \ \mu m$ )
- Charge sharing

Standard 2d stripixel detectors

**Interleaved stripixel detectors (ISD)** 

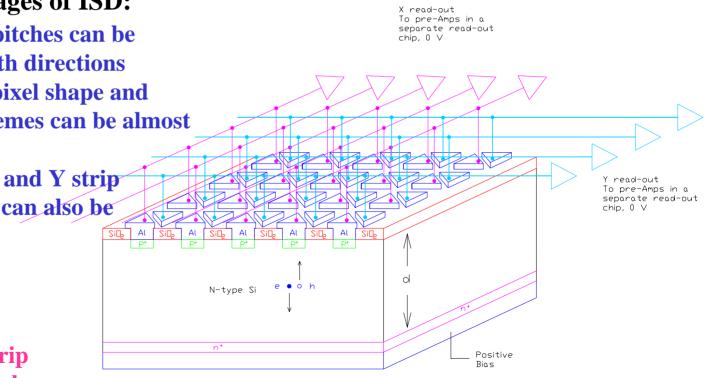
Each pixel is divided into two halves: X-cell and Y-cell, and connected by X and Y readout lines (strips) respectively X-cell and Y-cell are interleaved (coupled)

#### **Unique advantages of ISD:**

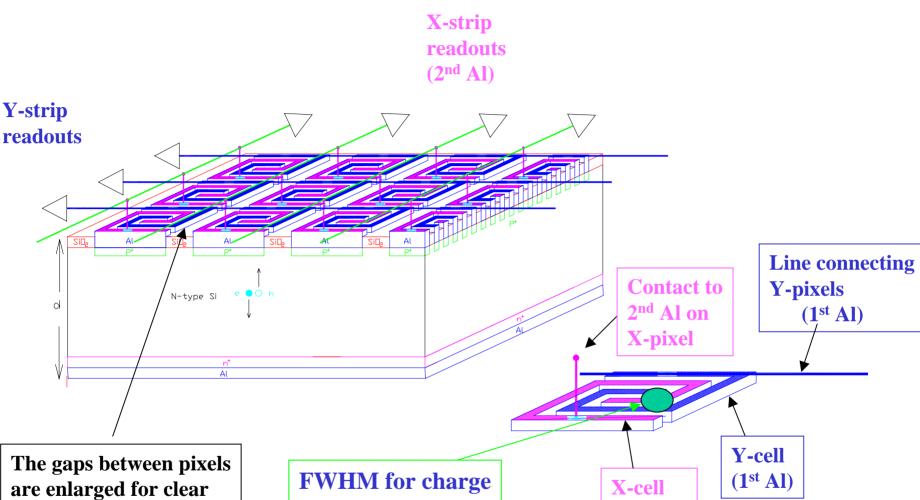
- pixels and strip pitches can be made large in both directions
- The choices for pixel shape and interleaving schemes can be almost infinity
- The choice for X and Y strip readout schemes can also be almost infinity

- large capacitance/strip due to interleaving scheme
- Charge sharing





#### Schematic of a spiral interleaving scheme for ISD (PHENIX Upgrade at RHIC)



Prototype for PHENIX Upgrade at RHIC produced, 2d resolution of 25 μm obtained (80 μm pitches) (Tojo et al., IEEE TNS Vol.51, No.5, pp 2337-2340)

diffusion

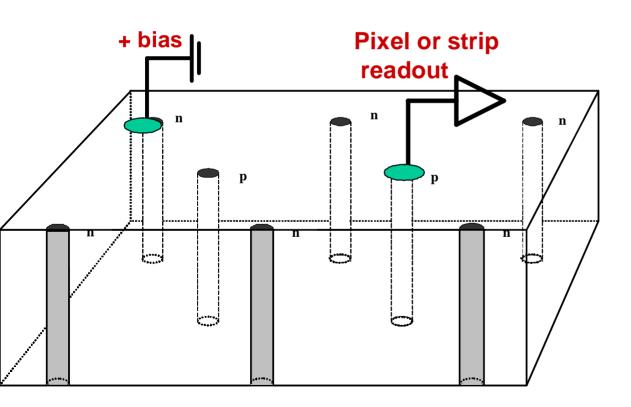
illustrations

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(1<sup>st</sup> Al)

Standard 3d Si detectors

### Schematics of a 3d detector (3d in terms of processing)



o 3d processing

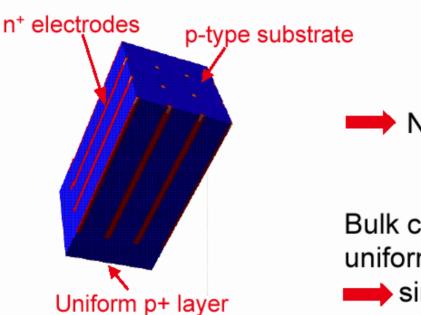
- o 2-column etched
- Read out only p or n electrodes

Sherwood I. Parker et al., UH 511-959-00

### **3DSTC detectors** - concept (2)



Further simplification: holes not etched all through the wafer

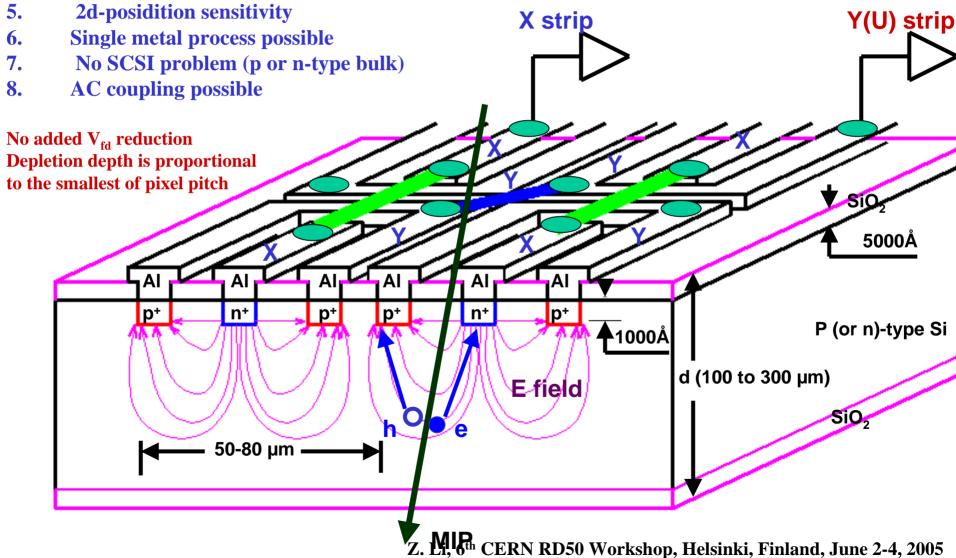


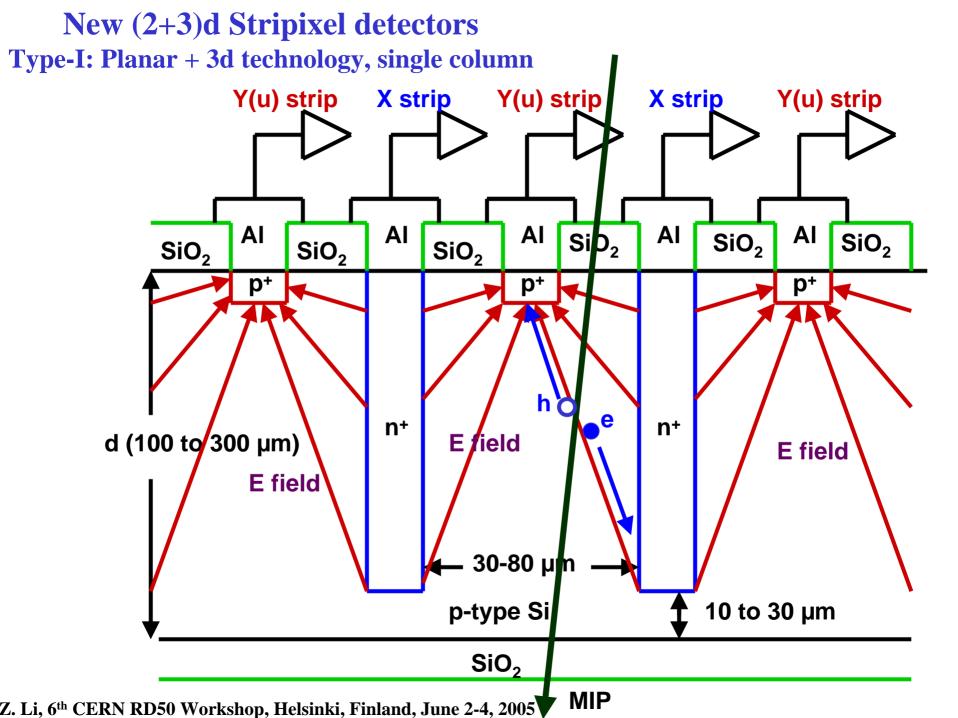
- o Planar and 3d processing
- o 1-sided process
- Read out only
  n (or p) electrodes
- No need of support wafer.

Bulk contact is provided by a backside uniform p+ implant single side process.

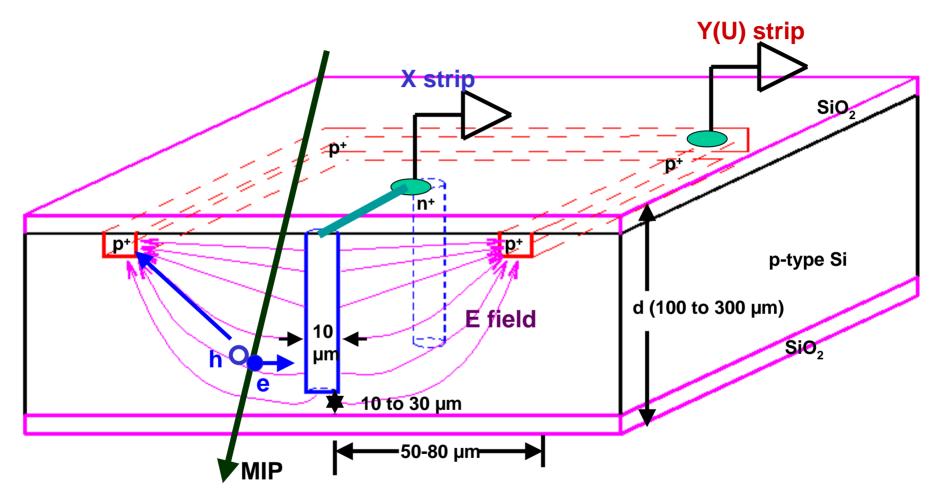
### New planar single-sided 2d stripixel detectors

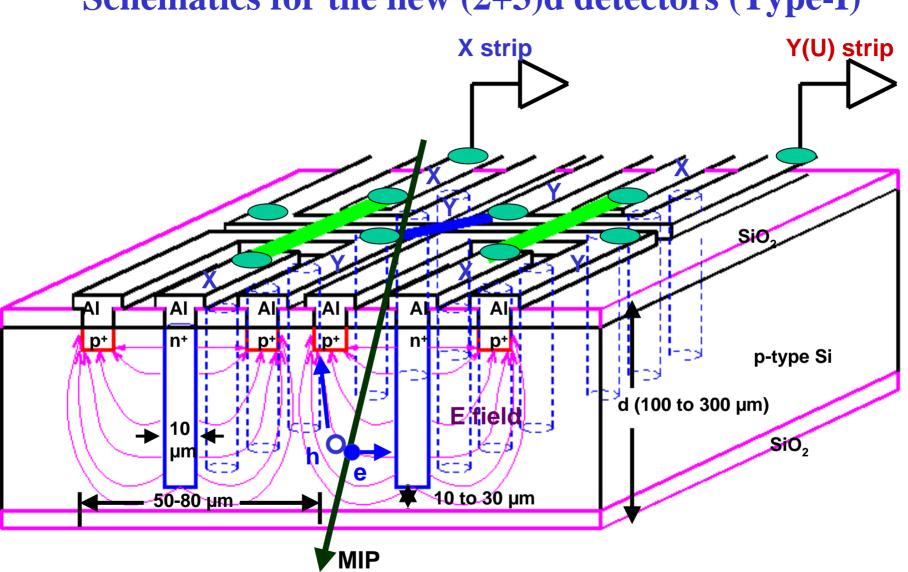
- 1. All planar technology
- 2. No charge sharing problem
- 3. No added capacitance
- 4. True one-sided process (no process on the back side at all)



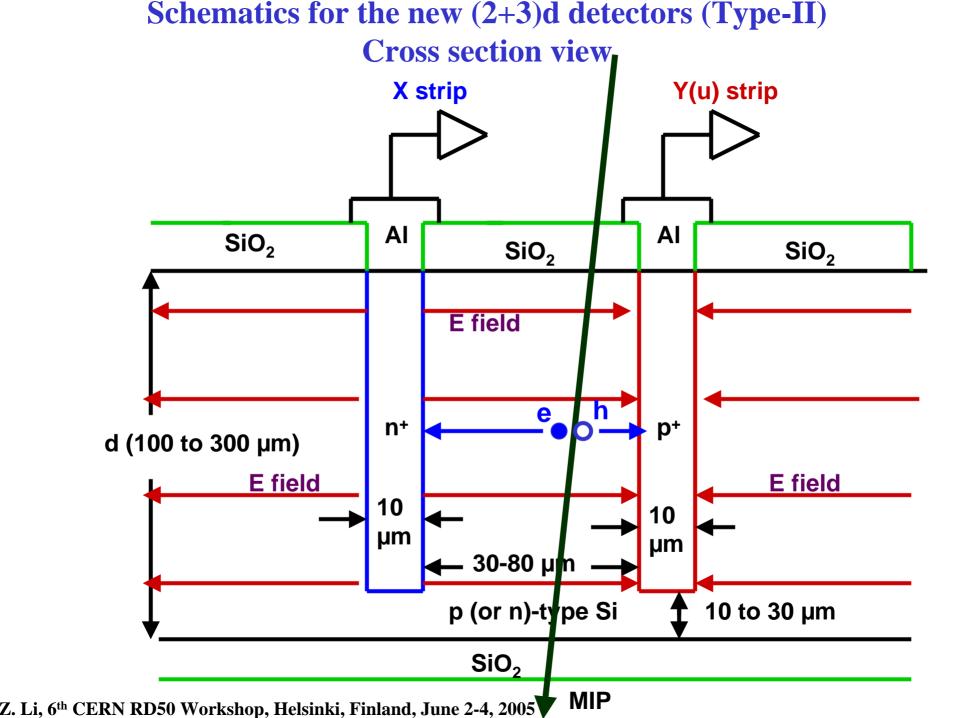


### Schematics for the new (2+3)d detectors (Type-I) Single cell

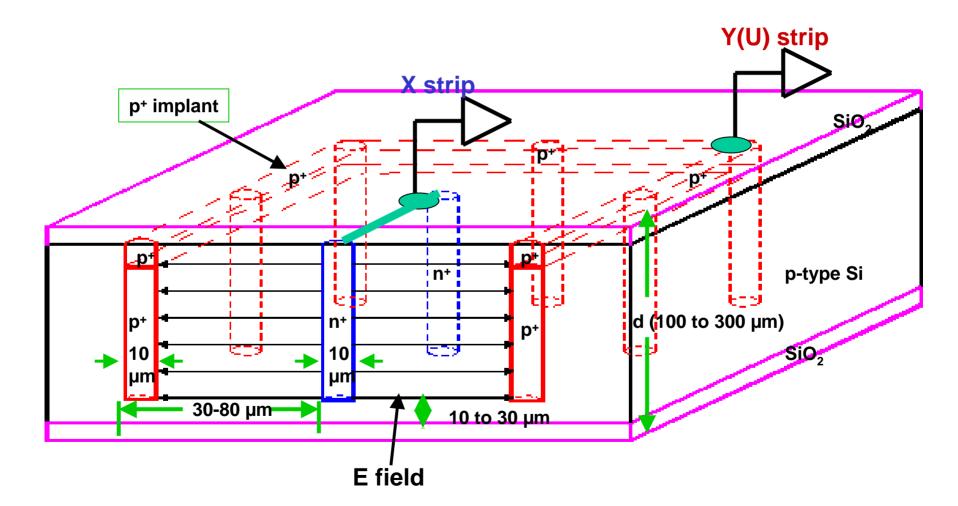




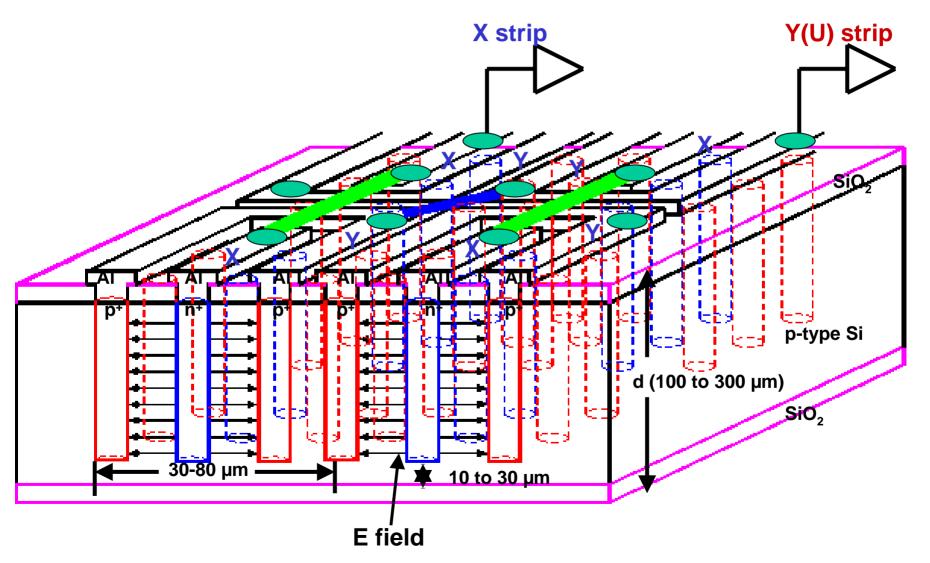
#### **Schematics for the new (2+3)d detectors (Type-I)**

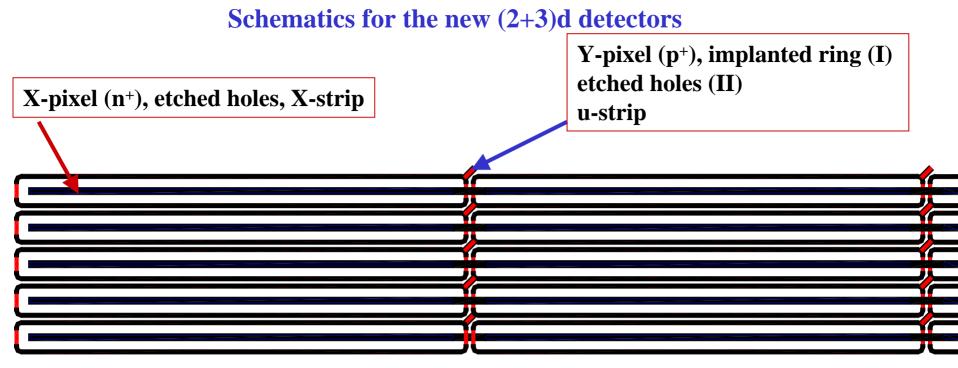


### Schematics for the new (2+3)d detectors (Type-II) Single cell



#### New (2+3)d Stripixel detectors Type-II: Planar + 3d technology, dual columns

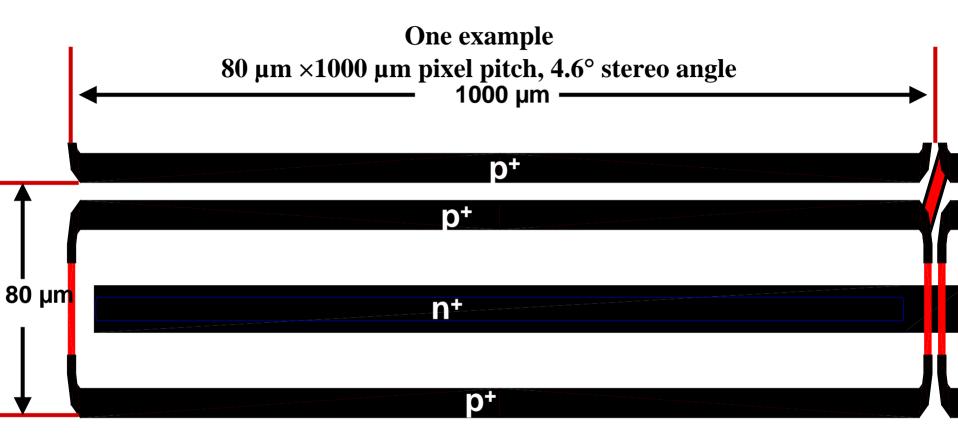




- 1. Partial planar technology ((2+3)d (I+II))
- 2. No charge sharing problem
- 3. No added capacitance
- 4. True one-sided process (no process on the back side at all)
- 5. **2d-posidition sensitivity**
- 6. Single metal process possible
- 7. AC coupling possible
- 8. No SCSI problem (p or n-type bulk)
- 9. As radiation hard as 3d detector ((2+3)d (II))

#### **Complicated 3d processing technology needed**

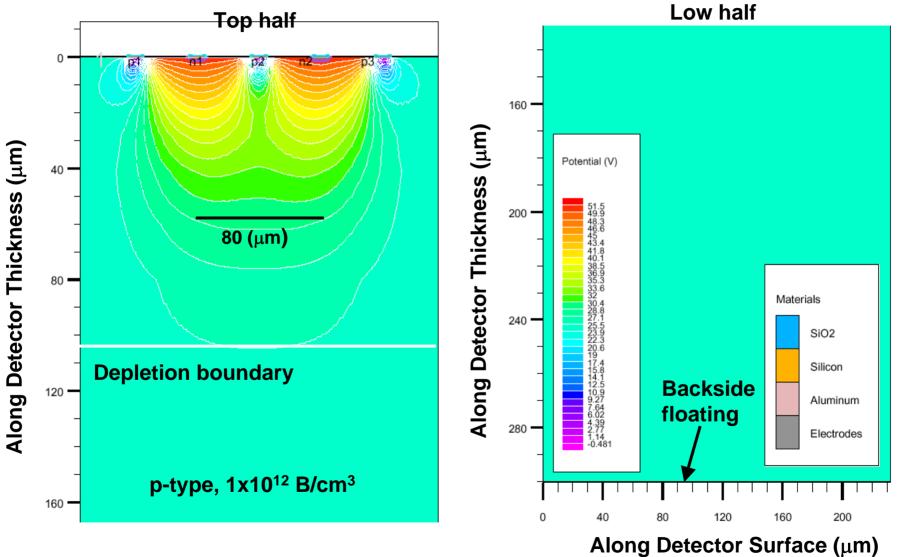
#### Schematics for the new (2+3)d (I and II) detectors



### **Simulation and Design of 1st Prototype detectors**

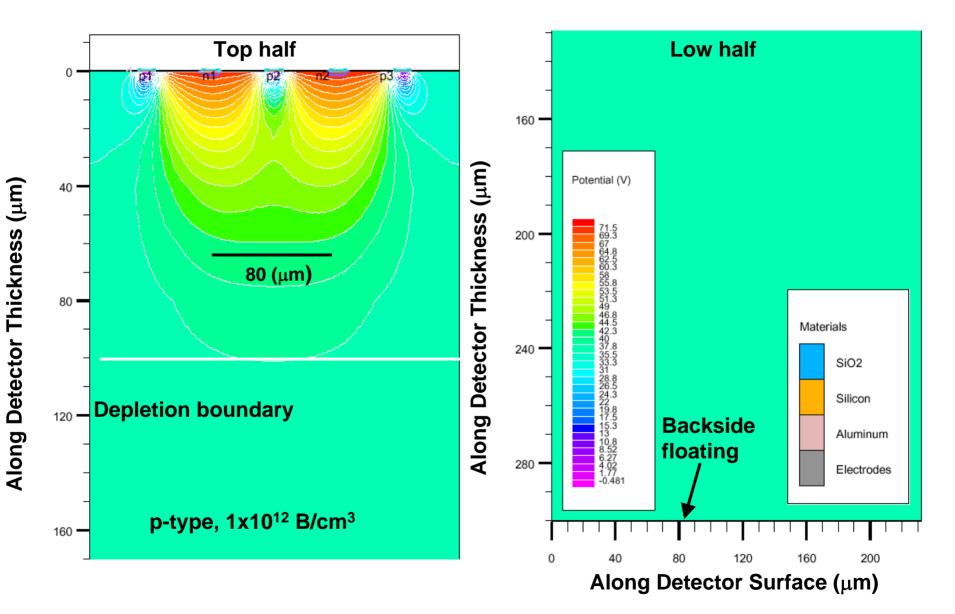
#### **2d potential profiles**

V = -50 V at p<sup>+</sup> electrodes, 0 V at n<sup>+</sup> electrodes



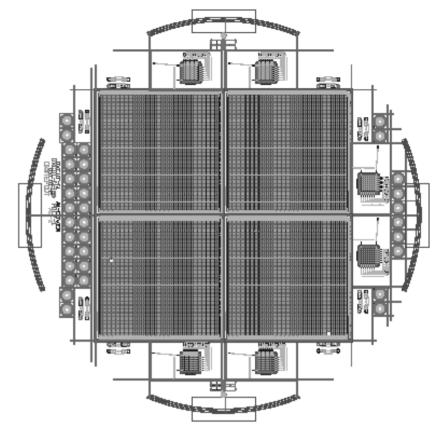
#### 2d potential profiles

#### V = -70 V at p<sup>+</sup> electrodes, 0 V at n<sup>+</sup> electrodes



### **Design of 1st Prototype**

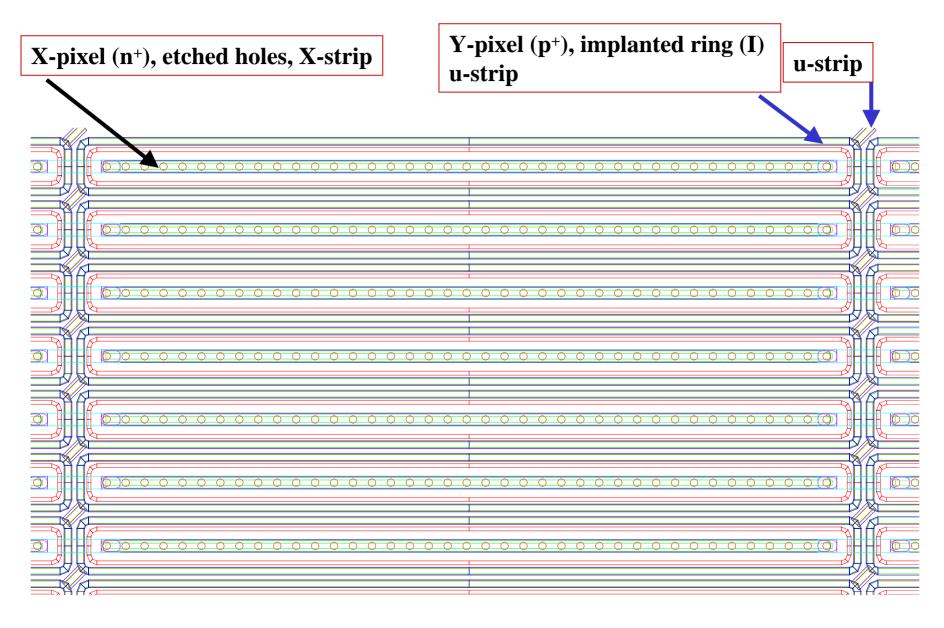
- 1. Design is just finished
- 2. Two 3 cm  $\times$  6 cm chip (4"-wafer)
- 3. 3 cm long strips
- 4. 80  $\mu$ m × 1000  $\mu$ m pixel size
- 5. 10  $\mu$ m n<sup>+</sup> hole sizes
- 6. 384 x and 384 u strips
- 7. **4.6°** stereo angle
- 8. If no n<sup>+</sup> holes etched just single-sided planar 2d detectors
- 9. If n<sup>+</sup> holes etched -- (2+3)d detectors



#### A collaboration program has been developed between BNL and CNM:

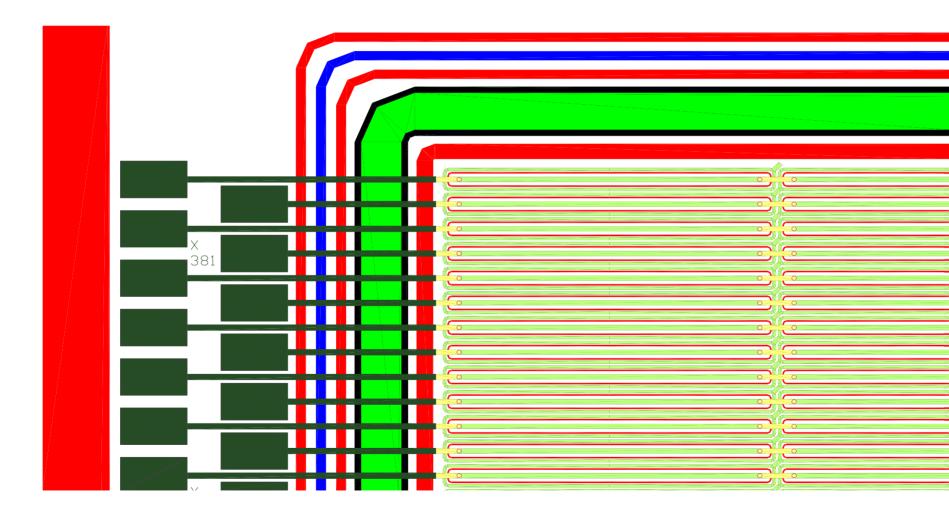
CNM will do the 3d processing (n<sup>+</sup> hole etching and doping) Giulio Pellegrini and Manuel Lozano BNL will do the remaining planar processing Mask set will be ordered in a week Prototype processing will start in one month Processing completion date projected as Fall, 2005 Z. Li, 6<sup>th</sup> CERN RD50 Workshop, Helsinki, Finland, June 2-4, 2005

### **Design of 1st Prototype detectors**



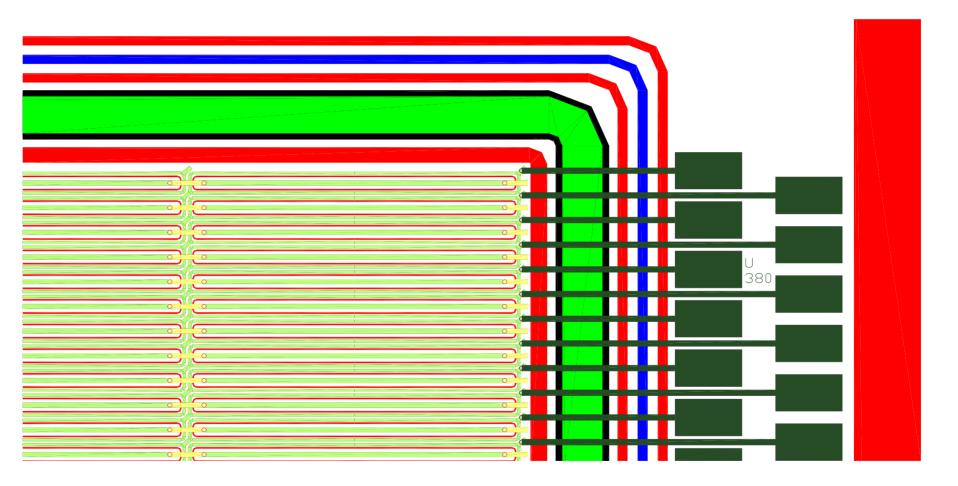
### **Design of 1st Prototype detectors**

**X-** strips



### **Design of 1st Prototype detectors**

u- strips



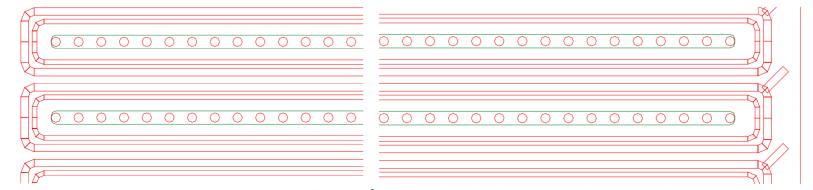
### **Processing steps**

**n**<sup>+</sup> hole etching

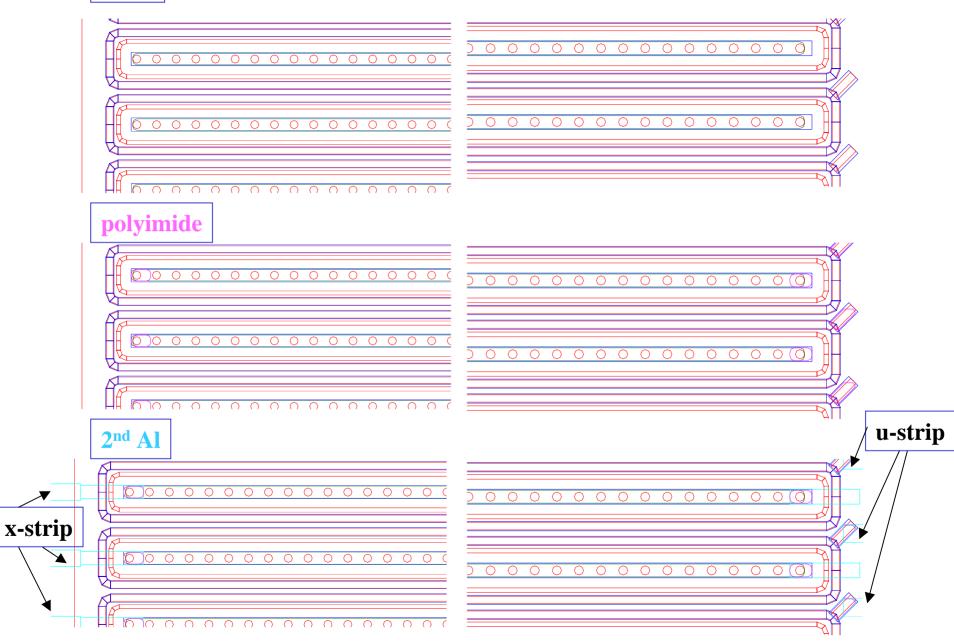
n<sup>+</sup> implant

 $\bigcirc$  $\bigcirc$ Ο 

p+ implant







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#### **SUMMARY**

#### • New single-sided stripixel detector concept:

- o True single-sided process
- o Two dimensional position sensitivity
- o No charge sharing problem
- o Minimum added capacitance
- Types of New single-sided stripixel detectors:
  - o Planar 2d detectors --- simple processing
  - (2+3)d detectors with single columns --- relative simple processing, modest radiation hardness
  - (2+3)d detectors with dual columns --- full 3d processing, most radiation hard
- First prototype of Planar 2d detectors and (2+3)d detectors with single columns has been designed, and processing will soon begin at CNM and BNL