

Session Program

6 September 2005

pre-GDB Meeting at CERN

Tuesday 6th September

CERN, IT Auditorium

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09:00

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Session | Location: CERN, IT Auditorium

10:00-10:40 Analysis of throughput tests and debugging process

Speaker

James Casey

10:40-11:00 Report from dCache workshop

Speaker

Maarten Litmaath

11:00-11:20 CASTOR2 experience

Speaker

Vlado Bahyl

11:20-11:40 DPM experience and future plans

Speaker

Jean-Philippe Baud

11:40-12:10 Experience with DPM and dCache at INFN-Bari

Speaker

Giacinto Donvito

12:10-12:40 Feedback from pilot LFC / FTS usage by experiments

Speaker

Patricia Mendez

12:40-13:10 Experiment test jobs and Integration with Site Functional Tests

Speaker

Piotr Nyczyk

13:10-13:30 Summary of Experiment Resource Requirements and Site Status

Speaker

Jamie Shiers / sites

13:30-14:30 LUNCH

14:30-14:45 CASTOR SRM status and plans

Speaker

Ben Couturier

14:45-15:15 Planning for SC4 and Initial LHC Service

Speaker

Jamie Shiers

15:15-15:45 Tier1 Plans for Achieving Nominal Data Rates by April 2006

Speaker
discussion

15:45-16:15 **Tier1 plans for Tier2 Support for SC4**

Speaker
discussion

16:15-16:45 **ARDA involvement in SC4**

Speaker
Massimo Lamanna

16:45-17:05 **End of Meeting**

18:00