

7th RD50 Workshop

Tuesday, 15 November 2005

Full Detector Systems (FDS) - AB Auditorium Meyrin (11:20 - 16:50)

time	[id] title	presenter
11:20	[33] Thermal issues for running Silicon detectors at the SLHC.	JOOST VOSSEBELD
11:40	[40] Numerical simulations of heavily irradiated p-type and n-type silicon detectors	MARCO PETASECCA
12:00	[36] Lunch Break	
13:30	[44] Study of radiation damage induced by 24 GeV/c and 26 MeV protons on heavily irradiated Magnetic Czochralski and Float Zone silicon detectors	NORMAN MANNA
13:50	[34] Progress on p-type isolation technology	MANUEL LOZANO
14:10	[39] TCAD simulations of isolation structures for p-type silicon microstrip detectors	CLAUDIO PIEMONTE
14:30	[35] Progress on Radiation Hardness tests on SiGe technologies for s-LHC	MIGUEL ULLÁN
14:50	[42] Signal-to-Noise Ratio for silicon Strip detectors at the LHC using p-type detectors and SiGe front-end electronics	HARTMUT SADROZINSKI
15:10	[38] Coffee Break	
15:40	[41] Pixel Devices on the common RD50 n-in-p Strip Detector Mask Set (6 inch)	TILMAN ROHE
16:00	[43] Update on Common Rd50 Project on 6 p-type wafers	HARTMUT SADROZINSKI
16:20	[37] Discussion: Full Detector Systems	