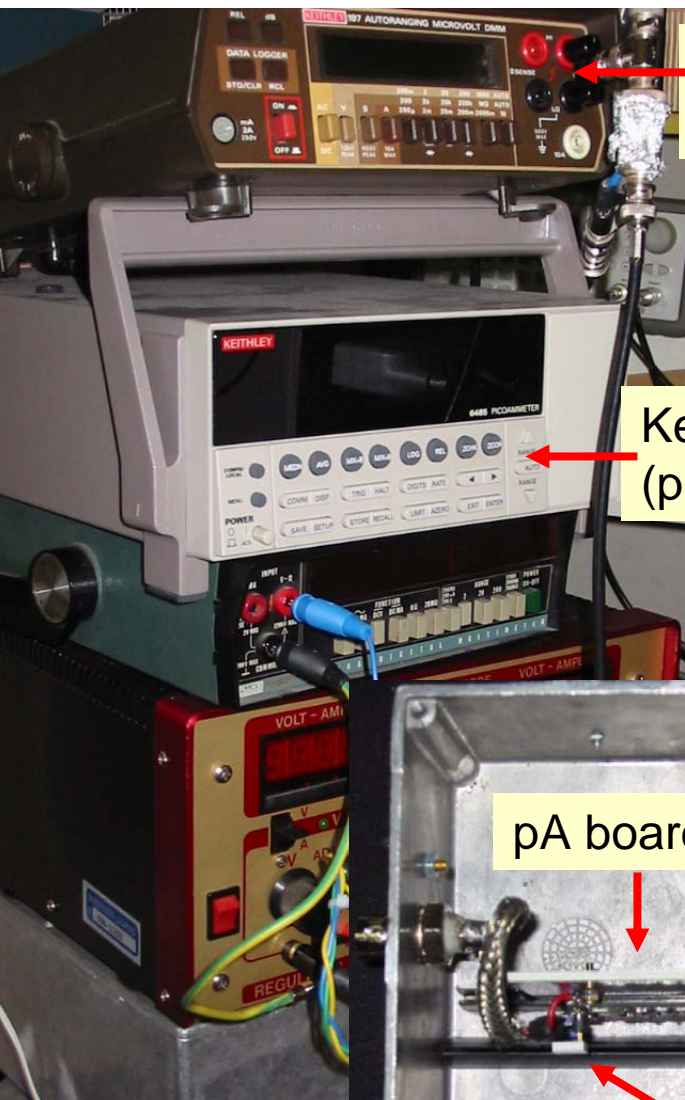


Expertises on
- SiPM
- wafer testing
- ... electronics design
at Rome INFN and
University "La Sapienza"

F. Meddi^(*)

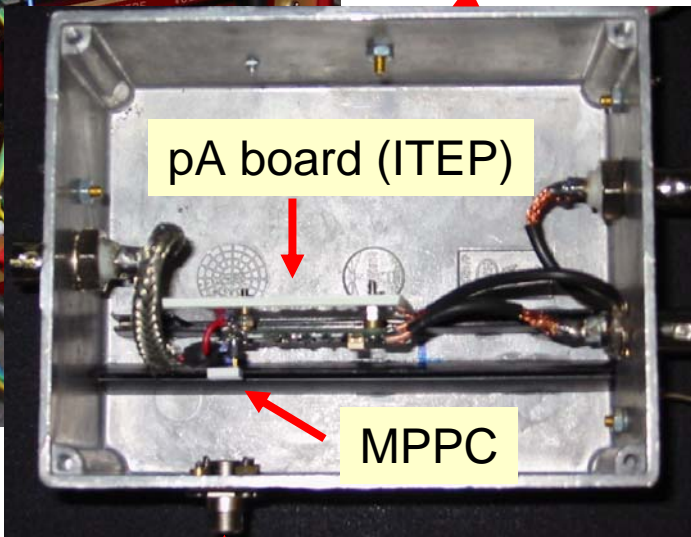
^(*)franco.meddi@roma1.infn.it



Keithley 197
(Microvolt DMM)

Keithley 6485
(picoammeter)

LM35 (T- sensor)



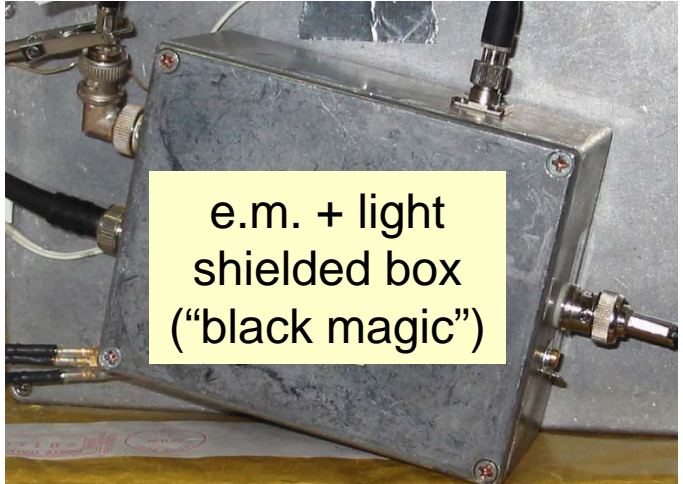
pA board (ITEP)

MPPC

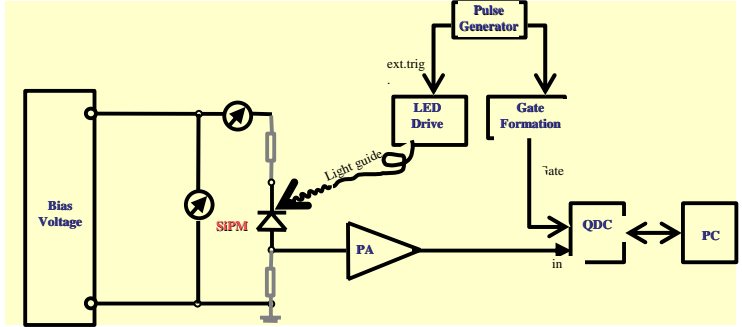
Optical Fiber
connector

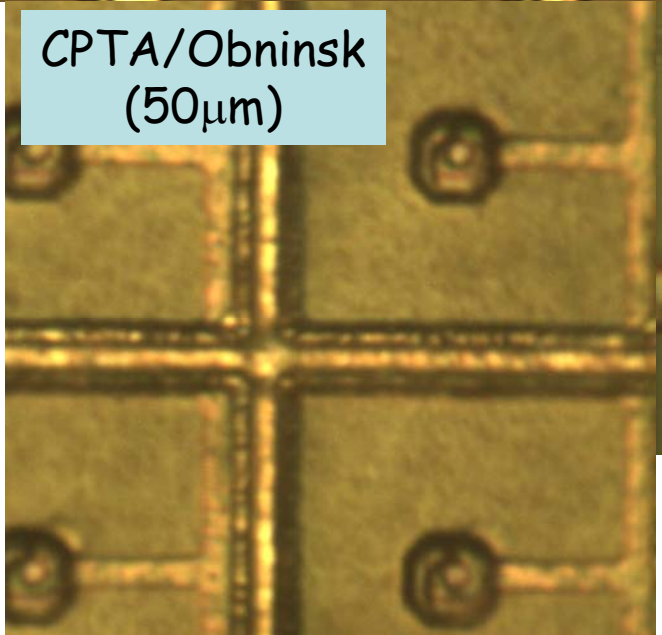
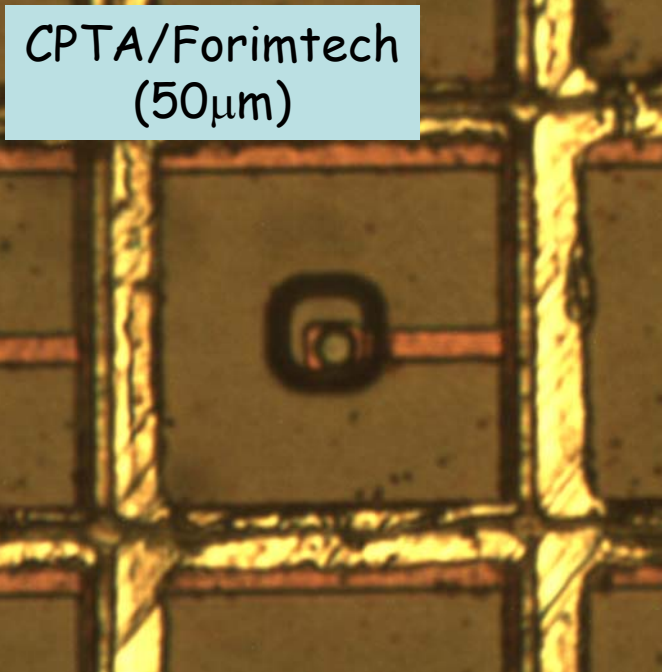


digital chilling incubator

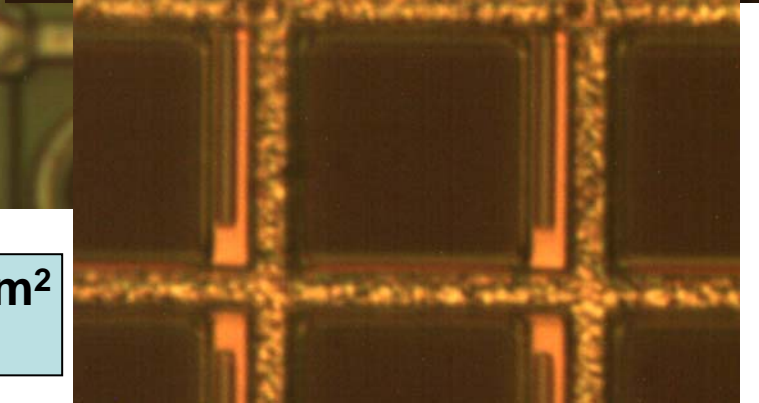
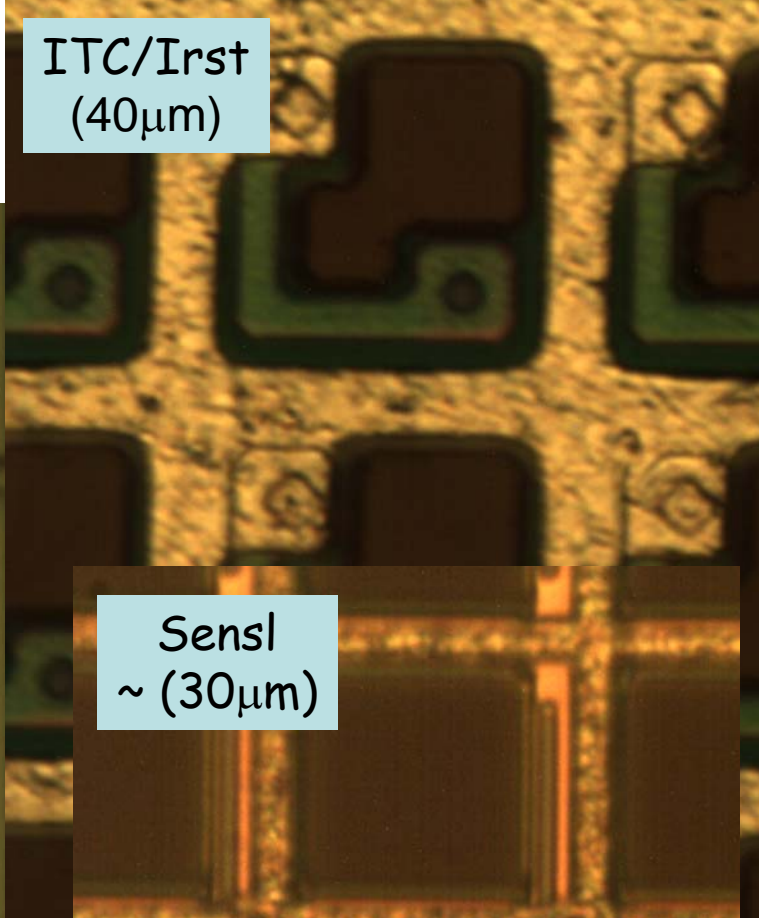
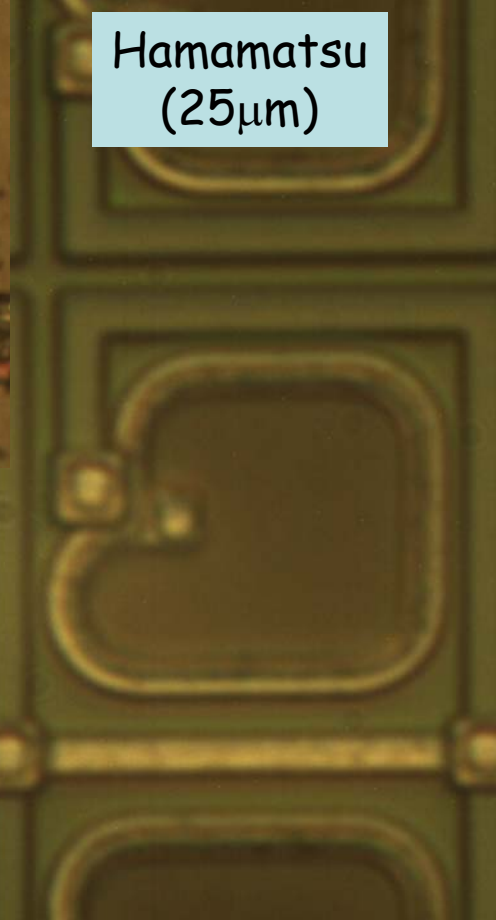


e.m. + light
shielded box
("black magic")





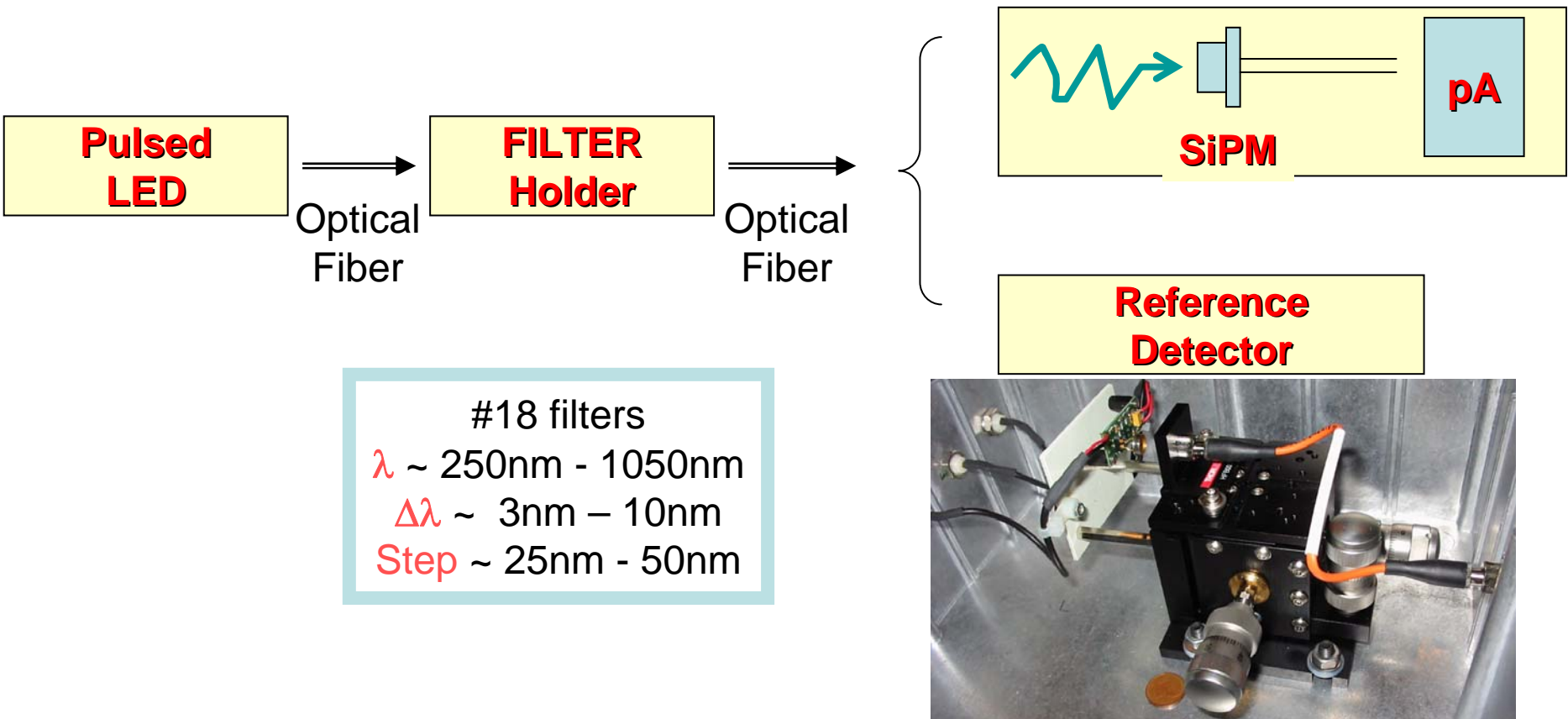
1. SiPM Photos (not in scale)



Cell size: $(25 \sim 50)^2 \mu\text{m}^2$
(500 ~ 1600) cells

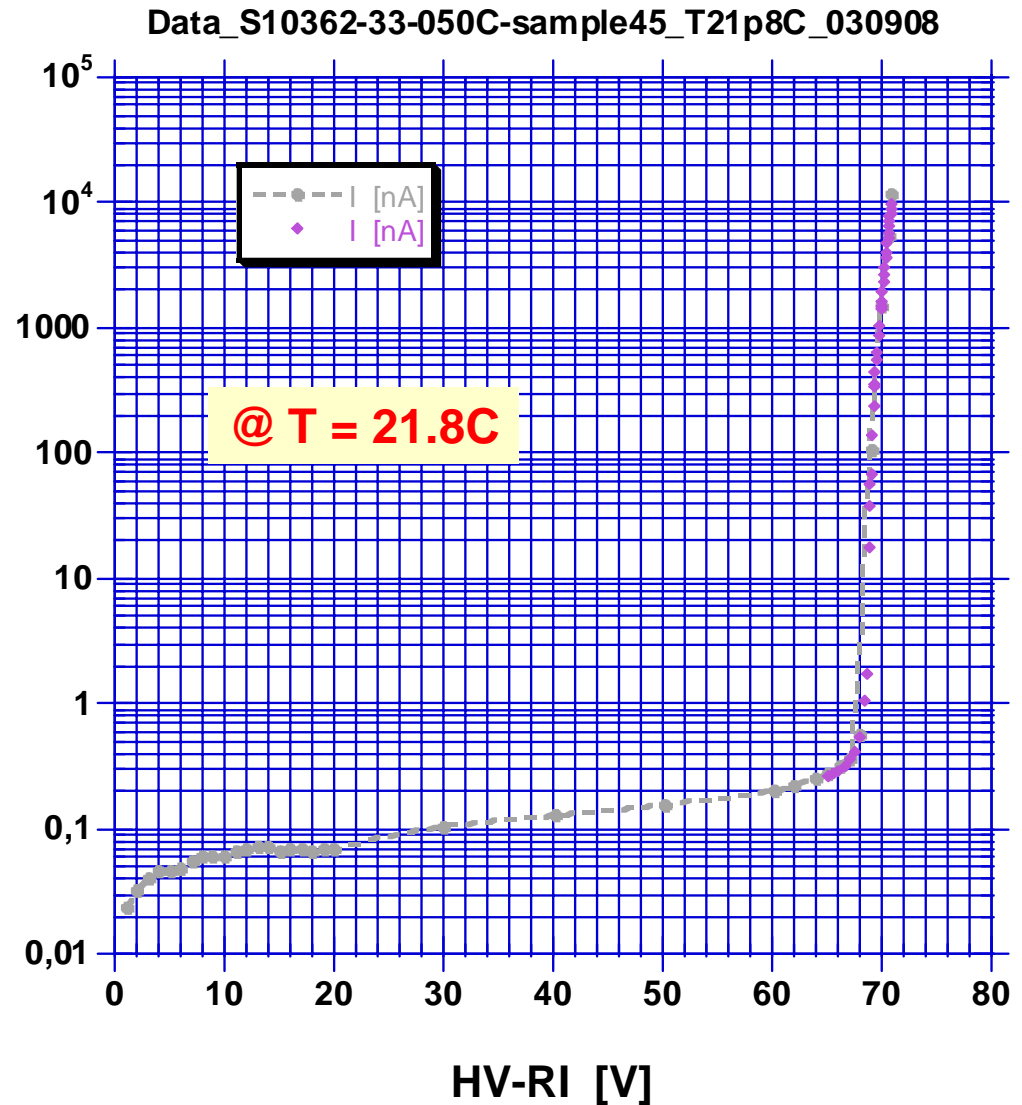
PDE:

- set-up modified in the optical part
 - mechanics for fine XYZ adjustment
- A new dark and electrically shielded box for SiPM and for reference Photo-detector



I-V measurements of 3x3 mm² MPPC

S10362-33-050C
Sample No. 44
Vop=69.81V



- Temp. stability < 0.1C
- Vbias correction applied (~ 100mV @ 1μA)
- Measure reproducibility: OK

S10362-33-050C
Sample No. 44
Vop=69.81V



LED: $\lambda=600\text{nm}$

HV69p441V_T20p0C

HV69p625V_T20p1C

→ $\Delta(\text{HV-RI})=+160\text{mV}$

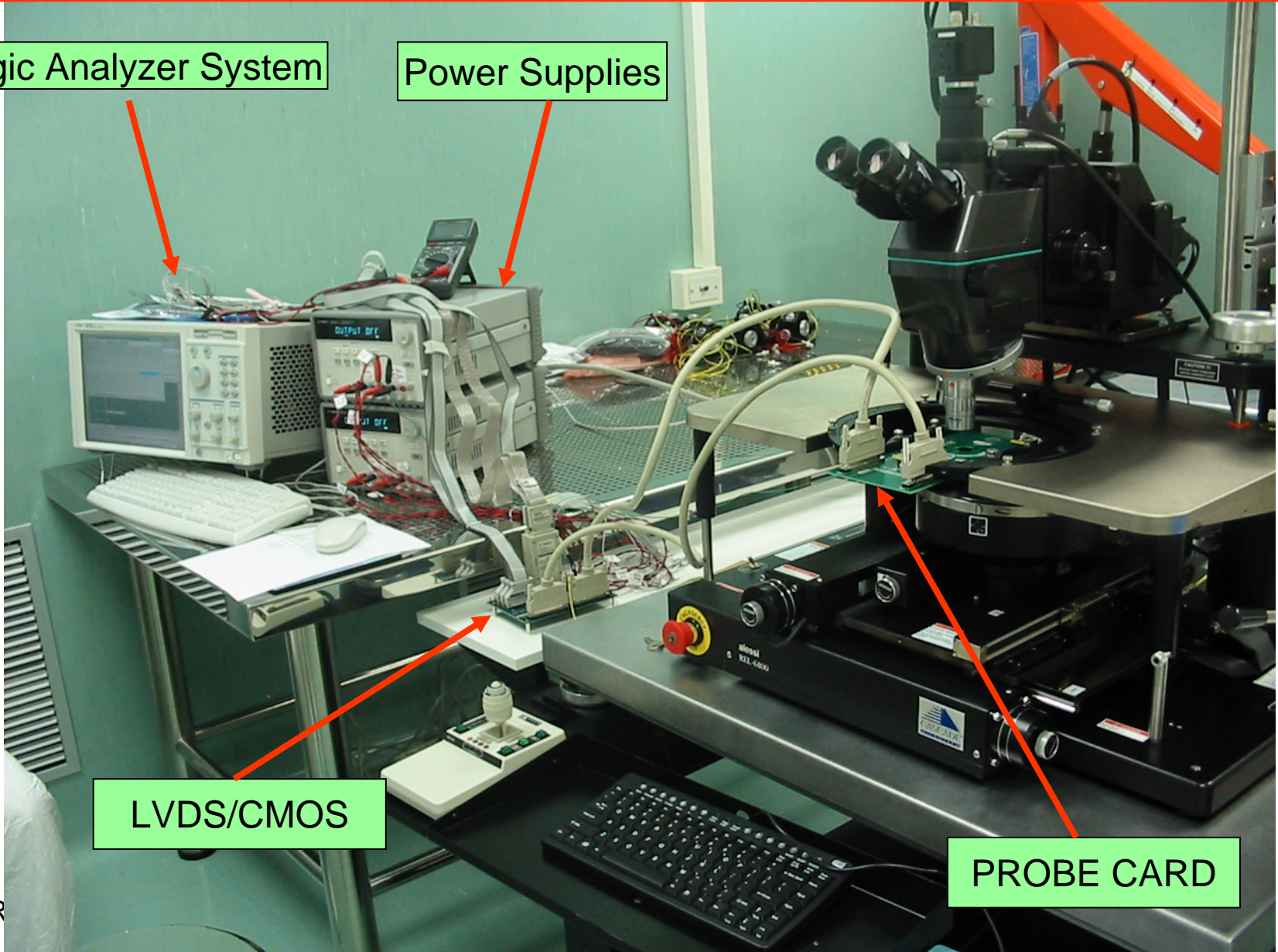
The Probe Station **CASCADE MICROTECH REL-6100** in the **CL100 Room**
equipped with Logic Analyzer **AGILENT 16702B** and S/W **LABVIEW**

Logic Analyzer System

Power Supplies

LVDS/CMOS

PROBE CARD



LHC-ALICE → S.D.D. - F.E.E. :

PASCAL and *AMBRA* ASICs
validation.

PASAm
wafer

