

ADT Post-LS1

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Special thanks to: L. Arnaudon, A. Boucherie, A. Butterworth, K. Fuchsberger, F. Killing, D. Jacquet, M. Jaussi, C. Julie, T. Levens, M. Mician, E. Montesino, S. Rains, A. Rey, and *all members of the RF piquet service!*

From **Evian'14**: “ADT pre-LS1” ...

... to **Evian'15**: “ADT Post-LS1”



Back in Business ...

Recap ADT: Transverse Feedback System

The transverse damper is a feedback system: it measures the bunch-by-bunch oscillations and damps them by fast electrostatic kickers.

Closed Loop Feedback → modified Target Response $T(s)$:

$$T(s) = \frac{X(s)}{Y(s)} = \frac{OL(s)}{1+OL(s)}$$

$OL(s) = GH_S H_C H_A \dots$ open loop transfer function

Key Parameters:

$K \dots \dots$ Feedback loop gain

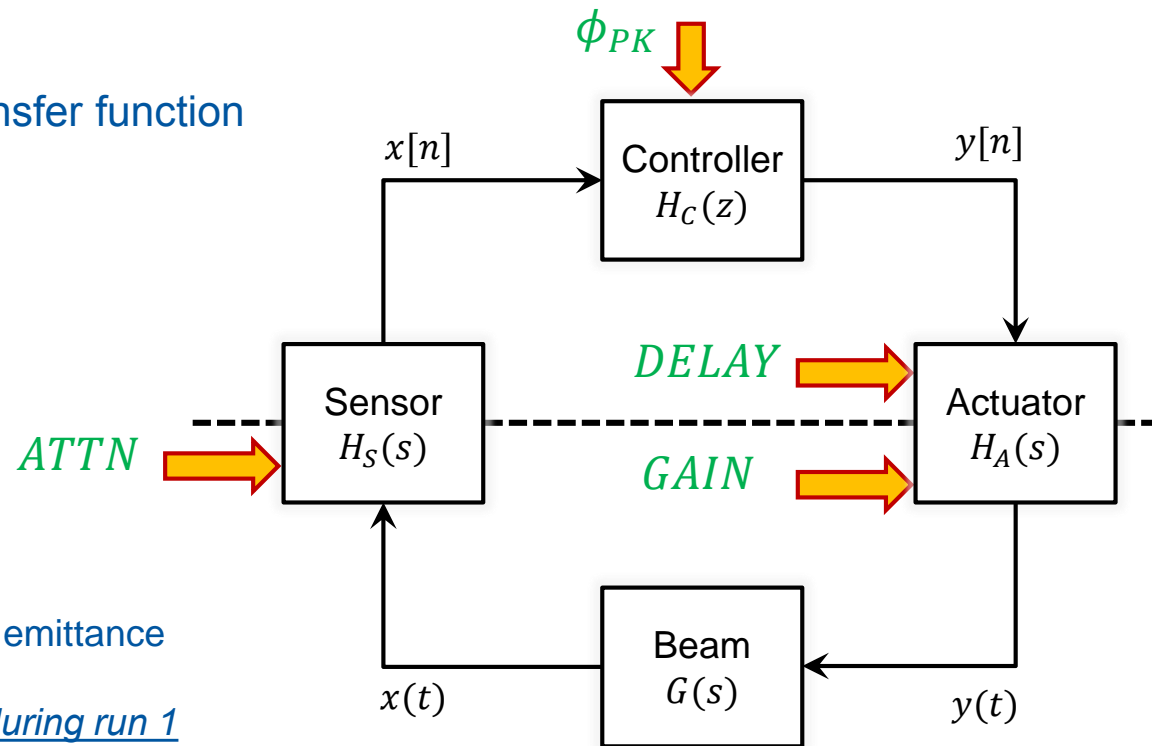
$\phi_{PK} \dots$ Feedback phase

$T \dots \dots$ Total loop delay

Primarily designed for:

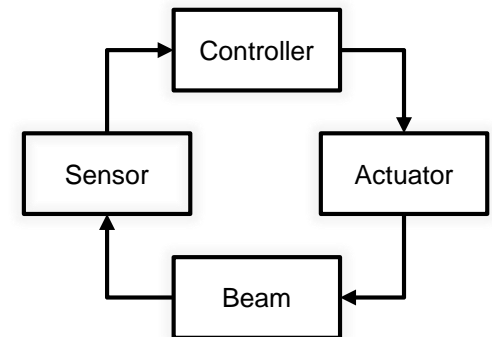
- Damping of injection oscillations
- Counteract coupled bunch instability
- Preservation of the transverse beam emittance

... plus more and more features added during run 1



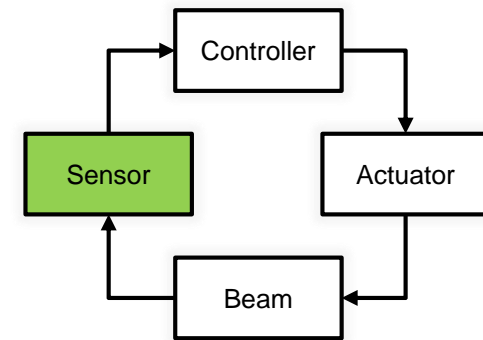
Outline

- Improvements applied during LS1
- Features, present uses of the ADT in different contexts
- Issues and possible mitigation for 2016 run
- Operation with doublets
- ADT settings, operation, and experts?
- Outlook 2016



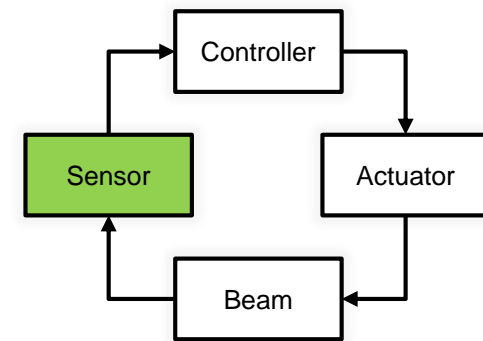
LS1: New PUs Q8/Q10 prepared (1)

- Four pick-ups per beam, per plane, located at point 4
 - Stripline PUs (constant coupling type)
 - All BPMCs existing (pick-up swap with BPF system)
 - Improvement in S/N by doubling the number PUs → *Plan for 2016*



B1 horizontal	Q10L	Q9L	Q8L	Q7L	Q7R	Q8R	Q9R	Q10R
		b = 111 m		b = 106 m		b = 133 m		b = 153 m
		existing		existing		new		new
B1 vertical	Q10L	Q9L	Q8L		Q7R	Q8R	Q9R	Q10R
	b = 175 m		b = 155 m		b = 161 m		b = 142 m	
	new		new		existing		existing	
B2 horizontal	Q10L	Q9L	Q8L		Q7R	Q8R	Q9R	Q10R
	b = 158 m		b = 96 m		b = 150 m		b = 101 m	
	new		new		existing		existing	
B2 vertical	Q10L	Q9L	Q8L	Q7L	Q7R	Q8R	Q9R	Q10R
		b = 160 m		b = 167 m		b = 151 m		b = 180 m
		existing		existing		new		new

LS1: New PUs Q8/Q10 prepared (2)

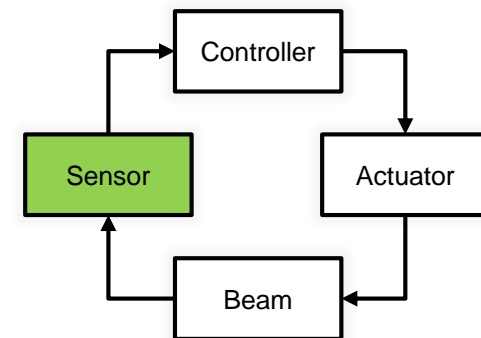


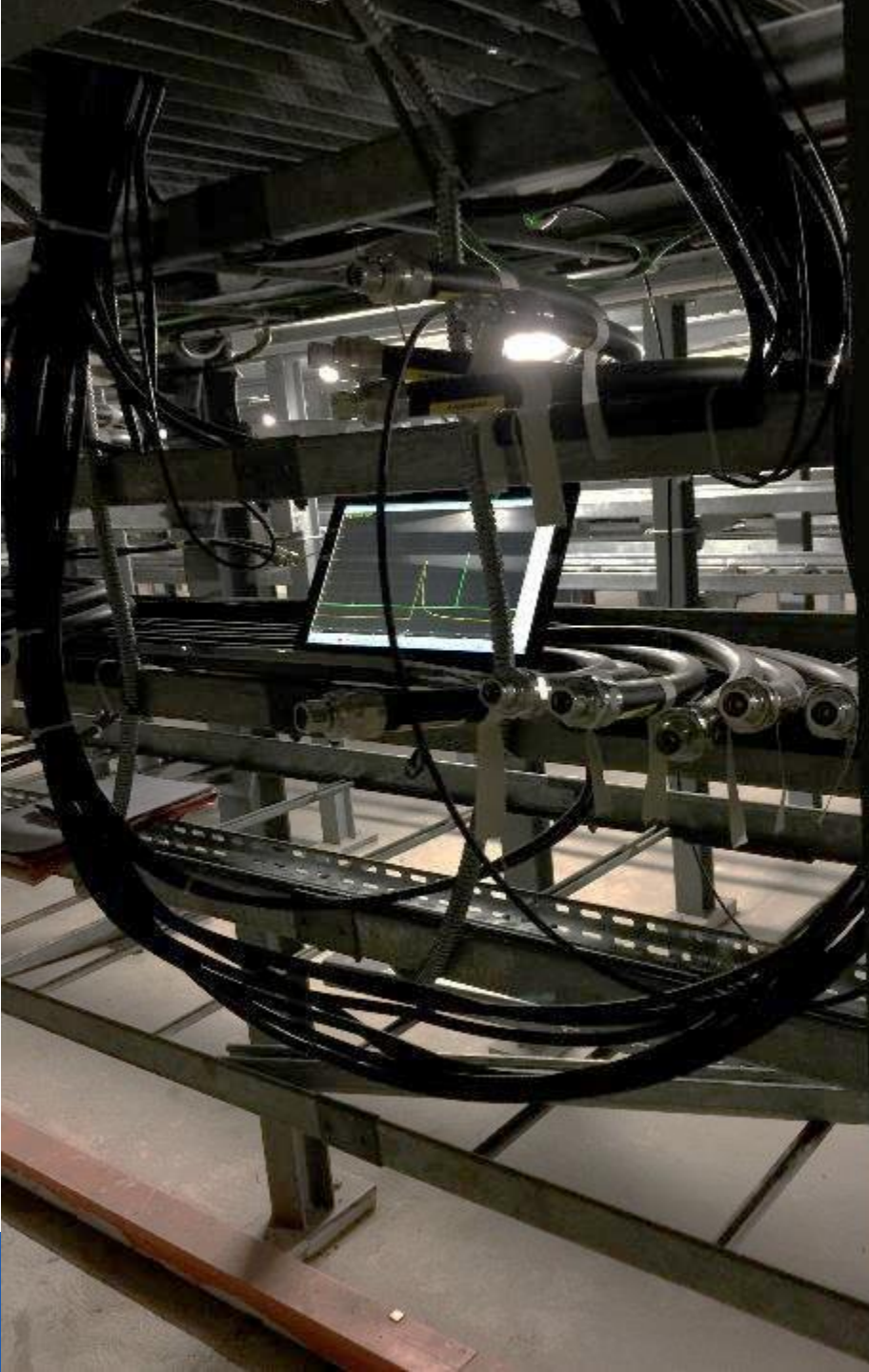
“Câbleur deluxe”

LS1: Cabling re-done (1)

Improvements applied during LS1:

- Replacement of PU cables to Q7/Q8/Q9/Q10 (corrugated coax vs. smooth wall)
- New cables for 8 drive signals from surface (SR4) to UX45 (better pulse for AGC)
→ lower losses + digitally phase compensated





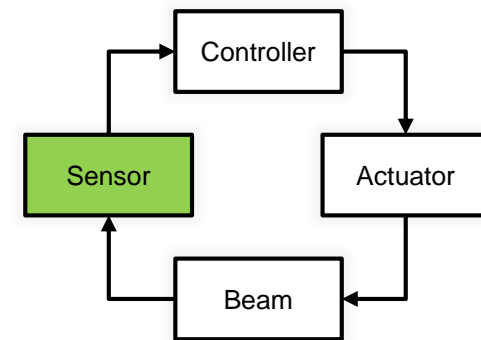
LS1: Cabling re-done (2)

New cables qualified on the spot, to ensure quality of installation:

- Trace and locate defects in cables
- Identification of degradation due to installation process
- Electrical length matched to $<25\text{ps}$ ($\pm 5\text{mm}$ for 800m cable!)

Result 1: **LS1 Re-cabling campaign was a big success** → unprecedented signal quality for beam position processing!

Result 2: We do have excellent WiFi coverage on CERN campus... Come and join us in the cable ducts!



LS1: ADT Power System

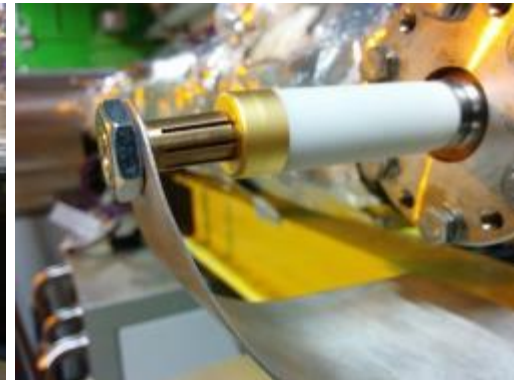
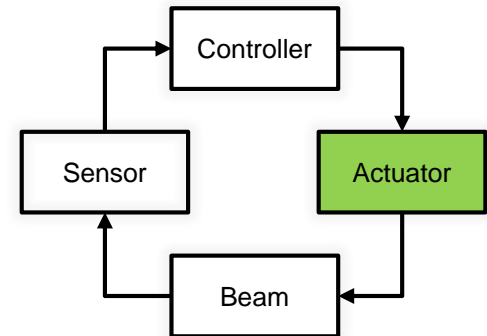
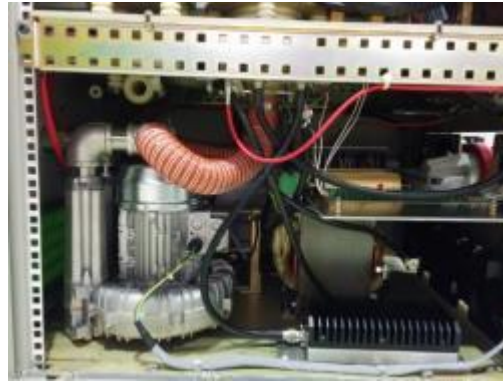
Power amplifiers

- Maintenance (cleaning, divider calibration, re-commissioning in B.867)
- New relay plates
- New air cooling system
- New air cooling pipes
- New 200W loads
- Removal of pre-heating resistances
- New water flow meters in RB44 – RB46
- New clamp between amplifiers and kickers
- New clamp between kickers striplines and feedthroughs
- Additional vacuum gauge in RB44 (3 gauges per beams and on each sides, 12 in all)

Anode Power converters

- Cleaning
- Exchange all fans
- Adding new H.V. switches (command off/online remotely)

Status of changes with respect to run 1:
increased reliability and availability



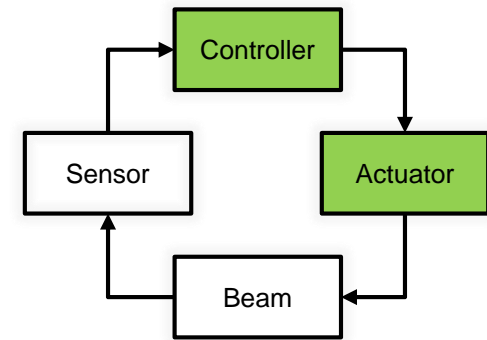
LS1: New Signal Processing Unit: *mDSPU* (1)

Goals and changes from Run1 to Run 2:

- Possibility to combine **four pick-ups**
→ digital, currently 2 in use for Q7 & Q9
- Separate control for all features with high resolution
→ calls for **independent output DACs**
- Implementation of all added features during run 1
→ more **powerful digital signal processing**
- High bandwidth **digital links** to observation box
- Complementary **data processing algorithm** to detect anti-symmetric intra-bunch motion (**Todo**)

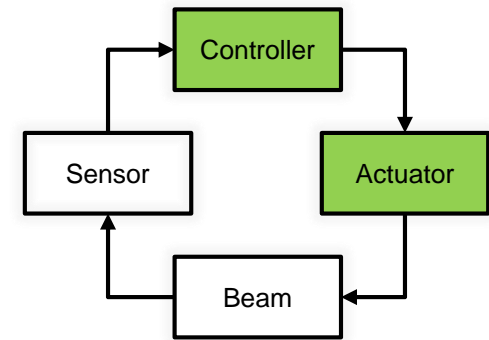
Result: **New Signal Processing HW** developed during LS1, **suitable in performance** to comfortably integrate all requested features

Synergy between SPS Damper upgrade and new LHC hardware:
All tests successfully passed in the SPS in 2014
→ further enhanced for LHC ADT startup in 2015

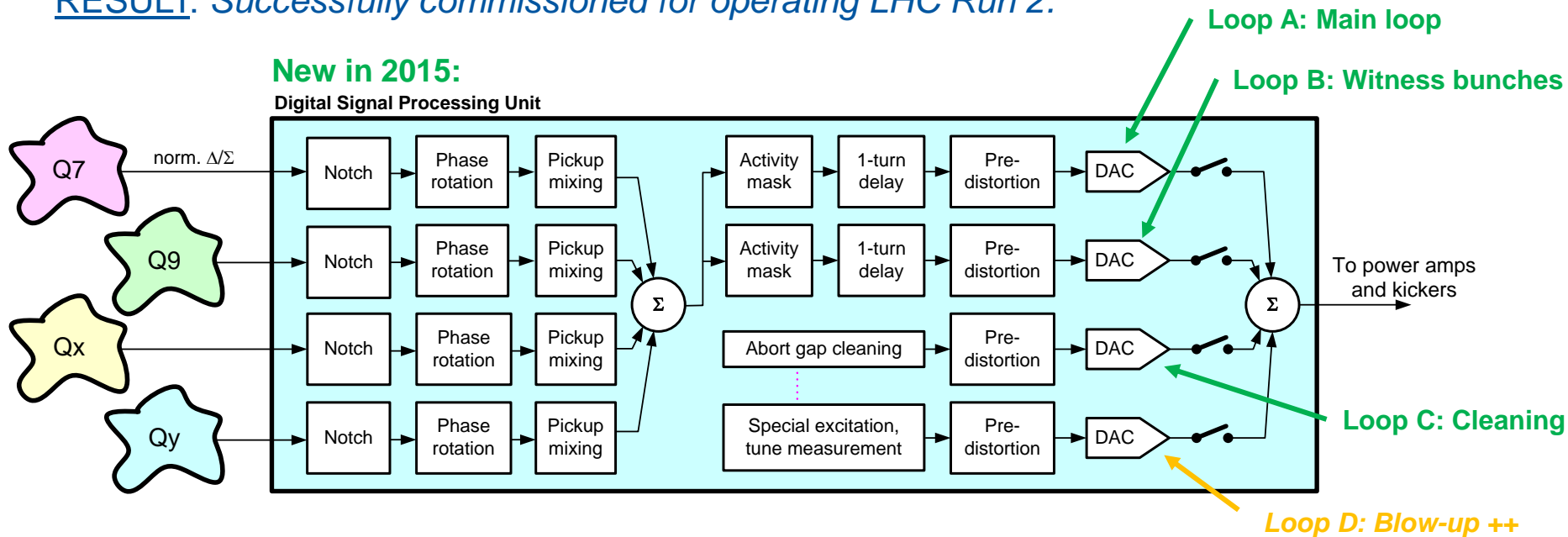


LS1: New Signal Processing Unit: *mDSPU* (2)

- Independent loop gain control for
 - main loop (**gain A**)
 - witness bunches, used by tune feedback (**gain B**)
- Dedicated output for cleaning (**gain C**)
 - Injection gap cleaning in horizontal plane
 - Abort gap cleaning in vertical plane
 - Transverse blow-up (noise) for individual bunches (e.g., loss maps)

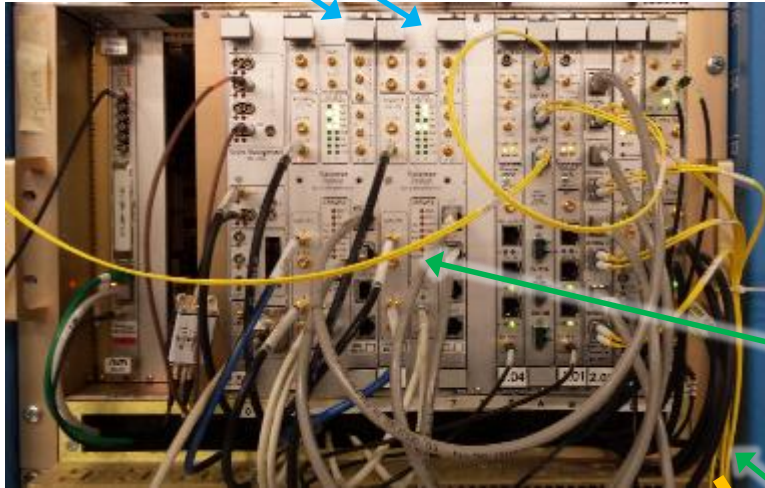


RESULT: Successfully commissioned for operating LHC Run 2.

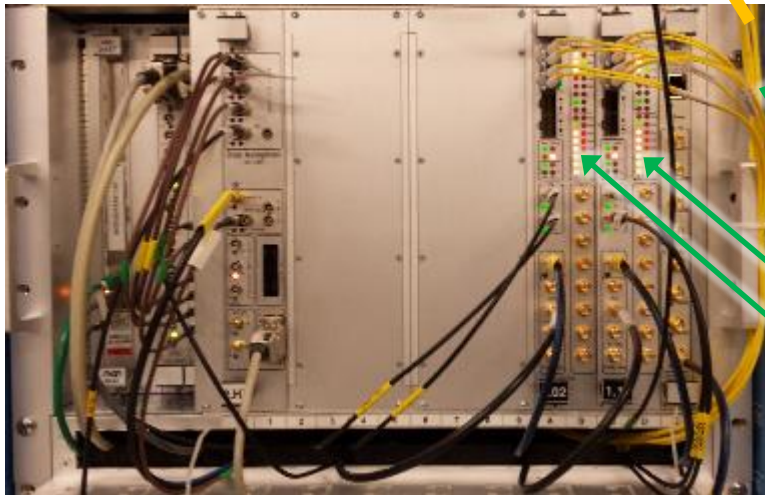


Implementation for ADT in 2015

BPOS Q7
BPOS Q9

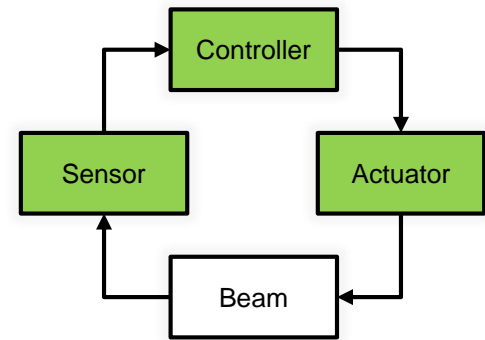


Link to Obs Box (Q7)



Links connecting BPOS VME modules to mDSPU VME processing module generating ADT drive signal

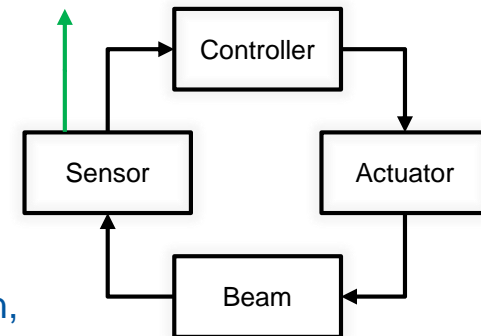
mDSPUM2
mDSPUM1



Shown: 1 beam, 1 plane → x4

ADT Features and Use Cases (1)

- ADT Observation Box, a.k.a. “ObsBox”
 - Bunch-by-bunch data available within the ADT blocks
 - Continuous, online analysis of the transverse (and longitudinal) motion, all kinds of fixed displays...
 - Storage of a full 40 MHz, bunch-by-bunch data for offline analysis
(see T. Levens et al: “Instability diagnostics”, these proceedings)



Server in SR4 for ADT (1st unit)

Front



Rear



Trigger inputs

Fiber Links

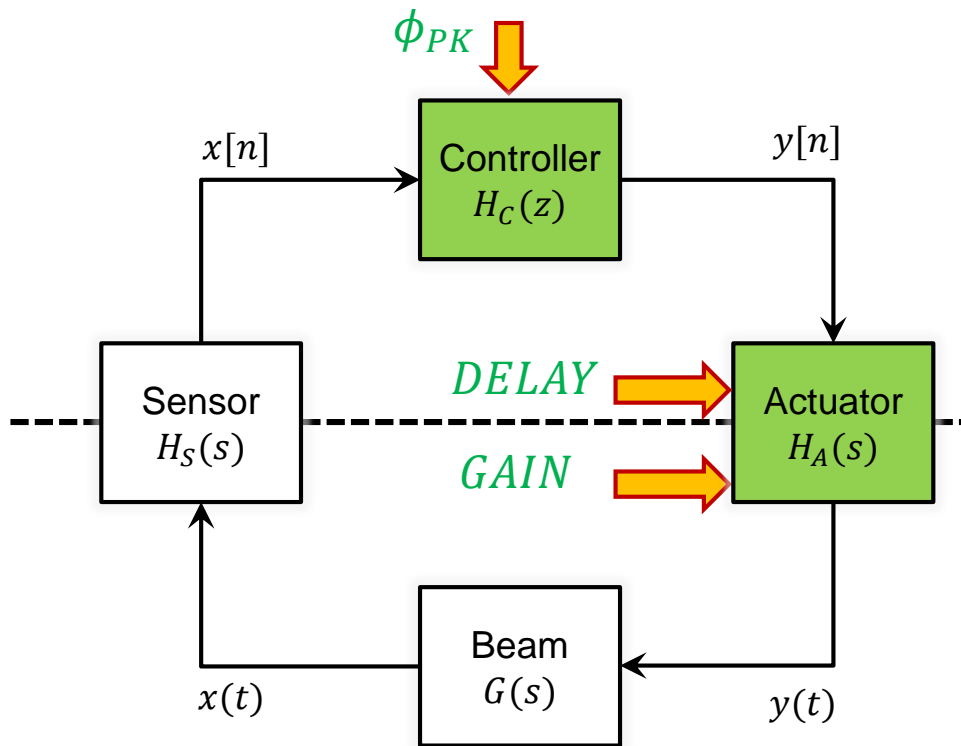
Just TOO BIG to fit on current LHCOP consoles!

New screens for ADT??

New FGC RF-style function generators

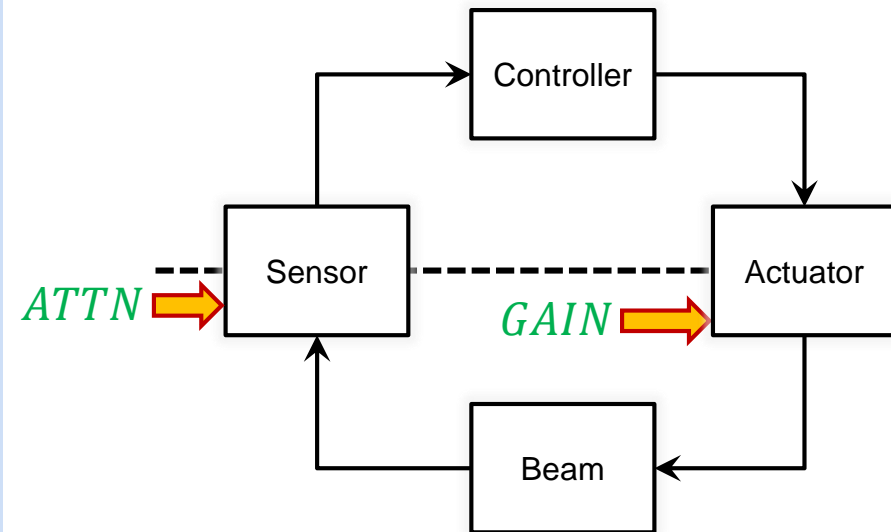
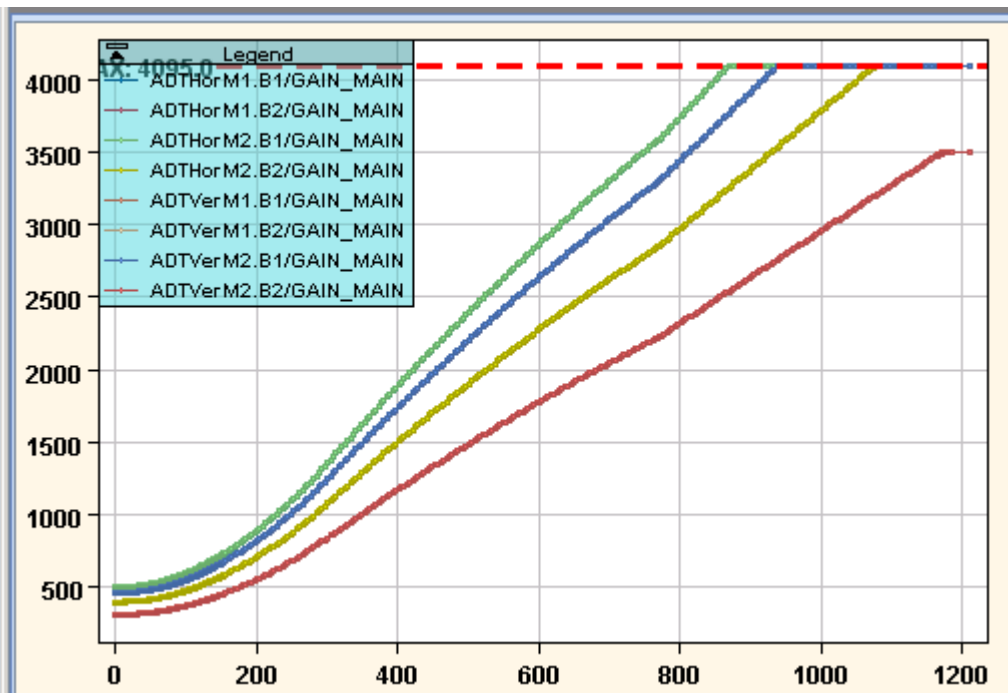
New FGC RF-style function generators (8 in total), providing 88 functions:

- Cycle-dependent control of all output gain stages
 - $3 \times 8 = 24$ **gain functions** → **commissioned**
- Transition from injection tunes to collision tunes
 - $4 \times 8 = 32$ **phase functions** → **commissioned**
 - $4 \times 8 = 32$ PU mixing functions → prepared



ADT Features, Performance, Use Cases (2)

- Comfortable gain margin at injection
 - Running with only 1 active module → still damped
- Loop gain saturation effects during ramp
 - Different saturation times per beam per plane → depends on frontend gain
 - Mitigation possible in digital → gain equalizer, rescaling, re-distribute loop gain



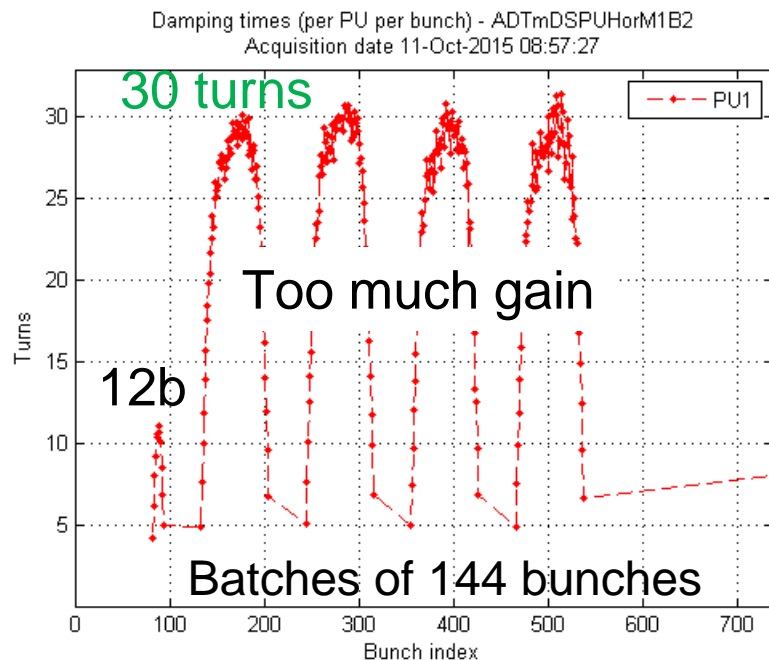
ADT Features, Performance, Use Cases (3)

○ Injection damping

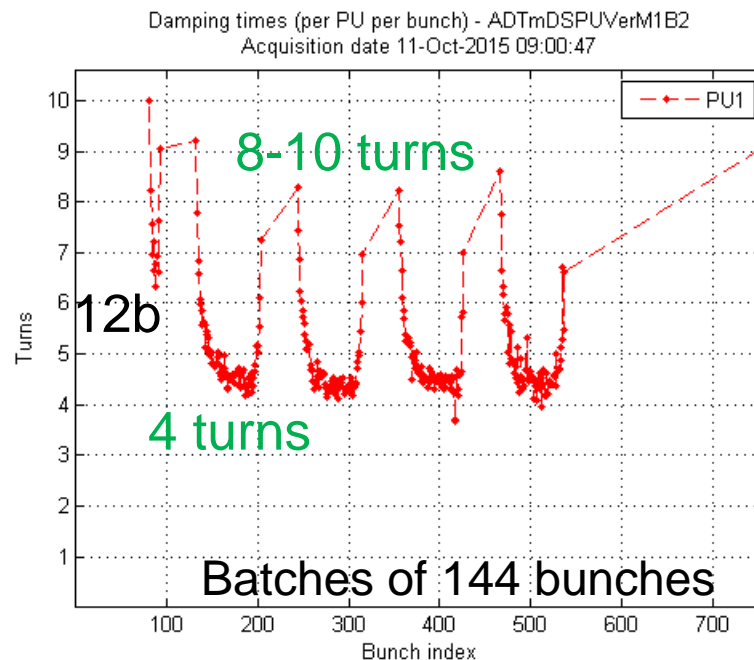
- Average low frequency damping times for coupled bunch modes < 10 turns
- Studies on damping times carried out (W. Hofle)
 - Single bunch \rightarrow extrapolation to bunch trains
- Overdamping discovered / partly corrected (\rightarrow elogbook)

See also: “*MARGINS TO INCREASE ADT GAIN AT INJECTION*”, W. Hofle, LBOC 49, 6 Oct. 2015
<https://indico.cern.ch/event/451051/>

H-plane (beam 2)

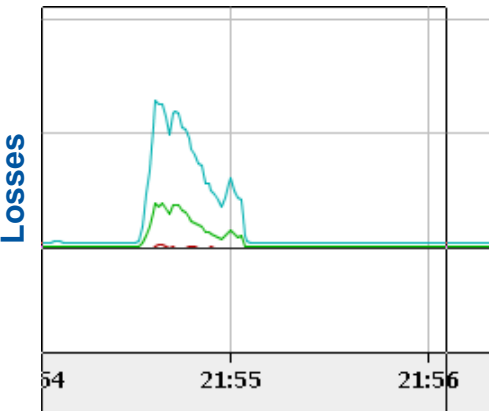


V-plane (beam 2)



ADT Features and Use Cases (4)

- Injection cleaning, abort gap cleaning fully automated
 - Integrated into SIS
 - Linking abort gap cleaning to abort gap monitor (automatic switch-on)
 - Pulse shape is bipolar, optimization of edges to limit effect outside gap
- Blow-up device:
 - “About the correlation between Loss Map Control and Distributed Noise.”



ADT Blowup by injecting noise



Distributed Noise Sources

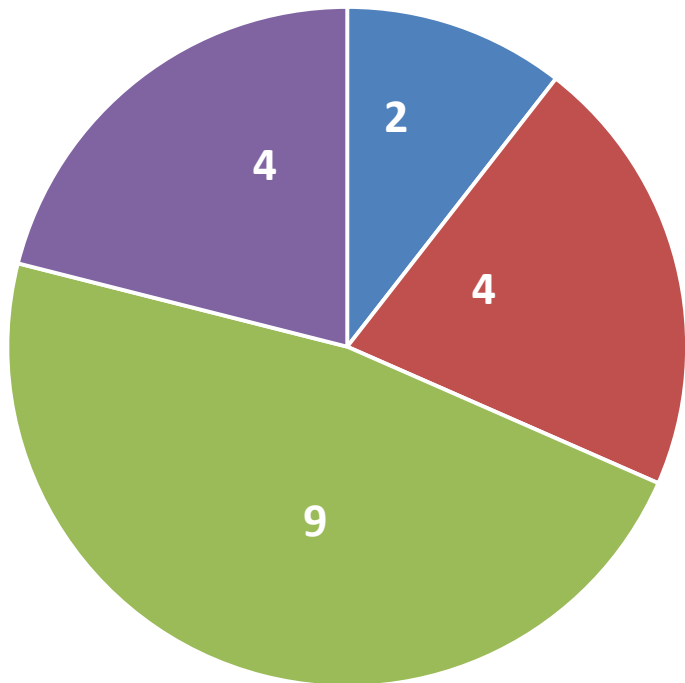
Result 1:
ADT Blow-up by injecting noise
→ Confirmed.

Result 2:
A correlation between loss map control and distributed noise
→ PLAUSIBLE.

Quench test,
13 Dec 2015

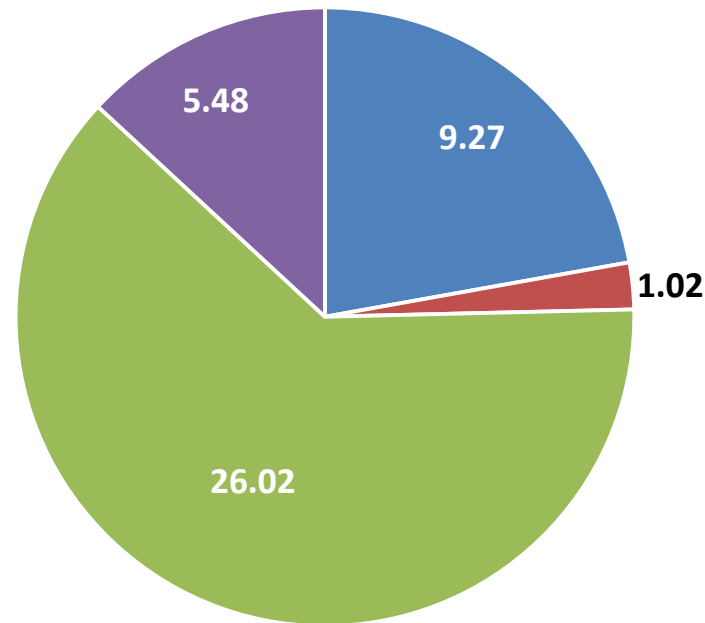
Overall availability: 18 ADT Faults in 2015

Number of Faults



- UW45 cooling recovery ADT
- ADT Power
- ADT controls /interlocks
- ADTLL/configuration

Hours recorded



Three largest faults → 82 % of fault time

- UX45 interlock chassis (22.05 h),
→ **3 interventions**
- CV water cooling UW45 recovery (9.27 h), → **2 interventions**
- Gbit Link ADTLL (3.16 h),
→ **3 interventions**

Thank you for your attention!

The ADT team works hard
to be always there also during LHC run 2.

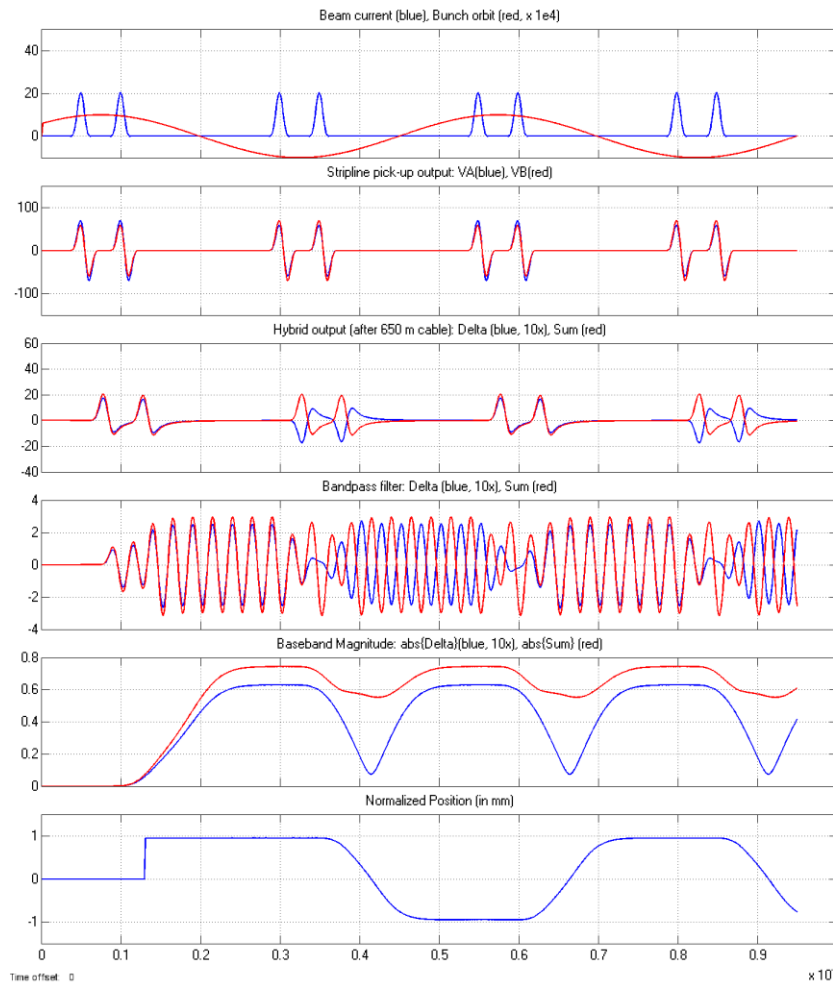


THANK YOU

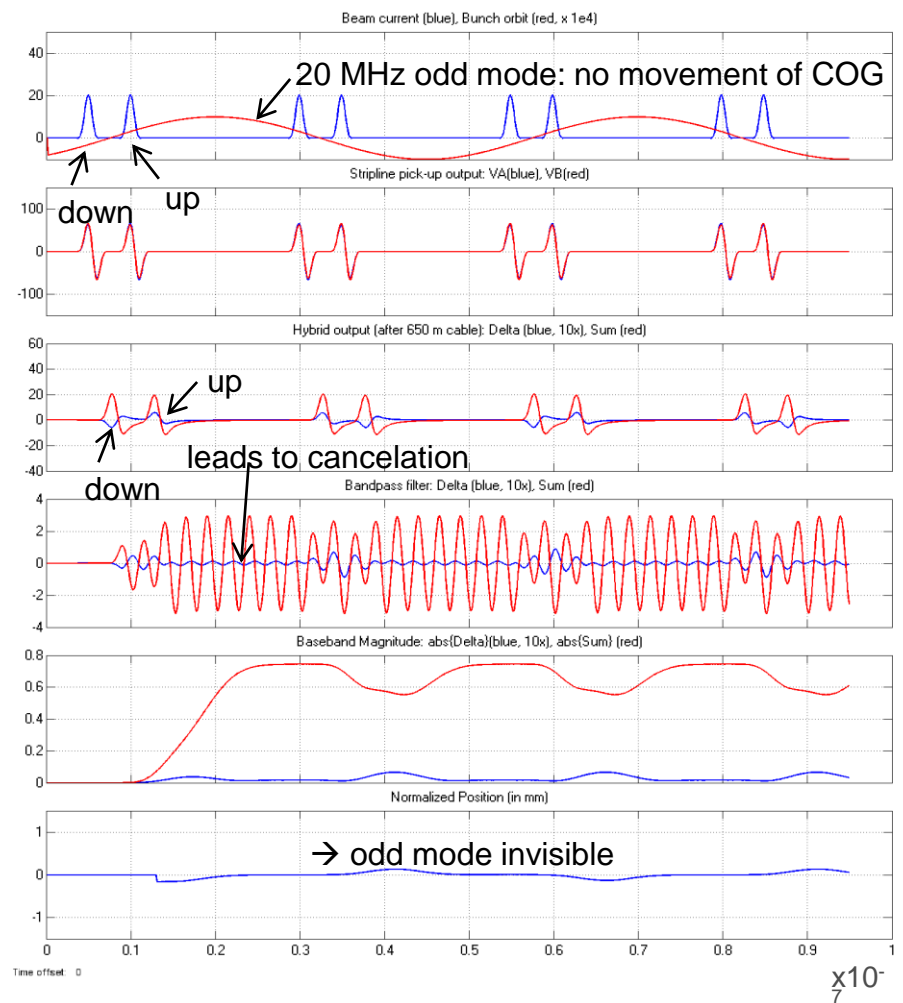
Questions?

Operation with Doublets

centre of gravity oscillation (even parity)



with-in bunchlet oscillation (odd parity)



LHC ADT HW working with 5 ns bunchlets, requires separate set-up (fine delays for clocks)