MT25 Conference 2017 - Timetable, Abstracts, Orals and Posters



Contribution ID: 482

Type: Poster Presentation of 1h45m

## Design and Test of 10 kV/400 A Flux-coupling Type Superconducting Fault Current Limiting Module

Tuesday 29 August 2017 13:15 (1h 45m)

The flux-coupling type superconducting fault current limiter (FC-SFCL) is suggested to suppress the shortcircuit current in the dc line of VSC-HVDC system. The FC-SFCL could generate larger inductance and smaller resistance at the beginning of current limiting process, but show smaller reactance and very large resistance at the end. One FC-SFCL module with two branches is designed and tested. For the FC-SFCL module, the two branches could use the same or different YBCO tapes. Furthermore the electromagnetic analysis and parameter calculation of the module are performed. In order to study the current-limiting performance of the FC-SFCL under the rapid change of fault current, a dynamic simulation model is established. Finally, the transient properties of FC-SFCL module under 10 ms dc impulse impact are tested.

## **Submitters Country**

China

**Authors:** QIU, Qingquan (Institute of Electrical Engineering, Chinese Academy of Science); Prof. XIAO, Liye (Institute of Electrical Engineering, Chinese Academy of Sciences); Dr ZHANG, Zhifeng (Applied Superconductivity Key Lab, Institute of Electrical Engineering, CAS, Being, 100190, China 2University of Chinese Academy of Sciences, Beijing, 100190, China); Ms JING, Liwei (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, Chinese Academy of Sciences); Mr LIU, Qingfeng (Institute of Electrical Engineering, CAS, Being, 100190, China 2University of Chinese Academy of Sciences, Beijing, 100190, China and University of Chinese Academy of Sciences, Beijing, 100190, China and University of Chinese Academy of Sciences, Beijing, 100190, China and University of Chinese Academy of Sciences, Beijing, 100190, China and University of Chinese Academy of Sciences, Beijing, 100190, China); Prof. XIA, Dong (Chinese Academy of Sciences)

**Presenter:** QIU, Qingquan (Institute of Electrical Engineering, Chinese Academy of Science)

Session Classification: Tue-Af-Po2.07

Track Classification: E6 - Transformers and Fault Current Limiters