

# Power Converters

*Tuesday 26 January 2016 11:30 (30 minutes)*

<P>The hardware performance of the LHC power converters during 2015 is presented. After a brief summary of the changes made during LS1, the performance of the power converters will be detailed by presenting the availability matrices of the different systems. The efficiency of the mitigations deployed during LS1 as well as the remaining observed failure modes are discussed. Finally, an outlook of foreseen measures for consolidation/mitigation for 2015-2016 YETS and the next years is given.</P>

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**Session Classification:** Sessions 3 & 4: LHC Hardware Performance

**Track Classification:** Session 3: LHC Hardware Performance