

2016 IEEE Power Modulator and High Voltage Conference

Wednesday, 6 July 2016

Oral 4: High Voltage Design, Devices, Testing, and Diagnostics - Gold Ballroom (15:30 - 17:30)

time	[id] title	presenter
15:30	[195] Power Evaluation of a Cree 1200 V 50A Si-C MOSFET using the Solid State Power Emulator	THOMAS, Richard
16:00	[79] High Voltage Capacitors Environmental Testing	BANKOWSKI, Elena
16:15	[41] A Compact Faraday Cup for Nanosecond-Pulse Runaway Electron Beams	ZHANG, Cheng
16:30	[186] Miniature High Voltage, High Temperature Component Package Development	Dr RASHKIN, Lee
16:45	[194] Reserch of sweep frequency impedance criterion to determine transformer winding deformation	Mr YANG, Fan
17:00	[239] Optimal design of a medium voltage high frequency transformer with a high isolation voltage (115kV)	JARITZ, Michael
17:15	[104] DESIGN CONSIDERATIONS FOR HIGHER ELECTRICAL POWER SYSTEM VOLTAGES IN AEROSPACE VEHICLES	Dr SCHWEICKART, Dan