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<th>Sunday, Sep. 25</th>
<th>Monday, Sep. 26</th>
<th>Tuesday, Sep. 27</th>
<th>Wednesday, Sep. 28</th>
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<th>Friday, Sep. 30</th>
<th>Saturday, Oct. 1</th>
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<td>Welcome</td>
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<td>Operational experience on current detectors</td>
<td>Applications to medical and other fields</td>
<td>New developments and detector R&amp;D</td>
<td>Online and offline tracking and vertexing</td>
<td>Bus departure to Roma FCO airport</td>
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<td>Chair: R. Dell’Orso</td>
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<td>Operational experience on current detectors</td>
<td>Detectors in design and construction</td>
<td>Applications to medical and other fields</td>
<td>New developments and detector R&amp;D</td>
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<td>Operational experience on current detectors</td>
<td>Detectors in design and construction</td>
<td>Radiation Hardness and Simulations</td>
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<td>Chair: D. Bortoletto</td>
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<td>Welcome cocktail at Hotel Hermitage</td>
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<td>Detectors in design and construction</td>
<td>Wine and cheese poster and industry session</td>
<td>Electronics and system integration</td>
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## Detailed Program

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<td>The LHCb VeLo for Phase 1 upgrade</td>
<td>R&amp;D networks and opportunities</td>
<td>Small pitch 3D devices</td>
<td>FTK status and perspectives for track trigger in ATLAS at HL-LHC</td>
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<td>ALICE ITS operational experience</td>
<td>The LHCb UT for Phase 1 upgrade</td>
<td>Applications of vertexing detectors</td>
<td>SOI Monolithic pixel detector technology</td>
<td>The CMS track trigger for the High Luminosity LHC</td>
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<td>Vertexing and tracking in hadrotherapy</td>
<td>More channel cooling solutions for silicon detectors</td>
<td>Tracking in high multiplicity events</td>
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<td>ATLAS IBL operational experience</td>
<td>The upgrade of the CMS pixel detector for phase 1</td>
<td>DepFET Direct Electron Detectors for time-resolved imaging applications</td>
<td>Low gain avalanche devices</td>
<td>Using precision timing information in high rate and high pileup conditions</td>
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<td>ATLAS Tracker and pixel operational experience</td>
<td>The CMS Silicon Pixel detector for HL-LHC</td>
<td>XFEL Detector Developments</td>
<td>High fluence effects on silicon detectors: damage and defects characterization</td>
<td>Investigating the Micron Automata Processor for HEP Pattern Recognition Applications</td>
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<td>The CMS Outer Tracker detector for HL-LHC</td>
<td>Diamond detector technology: status and perspectives</td>
<td>Experimental techniques for defect characterization at very high fluence</td>
<td>Tracking, calibration &amp; alignment, and data processing in the LHCb upgrade</td>
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<td>The Mu3e Pixel-Tracker</td>
<td>Radiation damage models, comparison and performance of TCAD simulation</td>
<td>The ILC Vertex Detector requirements</td>
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<td>The ATLAS tracker pixel detector for HL-LHC</td>
<td>Radiation Tolerance of 65nm CMOS</td>
<td>Development of detector technologies for ILC vertexing</td>
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<td>RD53 Status &amp; Plans</td>
<td>CLIC silicon pixel R&amp;D</td>
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<td>The DAMPE silicon tungsten tracker</td>
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<td>Wine and cheese poster and industry session</td>
<td>High Speed Optical and Electrical link developments</td>
<td>Workshop summary</td>
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<td>Development of wireless data and power transmission for tracking detectors</td>
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