

## **Fast beam interlock system for machine protection at ESS**

*Tuesday, 2 February 2016 16:30 (20 minutes)*

The ESS Fast Beam Interlock System (FBIS) is based on FPGA technology and has very high time and reliability requirements. Its development includes iterative processes to reduce the number of false beam trips and blind failures at the same time as processing time needs to be around 1  $\mu$ s. This presentation discusses the technologies implemented and possible design choices for the FBIS.

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