

#### Signal Formation & Timing in Conventional P-I-N Diodes

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Constraints for precision timing from sensors (currents) from pre-amplifier (voltages) Noise and pulse shaping in Broad-Band amplifiers Simulations of Pulse shapes with Weightfield 2 (WF2) Pivotal role of detector capacitance Case study: Simulation of recently reported MIP timing measurements



#### A time-tagging detector



For calorimeter applications, the sensors are un-segmented n-in-p pads with  $C_d=A/w$ . (Area =L<sup>2</sup>, thickness w) Will consider a broad-band pre-amplifier (BB) which contributes the input impedance (e.g. R=50 $\Omega$ ) to the crucial time constant R\*C<sub>d</sub>. The discussion of the time measuring

circuit is left to experts like Christophe.

#### Timing error:

$$\sigma_t^2 = \sigma_{TW}^2 + \sigma_J^2 + \sigma_L^2 + \sigma_{TDC}^2$$

The pulse **slew-rate (slope) dV/dt** is the critical parameter for timing consideration For signals of many MIPs, only jitter  $\sigma_j = Noise/(slope)$  is relevant if the time measuring circuit is under control.

Note that for N concurrent MIPs, the jitter is

 $\sigma_i (N) = 1/N^* \sigma_i (MIP)$ 

The is the "root cause" for the good timing resolution in calorimeters.

# Noise and Signal in an Broad Band Amplifier (BB)

With a charge sensitive amplifiers (CSA), aka a "Capacitor", the output signal does not depend on the detector capacitance  $C_d$ , but the noise does.

With a broad-band amplifier (BB), aka a "resistor", it will be shown that the signal height depends on  $C_d$  via the R\*C at the amplifier input and that the noise is independent of the capacitance, as measured with a bias scan of the sensor.



The amplifier noise is crucial parameter, since it causes the time jitter, which can be the dominant part of the timing resolution.

Jitter = Noise/slope = Noise /(dV/dt)

#### **Thin vs Thick detectors**



Ramo's Theory predicts the detector current (the weighting field  $E_w = 1/d$  for pads):

$$i = q * v * E_w$$

The current is proportional to the sum of the velocities of all moving charges. Highest at t=0 when all charges all still moving, and v is saturated. Lower if the bias is too low such that there is a velocity gradient.



## **Charge Collection Time in Si Sensors**



E-Field  $\geq$  20 kV/cm

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For 300um Si
Collection time ~ 4 ns (h), ~ 3 ns (e)
For 50um Si
Collection time ~ 0.7 ns (h), ~ 0.5 ns (e)
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Drift velocity saturates for both electrons and holes!

-> need thin sensors or large over-depletion for fast charge collection



Nicolo Cartiglia developed a full sensor simulation to optimize the sensor design

WeightField2, F. Cenna, N. Cartiglia 9<sup>th</sup> Trento workshop, Genova 2014 Available at http://personalpages.to.infn.it/~cartigli/weightfield2

It includes many "bells & whistles" required for the detailed description of the signals, i.e. charge generation, drift and collection.

It allows to separate the properties of the current source from the amplifier shaping.

(Perfect to teach and learn about silicon sensors)



#### WeightField2: the Silicon Detector Simulator





### The Current Source: Current from 10 MIPs

In the following simulate the signal from 10 MIPs

**Total current = electron current + hole current** Drift time difference electrons-holes clearly visible

Most importantly: The current peaks at t=0 where we want to make the time measurement

The current signal of sensors with different thickness' show that the height of peak at t=0 is independent of the detector thickness! Thick detectors contribute to the tail.

The 300µm sensor is not sufficiently overdepleted to reach saturation velocity, in contrast to the thinner ones.





## **WF2 : Radiation Effects on Signal Current**

Radiation damage are increase of leakage current and trapping (loss of charge).

Since trapping is characterized by a characteristic time of ~50ps (and thus a characteristic distance of ~50µm at saturated drift velocity) only the later part of the pulse is affected which is not used for timing (unless TOT is used!).

# The current maximum at the pulse front is preserved.

A pulse from 300µm sensor resembles one from a 50µm sensor after a fluence of several 10<sup>15</sup> neq/cm<sup>2</sup>

High bias operation is preferred to decreased the drift time.



## WF2: Radiation Effects on Voltage Pulse Shape

Assume BB amplifier with  $50\Omega$  input impedance and gain of 100. Vary: pad size A: 1x1, 3x3, 5x5 mm<sup>2</sup>.sensor thickness w:100µm, 300µm This samples different detector capacitance C<sub>d</sub>-= A/w from 0.3 to 40pF

Solid State Physics Current

convolution RC  $\rightarrow$ 

Electronics Voltage





Simulations by Charilou Labitan

SCH

## WF2: Radiation Effects on Voltage Pulse Shape

The pulse height varies both with the detector capacitance and the radiation

The slew-rate (20% - 80%) depends universally on detector capacitance e.g.  $dV/dt(5mm/300\mu m) \approx dV/dt(3mm/100\mu m)$ 

No radiation effect on slew-rate dV/dt!

Prefer thinner sensors because of leakage current





# VF2 Simulation: Radiation Effects on Signal Voltage

Pad size dimension 1x1, 3x3, 5x5 mm<sup>2</sup> Sensor thickness 100μm, 300μm



Need small detector sizes, small input impedance R for RC < 100ps to get large dV/dt



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# WF2: Radiation Effects on Pulse Height and dV/dt

Pad size dimension 1,3,5 mm are indicated for 2 sensor thickness' 100µm, 300µm

Max pulse voltage depends on capacitance Small dependence on radiation level



Slew-rate dV/dt depends on the pad capacitance. Small dependence on radiation level

Need small detector sizes, small input impedance R for RC < 100ps to get large dV/dt.



# ensor Thickness Dependence of MIP: WF2 vs. Data



BB gain ~150, noise = 2 mV

Very good agreement of the WF2 simulation and the MIP MPV data.

The apparent increase of the MIP MPV with sensor thickness is caused by the sensor capacitance, not by an increase in the sensor current!

Careful investigation of the impact of various sensor-BB amp options with different RC values should be considered.

(N.B. how was the S/N in the data calculated when the noise was quoted as 2mV?)



## Conclusions

- The P-i-N diodes present a unique opportunity for fast timing given their pulse shape
- The fast rising edge of the current pulse from SiPads of all thicknesses and areas are identical with sufficient over-depletion of the sensors.
- The rising edge of the voltage pulse is shaped through the RC of the SiPad capacitance and the input impedance of the broad-band amplifier.
- Since the time jitter depends inversely on the slew-rate, lower sensor capacitances are preferred since they result in higher slew-rate.
- Concurrent pulses from N MIPs have N-times the slew rate and thus 1/N the time jitter
- The data on time resolution of multiple MIP events in SiPad presented today are reproduced by the simulations using the Weightfield2 (WF2) program.
- Given that the capacitance of the pads depends both on area and thickness of the SiPad, it opens the opportunity to optimize the geometry of the sensors depending on Physics and detector performance; e.g. 5mmx5mm, 300µm thick sensors have ≈ the same capacitance as 3mmx3mm, 100µm and thus the same slew-rate dV/dt and timing resolution.
- To match the jet size one might go with the 5mmx5mm pad, or for radiation mitigation one might opt for the thinner bulk of 100µm pads with better radiation tolerance and tighter jet confinement.
- Thinner sensor will have less leakage current and less leakage current induced noise, although with cooling and the short shaping times this might not be an unsolvable issue.
- We should look forward to see how our electronic colleagues will face the challenges to minimize noise and power as well as optimize the time measuring systems. 15