Session Program

7-10 Mar 2016

ACES 2016 - Fifth Common ATLAS CMS Electronics Workshop for LHC Upgrades

Poster

CERN, 30/7-018 - Kjell Johnsen Auditorium
Building 30/7-018 CH - 1211 Geneva 23

Tuesday 8 March

16:00

Poster
Session | **Location:** CERN, 30/7-018 - Kjell Johnsen Auditorium, Building 30/7-018 CH - 1211 Geneva 23

16:00–16:02 **The Versatile Link Demontrator Board (VLDB)**

Speaker
Raul Martin Lesma

16:02–16:04

Rad-Hard Vertical JFET switch for the HV-MUX system of the ATLAS upgrade ITk

Speaker
Dr Pablo Fernandez-Martinez

16:05–16:07 **Status update and future plans for the GBT-FPGA project at CERN**

Speaker
Julian Maxime Mendez

16:07–16:09

The use of the ATLAS Small Wheel front end Electronics for the HL-LHC MDT upgrade

Speaker
Venetios Polychronakos

16:09–16:11

Preliminary Experimental Results of A 14-bit Split-SAR ADC for ATLAS LAr Phase-II Upgrade

Speaker
Dr Yun Chiu

16:11–16:13

Timepix detector network for luminosity monitoring and characterization of the radiation fields within the ATLAS cavern

Speaker
Benedikt Ludwig Bergmann

16:13–16:15

Design of low power 12bit 40MSPS SAR ADCs with a redundancy algorithm and digital calibration for high dynamic range calorimeter readout

Speaker
Daniel Dzahini

16:15–16:17

The intelligent, FPGA-based event builder and Data Acquisition System of COMPASS – an exemplary system for the future?

Speaker
Dominik Steffen

16:17–16:19 **ATLAS LAr Phase II upgrade of the Front End Electronics**

16:30

Speaker
Mitchell Franck Newcomer

Wednesday 9 March

16:00
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16:30

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