



Magnet Temperature Measurement LHC

- Balázs Nagy
- Máté Patalenszki

NI Big Physics Summit @ CERN
Conference 2016

Electronics engineering students @
University of Debrecen

The Team

- Ádám Papp
- Balázs Nagy
- Máté Patalenszki

Undergraduate Electronics
Engineering Students

- Kornél Sarvajcz
- László Kazup

PhD Students

- Angéla Váradiné Szarka Dr

Acc Professor at Dept. of Electrical
and Electronic Engineering

- Márta Bajkó

Leader of SM18, CERN

- Maryline Charrondiere

Technician at SM18, CERN

University of Debrecen

LabVIEW Academy Institution



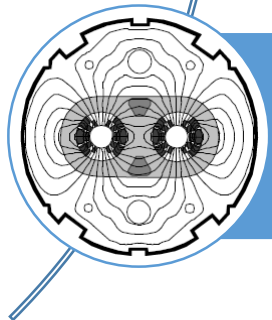
EuCARD-2



Enhanced European Coordination for
Accelerator Research & Development

WP9

13 Work Packages



MagNET@CERN

SM18 – Cryogenic Test Facility



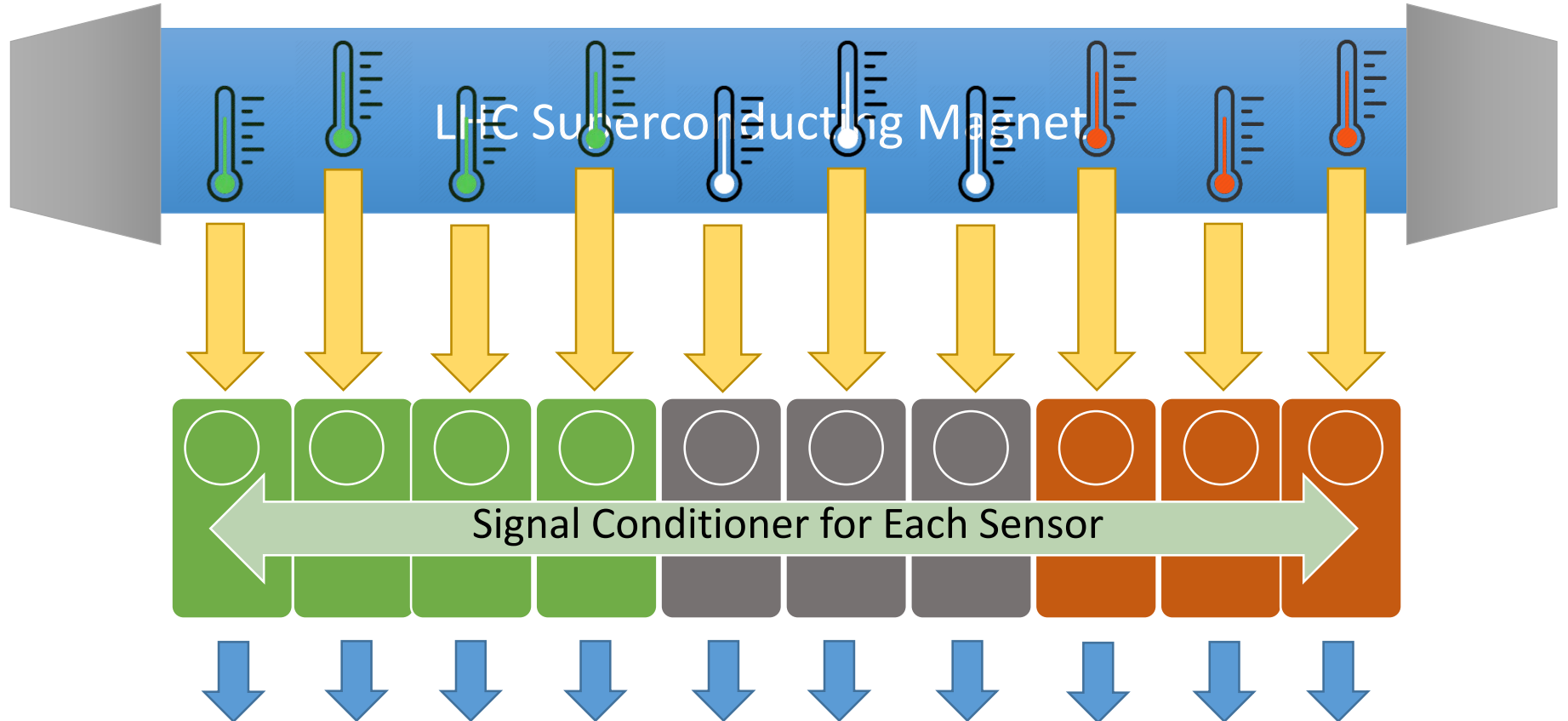
Testing superconducting magnets & instrumentation

10 test benches in horizontal or vertical position

Test on 1.9°K using up to 20kA current

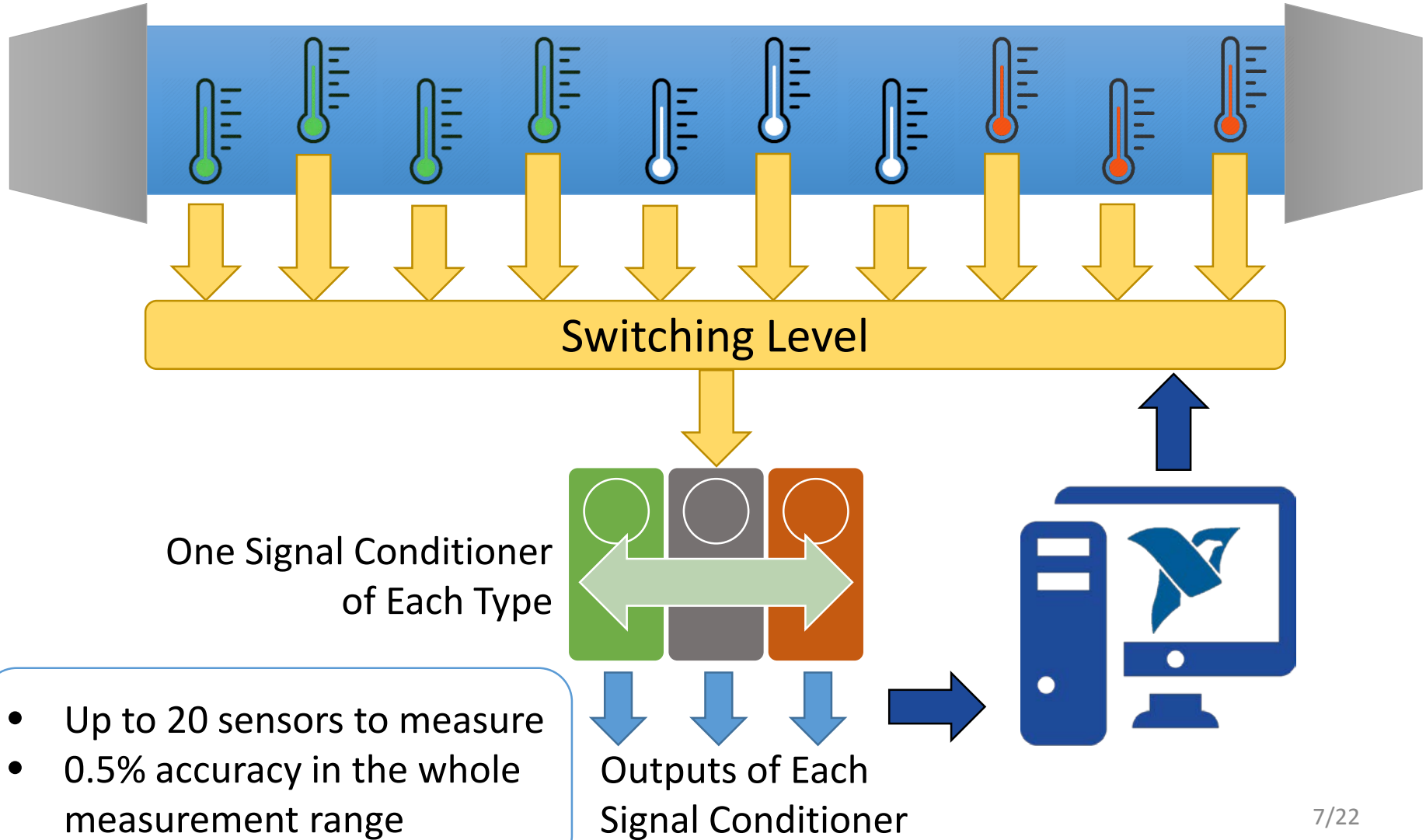
Serial and prototype testing

Testing Superconducting Magnets



- Up to 20 sensors to measure
- 0.5% accuracy in the whole measurement range

Aim of the Project



Challenges



Accuracy: 0.5% in the whole measurement range



3 different measurements on each sensors:

- CERN signal conditioners
- NI DMM 4 wire measurement
- Direct resistance measurement between each 2 wires

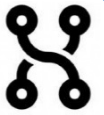


Compact & mobile instrument

NI Hardware - Requirements



Voltage, 4 wire resistance measurement



Control of the switching level



Data storage



Wireless communication



Mobility

NI Hardware - Configurations



PCIe

- PC is included in the chassis
- DAQ card
- DMM card



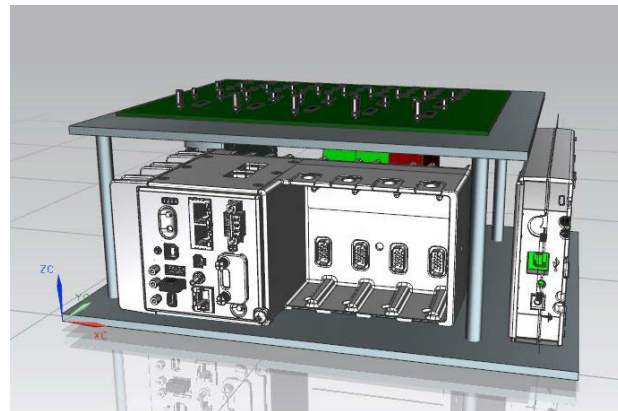
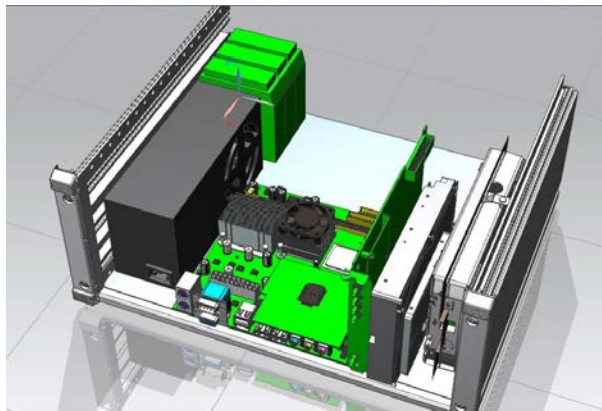
cDAQ

- Controller
- AI module
- DIO module
- DMM (USB)

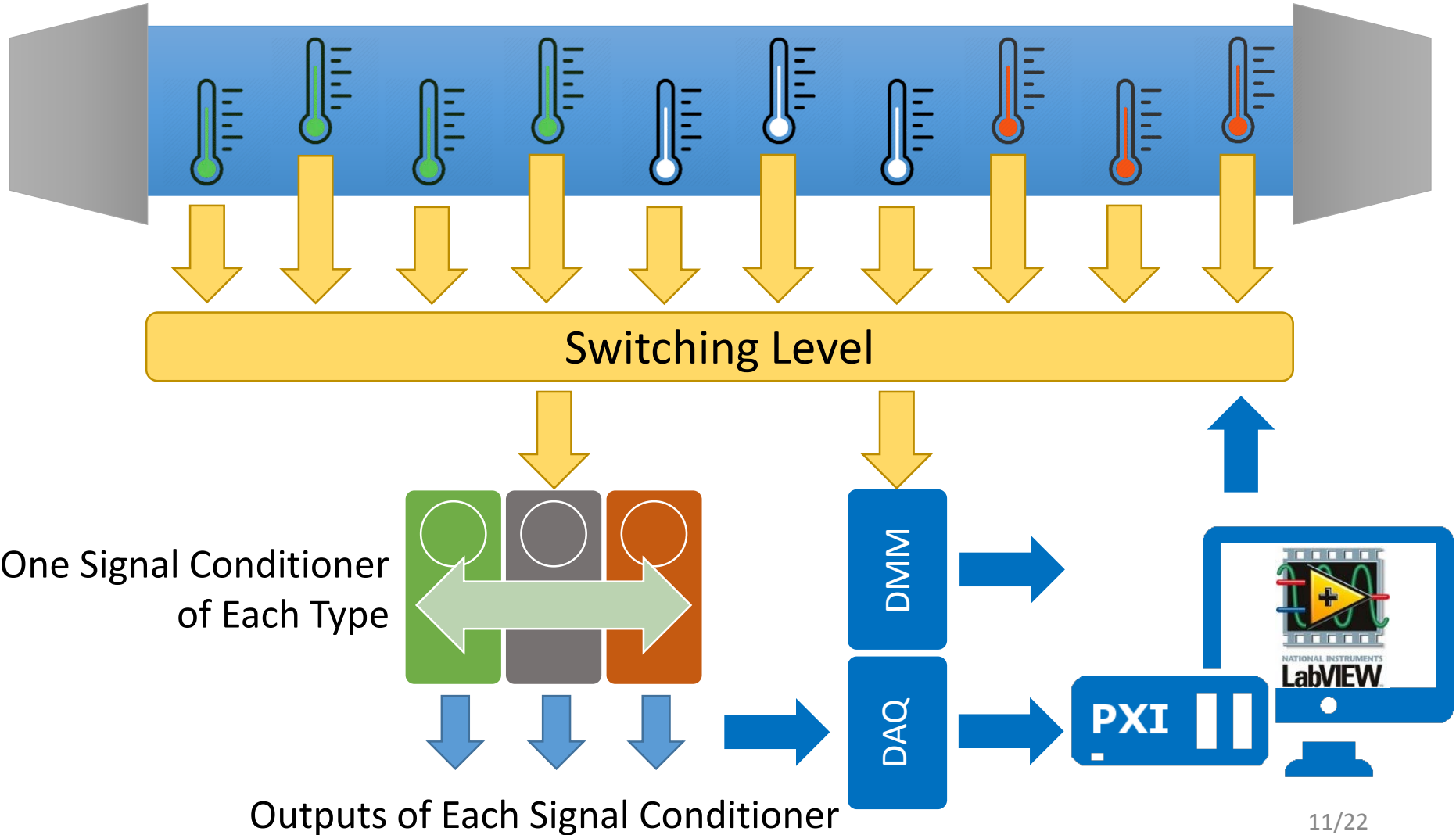


PXI

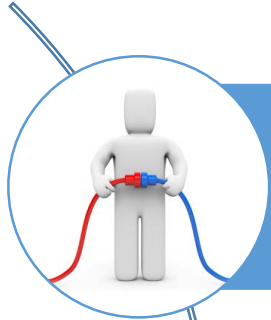
- Controller
- DAQ card
- DMM card



Block Diagram of the Instrument



Hardware – The Switching Level



Connecting sensor wires to the appropriate signal conditioner



Providing direct measurement for verification



Not exceeding 0.2% relative measurement error

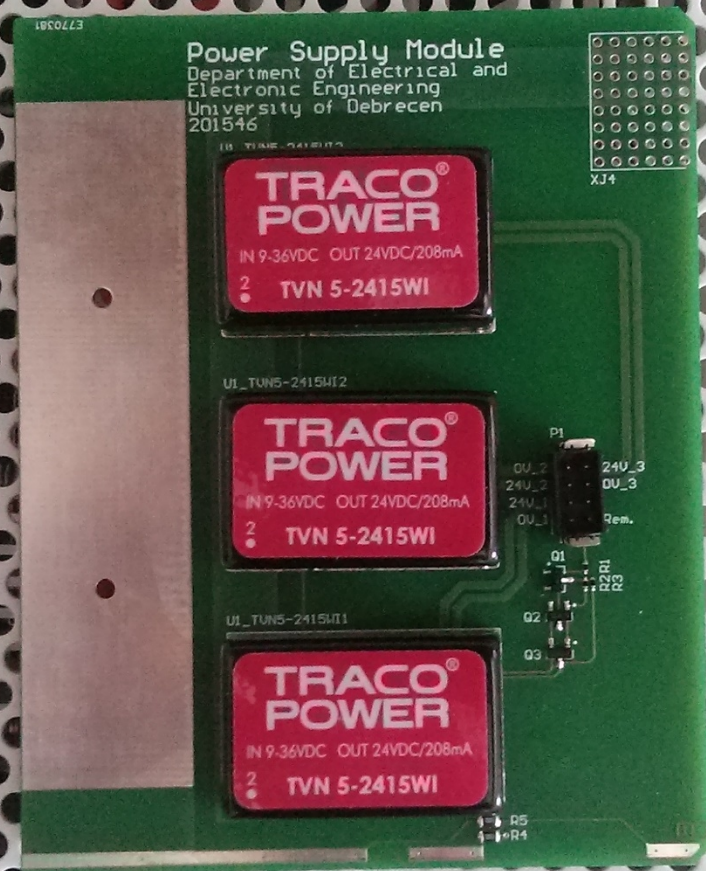
Switching Level - Specifications

Final circuit based on the same specifications as NI PXI Modules

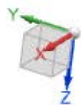
Switch caused relative error ≤ 80 ppm

Because of circuit considerations we decided to use analog switches

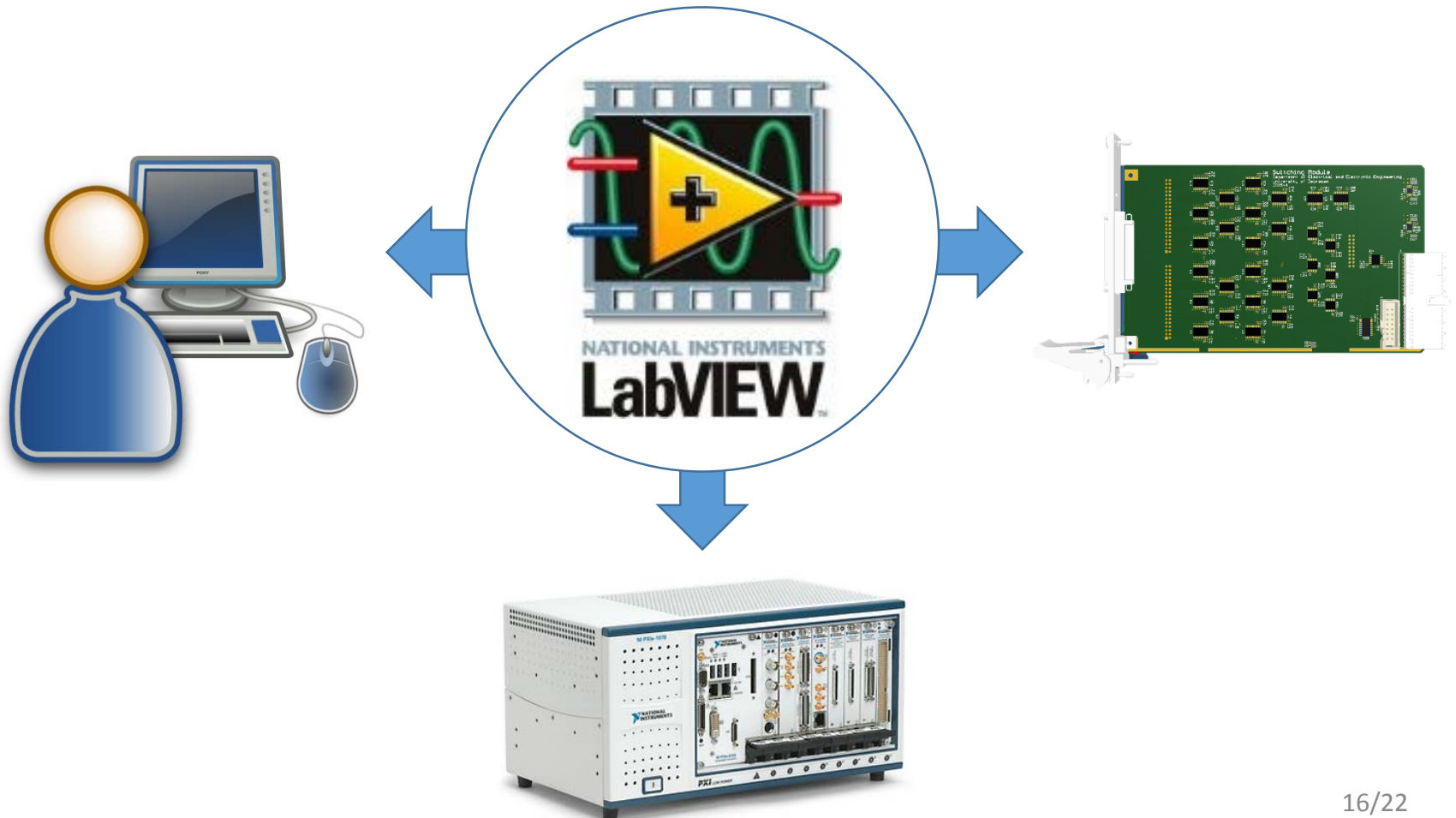
Switching Level - Realisation



Instrument Integration

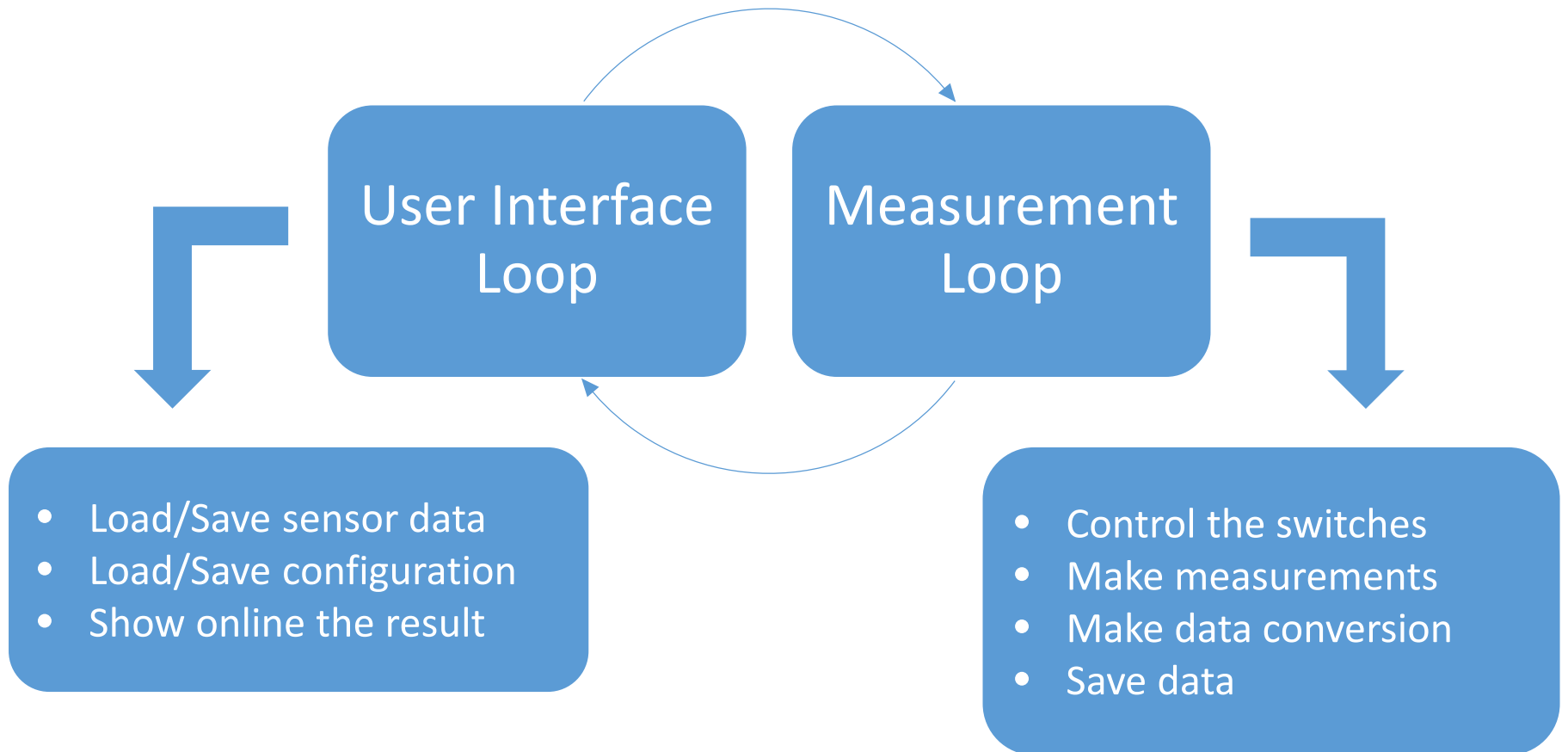


Software

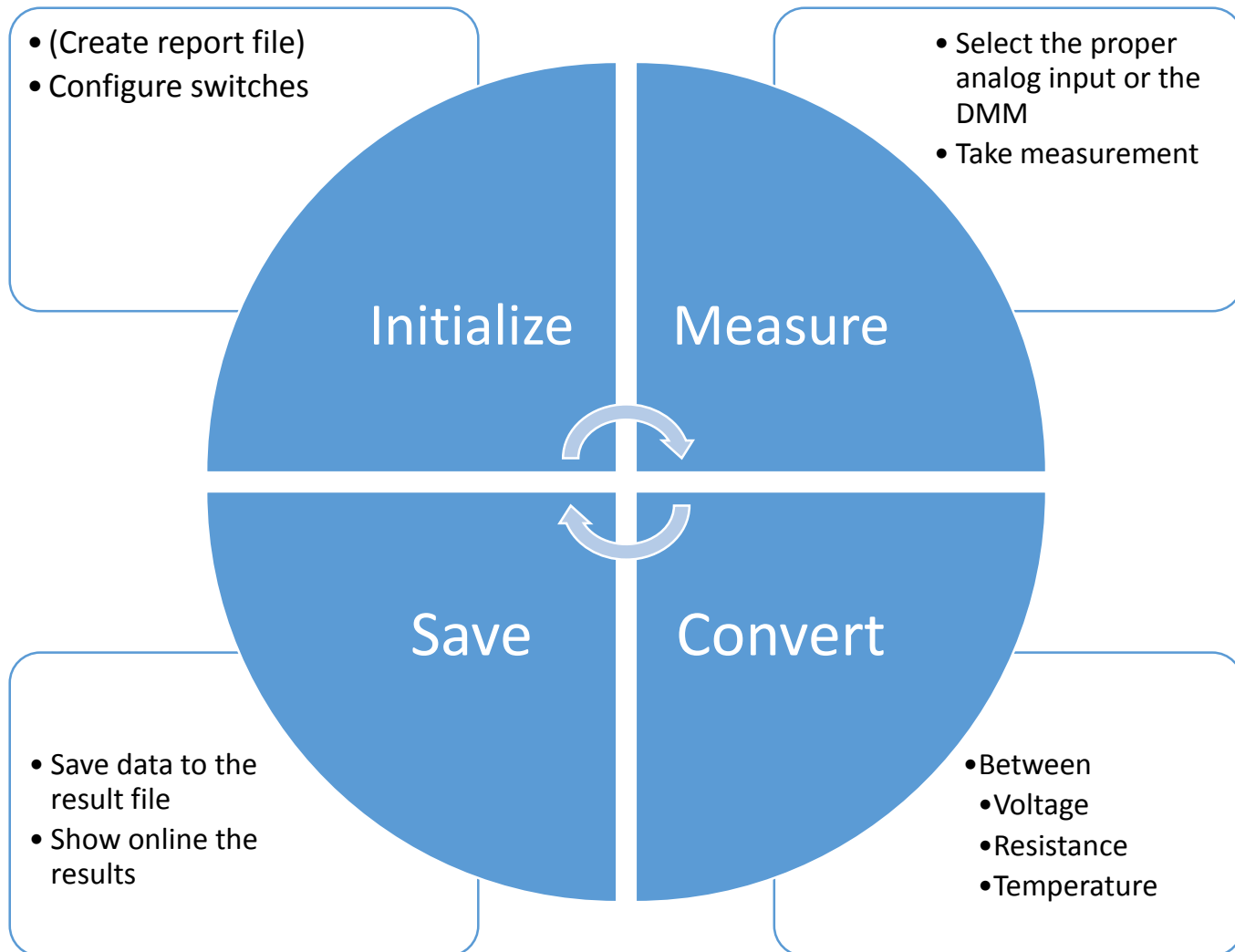


Software

Producer/Consumer Design Pattern



Software - Measurement



Summary



Acknowledgement

The research is part of the EuCARD-2 co-funded by the partners and the EC-Capacities-FP7 under Grant Agreement 312453.

Speakers would like to thank the cooperation possibility to CERN, and to express special thanks for contribution and help to Marta Bajko, Maryline Charrondiere, Maria de Fatima Gomez de la Cruz, Huber Raymond.

Also the team wishes to express their appreciation to NI Hungary for accepting and hosting this project in the Open Laboratory, and personal thanks to Botond Barabás who has been done his best to provide all the necessary hardware for research, development and tests.

Thank you for your attention