

Session Program

3-4 Mar 2009

ACES 2009

Power distribution, low power design, power simulation (cont.)

CERN, 500/1-001 - Main Auditorium

Tuesday 3 March

14:00

Power distribution, low power design, power simulation (cont.)

Session | Location: CERN, 500/1-001 - Main Auditorium

14:00-14:15 Buck DC-DC design and implementation

Speaker

Stefano Michelis

14:15-14:30

Development and system Tests of DC-DC converters for the CMS SLHC Tracker

Speaker

Waclaw Karpinski

14:30-14:45 Embedded switched capacitors DC-DC

Speaker

Mr Beat Meier

14:45-15:00 The serial Power interface chip

Speaker

Marcel Trimpl

15:00-15:20 Serial power circuitry in the ABC-Next and FE-I4 chips

Speaker

Wladyslaw Dabrowski

15:20-15:40 Discussion

15:40