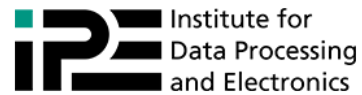


Session Program

26-30 Sept 2016



TWEPP 2016 - Topical Workshop on Electronics for Particle Physics

ASIC

Karlsruhe Institute of Technology (KIT), Tulla Lecture Hall (Building 11.40)
Kaiserstraße 12 76131 Karlsruhe GERMANY

Tuesday 27 September

09:50

ASIC

Session | **Location:** Tulla Lecture Hall (Building 11.40) | **Convener:** Christophe De La Taille

09:50–10:15

Development of the ABCStar front-end chip for the ATLAS Silicon Strip Upgrade

Speaker

Weiguo Lu

10:15–10:40

SALT Readout ASIC for Upstream Tracker in the Upgraded LHCb Experiment

Speaker

Krzysztof Piotr Swientek

10:40

11:10

ASIC

Session | **Location:** Tulla Lecture Hall (Building 11.40) | **Convener:** Christophe De La Taille

11:10–11:35

First results of the front-end ASIC for the strip detector of the PANDA MVD

Speaker

Tommaso Quagli

11:35–12:00

A Prototype of a New Generation Readout ASIC in 65nm CMOS for Pixel Detectors at HL-LHC

Speaker

Andrea Paterno

12:00–12:25

A synchronous analog very front-end in 65nm CMOS with local fast ToT encoding for pixel detectors at HL-LHC

Speaker

Ennio Monteil

12:25

14:50

ASIC

Session | **Location:** Tulla Lecture Hall (Building 11.40) | **Convener:** Alex Grillo

14:50–15:15

IC-PIX28: Pixel Detectors Read-Out in Bulk-CMOS 28nm

Speaker

Federica Resta

15:15–15:40

HEPS-BPIX, the hybrid pixel detector system for High Energy Photon Source in China

Speaker

Wei Wei

15:40-16:05

Development of a monolithic pixel detector with SOI technology for ILC vertex detector

Speaker

Miho Yamada

16:05

Wednesday 28 September

09:50

ASIC

Session | **Location:** Tulla Lecture Hall - Build 11.40 | **Convener:** Marcus Julian French

09:50–10:15 **SAMPA Chip: the New ASIC for the ALICE TPC and MCH Upgrades**

Speaker

Marco Bregant

10:15–10:40 **SKIROC2_CMS : an ASIC for testing CMS HGAL**

Speaker

Christophe De La Taille

10:40

11:10

ASIC

Session | **Location:** Tulla Lecture Hall - Build 11.40 | **Convener:** Marcus Julian French

11:10–11:35

HARDROC3, a 3rd generation ASIC with zero suppress for ILC Semi Digital Hadronic Calorimeter

Speaker

Stephane CALLIER

11:35–12:00

PETIROC2A, a 32-channel 20 GHz GBW readout ASIC for accurate time resolution and precise charge measurements

Speaker

Christophe De La Taille

12:00–12:25

First experimental results with TOFPET2 ASIC

Speaker

Agostino Di Francesco

12:25

14:50

ASIC

Session | **Location:** Tulla Lecture Hall - Build 11.40 | **Convener:** Alex Grillo

14:50–15:15

A fast, ultra-low power 10-bit SAR ADCs in CMOS 130 nm technology

Speaker

Dr Jakub Moron

15:15–15:40

MATRIX: a 15 ps resistive interpolation TDC ASIC based on a novel regular structure

Speaker

Juan Mauricio Ferre

15:40–16:05

Developments of two 4 × 10-Gbps radiation-tolerant VCSEL array drivers in 65 nm CMOS

16:05

Speaker
Datao Gong

Thursday 29 September

09:50

ASIC

Session | **Location:** Tulla Lecture Hall (Building 11.40) | **Convener:** Jean-Pierre Cachemiche

09:50–10:15

A 1.2 Gb/s Data Transmission Unit in CMOS 0.18 μm technology for the ALICE Inner Tracking System front-end ASIC.

Speaker

Giovanni Mazza

10:15