

TWEPP 2016 - Topical Workshop on Electronics for Particle Physics

Tuesday 27 September 2016

POSTER: Session 1 - Building 11.40 Room 014 (16:30 - 18:30)

-Conveners: Ken Wyllie

time	[id] title	presenter
16:30	[151] Development of Clock-Data Recovery circuit, Serializer and CML Driver in 65nm CMOS for HL-LHC pixel readout chip	RYMASZEWSKI, Piotr
16:31	[13] 6-Bit Low Power Area Efficient SAR ADC for CBM MUCH ASIC	SHUMIKHIN, Vitaly
16:32	[53] Design of a Depleted Monolithic CMOS Sensor in a 150 nm CMOS Technology for the ATLAS Inner Tracker Upgrade	WANG, Tianyang
16:33	[33] Characterization of ALICE SAMPA ASIC Using Prototype GEM Detector for LHC Run3 and Beyond	TAMBAVE, Ganesh Jagannath
16:34	[75] Performance of CATIROC: ASIC for Smart Readout of Large Photomultiplier Arrays	CONFORTI DI LORENZO, Selma
16:35	[108] Characterization of SLVS Driver and Receiver in a 65 nm CMOS Technology for High Energy Physics Applications	DE CANIO, Francesco
16:36	[58] Development of Radiation-Hard Bandgap Reference Circuit in CMOS 130 nm Technology	KUCZYNSKA, Marika
16:38	[141] HVCMOS Sensors for the High Luminosity Upgrade of ATLAS Experiment: The Second Generation of Prototypes and their Electronic Blocks	Mr BLANCO, Roberto
16:39	[166] LDQ10P: A Compact Low-Power 4x10 Gb/s VCSEL Driver Array IC	Mr ZENG, Zhiyao
16:40	[81] Readout Electronics for Silicon Micro-Strip Sensors	ALONSO CASANOVAS, Oscar
16:41	[32] A New Readout Electronics for the LHCb Muon Detector Upgrade.	CASU, Luigi CADEDDU, Sandro
16:42	[7] 65nm Receiver with Decision Feedback Equalization for Radiation Hard Data Link at 5Gbps	WALLANGEN, Veronica
16:44	[162] A Fully Monolithic HV-CMOS Pixel Detector with a Time-to-Digital Converter for Nanosecond Time Measurements	CASANOVA MOHR, Raimon
16:45	[134] Development of 32-Channel System for Processing Asynchronous Data from the CBM GEM Detectors	ATKIN, Eduard
16:56	[31] Simulation Environment Based on the Universal Verification Methodology	FIERGOLSKI, Adrian
16:57	[49] SPIDR, a General-Purpose Readout System for Pixel ASICs	VAN DER HEIJDEN, Bas
16:58	[74] The ARAGORN Front-End - FPGA Based Implementation of a Time-to-Digital Converter	SCHAFFER, Carl
16:59	[1] Single Event Effects Mitigation with TMRG Tool	KULIS, Szymon
17:00	[96] Versatile ASIC and Protocol Tester for STS/MUCH-XYTER2 in CBM Experiment	Dr ZABOLOTNY, Wojciech Marek
17:11	[158] Flex Based Data and Power Transmission for the ATLAS Pixel Upgrade	RUMMLER, Andre

17:12	[42] A System-Level Model for High-Speed, Radiation-Hard Optical Links in HEP Experiments Based on Silicon Mach-Zehnder Modulators	ZEILER, Marcel
17:23	[100] Development of a Rest Gas Ionisation Profile Monitor for the CERN Proton Synchrotron Based on a Timepix3 Pixel Detector	LEVASSEUR, Swann
17:24	[118] HDI Flexible Front-End Hybrid Prototype for the PS Module of the CMS Tracker Upgrade	KOVACS, Mark Istvan
17:25	[102] Properties of Thin Polyurethane Wire Bond Coatings after Irradiation	IZEN, Joseph Michael
17:36	[106] Characterization of Radiation Effects in 65nm Digital Circuits with the DRAD Digital Radiation Test Chip	JARA CASAS, Luis Miguel
17:37	[170] Component Qualification for the Mu2E Calorimeter Waferform Digitizer	SPINELLA, Franco
17:47	[56] The Versatile Link Demonstrator Board (VLDB)	MARTIN LESMA, Raul
17:48	[57] The MuPix Telescope – A Thin, High Rate Particle Tracking Telescope	HUTH, Lennart
17:49	[138] Design Studies for the Phase II Upgrade of the CMS Barrel Electromagnetic Calorimeter	BORNHEIM, Adolf
17:50	[62] Prototype Readout Electronics for the ALICE Inner Tracking System	SIELEWICZ, Krzysztof Marek
17:51	[149] Testing and Integration of the Service Cylinders for the CMS Phase 1 Pixel	NGADIUBA, Jennifer
17:52	[83] The Common Data Acquisition Platform in the Helmholtz Association	KAEVER, Peter
17:53	[103] Phase 1 Upgrade of the CMS Drift Tubes Read-Out System	NAVARRO TOBAR, Alvaro
17:54	[112] Upgrades to the CSC Cathode Strip Chamber Electronics for HL-LHC	WOOD, Darien
17:55	[8] Electronics for the RICH Detectors of the HADES and CBM Experiments	MICHEL, Jan
17:56	[84] A New Profibus-DP Slave Interface Card for CERN's Vacuum Sector Valve Controller	PIGNY, Gregory
17:57	[139] Precision Timing with PbWO Crystals and Prospects for a Precision Timing Upgrade of the CMS Barrel Electromagnetic Calorimeter at HL-LHC	CIRIOLO, Vincenzo
17:58	[144] Phase 1 Upgrade of the CMS Forward Calorimeter	NOONAN, Daniel
18:08	[23] The Address in Real Time Data Driver Card for the Micromegas Detector of the ATLAS Muon Upgrade	YAO, Lin
18:18	[159] Performance and Advantages of a Soft-Core Based Parallel Architecture for Energy Peak Detection in the Calorimeter Level 0 Trigger for the NA62 Experiment at CERN	FEDERICI, Luca
18:19	[155] Performance and Operation of the Calorimetric Trigger Processor of the NA62 Experiment at CERN SPS	DE SIMONE, Nico BONAIUTO, Vincenzo
18:20	[163] Development of Network Interface Cards for TRIDAQ Systems with the NaNet Framework	LONARDO, Alessandro
18:21	[131] L-1 Trigger System for Electromagnetic Calorimeter of COMET Experiment	EPSHTEYN, Leonid
18:22	[129] ALICE Trigger System in RUN3	PEREZ MORENO, Luis Alberto
18:23	[145] FPGA Based Algorithms for the New Trigger System for the Phase 2 Upgrade of the CMS Drift Tubes Detector	NAVARRO TOBAR, Alvaro
18:24	[69] A High Bandwidth and Versatile Advanced MC Board, TRB_v1	YANG, Yifan
18:25	[135] Study of Hardware Implementation of Fast Tracking Algorithms	Ms SONG, Zixuan
18:26	[2] The Level-1 Tile-Muon Trigger in the Tile Calorimeter Upgrade Program	RYZHOV, Andrey

18:27	[97] Implementation of the data acquisition system for the Overlap Modular Track Finder in the CMS experiment	Dr ZABOLOTNY, Wojciech
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Wednesday 28 September 2016

POSTER: Session 2 - Building 11.40 Room 014 (16:30 - 18:30)

-Conveners: Ken Wyllie

time	[id] title	presenter
16:30	[18] Readout Channel with Majority Logic Timestamp and Digital Peak Detector for Muon chambers of the CBM Experiment	MALANKIN, Evgeny
16:31	[164] Front-End and Back-End Solutions in the CBM STS Readout ASIC	KASIŃSKI, Krzysztof
16:32	[94] ASPIC and CABAC: Two ASICs to Readout and Pilot CCD	JEGLOT, Jimmy
16:33	[11] SLVS Transmitter and Receiver for Readout ASIC	BULBAKOV, Ivan
16:34	[147] A Low-Power 10 Gbps Serial Link Transmitter ASIC for Particle Detectors in 65nm CMOS	Prof. CHEN, Jinghong
16:35	[124] A Low-Power and Low Transmission Latency Dual Channel Serializer ASIC for Detector Front-End Readout	GONG, Datao
16:36	[99] Integrated Input Protection Against Discharges for Micro Pattern Gas Detectors Readout ASICs	Dr FIUTOWSKI, Tomasz Andrzej
16:37	[150] MGPA++ A Pre-Amplifier for CMS Barrel ECAL at HL-LHC	ZAHID, Sema
16:38	[161] A Temperature Compensated Triple-path PLL for DUNE Experiments	Mr LIU, Tianwei
16:39	[79] uTRiG: A Mixed Signal Silicon Photomultiplier Readout ASIC for Ultra-Fast Timing and Ultra-High Rate Applications	CHEN, Huangshan
16:40	[59] Pixel Architectures in HV/HR CMOS Process for ATLAS Inner Detector Upgrade	DEGERLI, Yavuz
16:41	[46] Multiple Use SiPM Integrated Circuit (MUSIC) for SiPM Anode Readout	FERNANDEZ, Gerard
16:42	[148] A Low-Power 10-bit 250-MS/s Dual-Channel Pipeline ADC in 0.18 μm CMOS	Prof. CHEN, Jinghong
16:43	[91] Characterization of the Column-Based Priority Logic Readout of Topmetal-II- CMOS Pixel Direct Charge Sensor	Mr ZHANG, Wei
16:54	[26] 2.5Gb/s Simple Optical Wireless Communication System for Particle Detectors in High Energy Physics	ALI, Wajahat
16:55	[47] Versatile Transceiver Production and Quality Assurance	OLANTERA, Lauri Juhani
17:06	[130] First Implementation of a Two-Stage DC-DC Conversion Powering Scheme for the CMS Phase-2 Outer Tracker	KLEIN, Katja
17:07	[120] Spotting and Curing Noise Issues in the Silicon Vertex Detector of the Belle II Experiment	THALMEIER, Richard
17:08	[160] An Advanced Power Analysis Methodology Targeted to the Optimization of a Digital Pixel Readout Chip Design and its Critical Serial Powering System	MARCONI, Sara
17:09	[140] High Precision, Low Disturbance Calibration of the High Voltage System of the CMS Barrel Electromagnetic Calorimeter	FASANELLA, Giuseppe
17:10	[36] System-Level Considerations of the Front-End Readout ASIC in the CBM Experiment from the Power Supply Perspective	Dr KASIŃSKI, Krzysztof
17:21	[89] Testing of Hybrid Circuits for the CMS Tracker Upgrade of Front-End Electronics	GADEK, Tomasz
17:22	[173] Test Strategies for Industrial Testers for Converter Controls Equipment	OLENIUK, Patryk Wojciech

17:23	[30] Lessons Learned in High Frequency Data Transmissions Design	SULLIVAN, Stephanie W
17:24	[40] A PCI DAQ Board Prototype after the ATLAS Pixel Detector IBL-Layer 1 and 2 ROD Cards	LAMA, Luca
17:35	[169] Design of a Radiation Tolerant System for Total Ionizing Dose Monitoring Using Floating Gate and RadFET Dosimeters	FERRARO, Rudy
17:36	[28] Adaption of Low Cost Safety COTS MCU For Low Level Radiation Applications in Accelerator Facilities	LUCIO, Antonio
17:37	[43] Radiation Hardened by Design, Low Jitter, 2.56 Gbps LVDS/SLVS Based Receiver in 65 nm CMOS	FAES, Bram
17:48	[128] Integration and Testing of the DAQ System for the CMS Phase 1 Pixel Upgrade	AKGUN, Bora
17:49	[136] Precision Electronics for a System of Custom MCPs in the TORCH Time of Flight Detector	GAO, Rui
17:50	[86] ATLAS Phase-II-Upgrade Pixel Data Transmission Development	WENSING, Marius
17:52	[107] A Versatile Small Form Factor Twisted-Pair TFC FMC for mTCA AMCs	MEDER, Lukas
17:53	[77] A Universal FMC-Based DAQ System	Mr JEGLOT, Jimmy
17:54	[109] Digital Readout Board for CMS and TOTEM Precision Proton Spectrometer Timing Upgrade Project	CENNA, Francesca
17:55	[142] The Next Generation Front-End Controller for the Phase 1 Upgrade of the CMS Hadron Calorimeters.	COSTANZA, Francesco
17:56	[146] Design of an AdvancedTCA board Management Controller Solution	MENDEZ, Julian Maxime
17:57	[88] ALICE Inner Tracking System Readout Electronics Prototype Testing with the CERN "Giga Bit Transceiver"	SCHAMBACH, Joachim
17:58	[119] A Silicon Strip Telescope for Prototype Sensor Characterisation Using Particle Beam and Cosmic Rays	FU, Jinlin
17:59	[5] Web-Based DAQ Systems: Connecting the User and Electronics Front-Ends	LENZI, Thomas
18:10	[143] The CMS Electron and Photon Trigger for the LHC Run 2	NABARUN, Dev
18:11	[104] Design and Performance of the Phase I Upgrade of the CMS Global Trigger	WITTMANN, Johannes
18:12	[165] Readout and Trigger for the AFP Detector at ATLAS Experiment	KOCIAN, Martin
18:13	[78] A Prototype for an Artificial Retina Processor Aimed at Reconstructing Tracks at the LHC Crossing Rate	CENCI, Riccardo
18:14	[172] Pulsar IIb Design, System Integration and Next-Generation Full Mesh ATCA Backplane Test Results	XU, Zijun
18:15	[127] Processing of the Liquid Xenon Calorimeter's Signals for Timing Measurements	EPSHTEYN, Leonid
18:16	[125] A Neural Network on FPGAs for the z-Vertex Track Trigger in Belle II	BAEHR, Steffen
18:17	[24] Tile Rear Extension Module for the Phase-I Upgrade of the ATLAS L1Calo PreProcessor System	ANDREI, Victor
18:18	[122] A High-Speed DAQ Framework for Future High-Level Trigger and Event Building Clusters	CASELLE, Michele