

TWEPP-09 Topical Workshop on Electronics for Particle Physics

Tuesday 22 September 2009

MUG (17:30 - 19:00)

time	[id] title	presenter
17:30	[169] Access to ASIC Design Tools and Foundry Services at CERN for SLHC	KLOUKINAS, Kostas
17:45	[178] Mixed-Signal Challenges and Solutions for advanced process nodes	DUTREY, Bruno
18:15	[179] Digital Block Implementation Methodology for a 130nm process	BONACINI, Sandro
18:45	[180] Discussion	KLOUKINAS, Kostas