TWEPP-09 Topical Workshop on Electronics for Particle Physics

Thursday, 24 September 2009

POSTERS SESSION (16:15 - 18:30)

[id] title	presenter	board
[123] OMEGAPIX : 3D electronics chip for pixel readout	Mr THIENPONT, Damien	
[135] Study Radiation Hardness Performance of PiN diodes for the ATLAS Pixel Detector at SLHC	Mr ABI, Babak	
[45] Error-free 10.7 Gb/s digital transmission over 2 km optical link using an ultra-low-voltage electro-optic modulator	Prof. MESCHINI, Marco	
[29] A Zero Suppression Micro-Circuit for Binary Readout CMOS Pixel Sensors	Mr HIMMI, Abdelkader	
[115] Characterization of Semiconductor Lasers for Radiation Hard High Speed Transceivers	Mr SILVA, Sergio	
[19] A Radiation Tolerant 4.8 Gb/s Serializer for the Giga-Bit Transceiver	Dr COBANOGLU, Ozgur	
[4] Commissioning of the CSC Level 1 Trigger Optical Links at CMS	MATVEEV, Mikhail	
[18] Detector Control System for the Electromagnetic calorimeter in CMS Experiment	LESHEV, Georgi	
[131] A Prototype Front-End Readout Chip for Silicon Microstrip Detectors Using an Advanced SiGe Technology	Dr GRILLO, Alexander A.	
[136] DC-DC switching converter based power distribution vs Serial power distribution: EMC strategies for SLHC tracker up-grade	Dr ARTECHE, Fernando	
[134] Interference coupling mechanisms in Silicon Strip Detector – FEE. -CMS tracker “wings”: A leaned lesson for SLHC-	ARTECHE, Fernando	
[139] Development and commissioning of the ALICE pixel detector control system	Mr BORTOLIN, Claudio	
[138] Upgrade of the BOC for the ATLAS Pixel Insert-able B-Layer	DOPKE, Jens	
[24] The ATLAS ReadOut System - improved performance with the switch-based architecture	Mr SCHROER, Nicolai	
[25] Development of a high resolution transient recorder	Mr SCHOPFERER, Sebastian	
[20] Novel charge sensitive amplifier design methodology suitable for large detector capacitance applications	Dr NOULIS, Thomas	
[21] Readout and Data Processing Electronics for the Super-Belle Silicon Vertex Detector	Dr FRIEDL, Markus	
[28] e-link: A Radiation-Hard Low-Power Electrical Link for Chip-to-Chip Communication	Dr BONACINI, Sandro	
[122] Radiation hardness studies of a 130 nm Silicon Germanium BiCMOS technology with a dedicated ASIC	Mr DÍEZ, Sergio	
[125] Design and measurements of 10 bit pipeline ADC for the Luminosity Detector at ILC	Mr KULIS, Szymon	
[127] A 10-bit 40MS/s pipelined ADC in a 0.13µm CMOS process	Mr FRANCA SANTOS, Hugo	
[91] A self triggered amplifier/digitizer chip for CBM	Mr ARMBRUSTER, Tim	

[58] Measurement of the performances of a Low-Power Multi-Dynamics Front-End for Neutrino Underwater Telescope Optical Modules	Dr SIPALA, Valeria	
[57] The Control System for a new Pixel Detector at the sLHC	Mrs BOEK, Jennifer	
[51] High-Speed Serial Optical Link Test Bench Using FPGA with Embedded Transceivers	Ms XIANG, Annie	
[50] The design of a low power, high speed phase locked loop	Mr LIU, Tiankuan	
[52] Development of a 16:1 serializer for data transmission at 5 Gbps	Mr GONG, Datao	
[114] Presentation of the “ROC” chips readout	Mr CALLIER, Stéphane	
[116] Position Measurements with Micro-Channel Plates and Transmission Lines using Pico-second Timing and Waveform Analysis	Dr GENAT, Jean-Francois	
[110] Hardware studies for the upgrade of the ATLAS Central Trigger Processor	Mr HAAS, Stefan	
[113] Silicon Photomultiplier integrated readout chip (SPIROC) for the ILC: characterization and mesurements	Mr CALLIER, Stépahne	
[112] ASPIC: LSST camera readout chip Comparison between DSI and C&S	Mr WICEK, François	
[80] An FPGA-based Emulation of the G-Link Chip-Set for the ATLAS Level-1 Barrel Muon Trigger	Dr GIORDANO, Raffaele	
[87] A new paradigm using GPUs for fast triggering and pattern matching at the CERN experiment NA62	COLLAZUOL, Gianmaria	
[109] The Fast Tracker Architecture for the LHC baseline luminosity	Mr VOLPI, Guido	
[102] A 40 MHz trigger-free readout architecture for the LHCb experiment	ALESSIO, Federico	
[103] The Prompt Trigger of the Silicon Pixel Detector for the ALICE Experiment	Ms CAVICCHIOLI, Costanza	
[39] A programmable 10 Gigabit injector for the LHCb DAQ and its upgrade	Mr DELORD, Vincent Pierre	
[33] Wafer Screening of ABCN-25 readout ASIC	Mr PHILLIPS, Peter	
[30] A Digitally Calibrated 12 bits 35 MS/s Pipelined ADC with a 32 input multiplexer for CALICE Integrated Readout	Mr RARBI, Fatah	
[37] Standalone radiation monitors for electronics in High Energy Physics	Dr WIJNANDS, Thijs	
[36] On-chip Phase Locked Loop (PLL) design for clock multiplier in CMOS Monolithic Active Pixel Sensors (MAPS)	Dr VALIN, Isabelle	
[35] Charge Pump Clock Generation PLL for the Data Output Blocks of the Upgraded ATLAS Front-End in 130nm CMOS	KRUTH, Andre Konrad	
[34] A 12 µm pitch CMOS Pixel Sensor Designed in the 3DIT for the ILC Vertex Detector	Mr FU, Yunan	
[63] ATLAS Silicon Microstrip Tracker Operation	Dr VANKOV, Peter	
[64] The GBT-SCA, a radiation tolerant ASIC for detector control applications in SLHC experiments	Dr GABRIELLI, Alessandro	
[68] Upgrade of the Cold Electronics of the ATLAS HEC Calorimeter for sLHC: Generic Studies on Radiation Hardness and Temperature Dependence.	Ms RUDERT, Agnes	
[6] A facility and a web application for real-time monitoring of the TTC backbone status	Mr JURGA, Piotr	
[11] A low-cost multi-channel analogue signal generator	Mr MÜLLER, Felix	
[10] CMD-3 First Level Trigger Infrastructure	Mr KOZYREV, Alexey	
[44] An integrated DC-DC step-up charge pump and step-down converter in 130 nm technology	Mr BOCHENEK, Michal	

[43] ALICE TPC CONTROL AND READOUT SYSTEM	LARSEN, Dag Toppe	
[5] Simple parallel stream to serial stream converter for Active Pixel Sensor readout.	KUSHPIL, Vasilii	
[146] Total dose effects on a deep-submicron SOI technology for Monolithic Pixel Sensor development	Mrs MATTIAZZO, Serena	
[147] AFTER, the Front-End ASIC of the T2K Time Projection Chambers	Mr DELAGNES, Eric	
[73] Buffer Control Chip (BCC) for the ATLAS Tracker Upgrade	Mr WARREN, Matt	
[144] Electronic development for the upgrade of the LHCb Vertex detector.	Mr BUYTAERT, Jan	
[141] The Online Error Control and Handling of the ALICE Pixel Detector	Dr CASELLE, Michele	
[79] DEPFET Mini-matrix Readout System	Mr SCHEIRICH, Jan	
[41] Low power discriminator for ATLAS pixel chip	MENOUNI, Mohsine	
[47] Design of the CMS-CASTOR sub detector readout system by reusing existing designs	Mr BEAUMONT, Wim	