

Session Program

21-25 Sept 2009

TWEPP-09 Topical Workshop on Electronics for Particle Physics

Plenary Session 6 - Programmable Logic, Boards, Crates and Systems

Institut des Cordeliers 15, rue de l'Ecole de Médecine (Métro Odéon) Paris, France

Friday 25 September

09:00

Plenary Session 6 - Programmable Logic, Boards, Crates and Systems

Session | Location: Institut des Cordeliers 15, rue de l'Ecole de Médecine (Métro Odéon) Paris, France

09:00-09:45

Recent Advances in Architectures and Tools for Complex FPGA-based Systems (Invited Talk)

Speaker

Prof. Michael Schulte

09:45-10:10

A flash high-precision Time-to-Digital Converter implemented in FPGA technology

Speaker

Dr Salvatore Loffredo

10:10-10:35

Implementing the GBT data transmission protocol in FPGAs

Speaker

Mr Frederic MARIN

10:35-11:00

FPGA-based Bit-Error-Ratio Tester for SEU-hardened Optical Links

Speaker

Mr Csaba Soos

11:00