



AGH UNIVERSITY OF SCIENCE  
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# SALT Chip Testing

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## Ideas and open questions

- Who is responsible for testing?
- Difference between prototype and volume production tests
- Wafer screening system → time per ASIC
- Flow of tests
  - Power consumption measurement
  - FE&ADC tests → gain, offset, noise performance
  - DACs characteristics
  - Digital blocks tests → full functionality
- Flow of data → yield analysis