



# RapidIO as a multi-purpose interconnect

Presentation Highlights

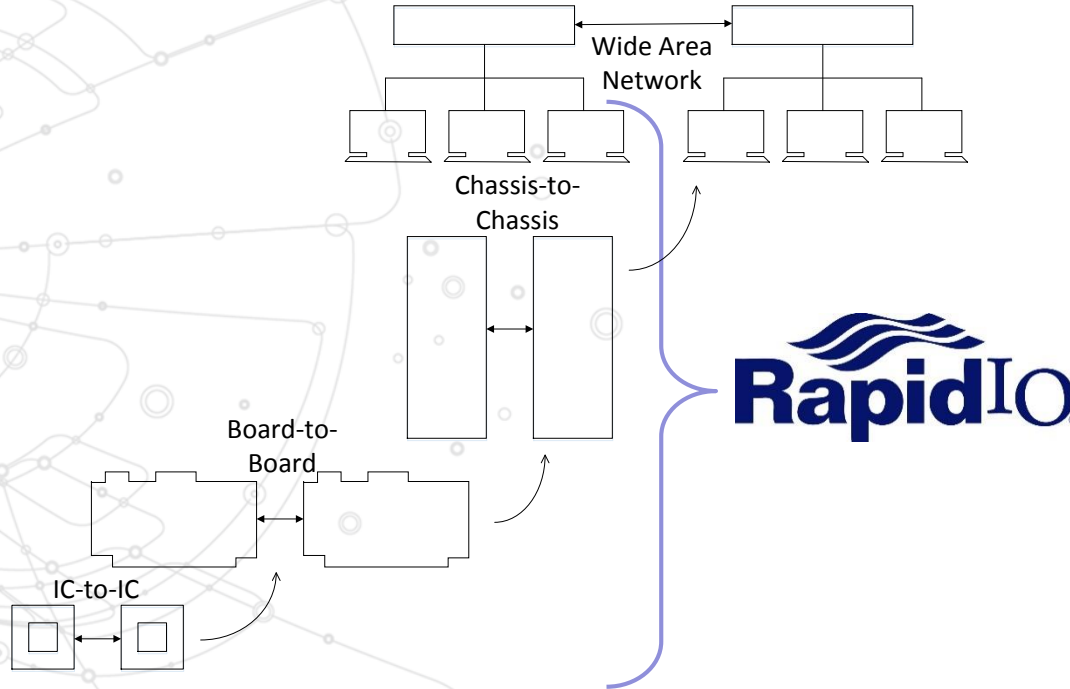
CHEP 2016

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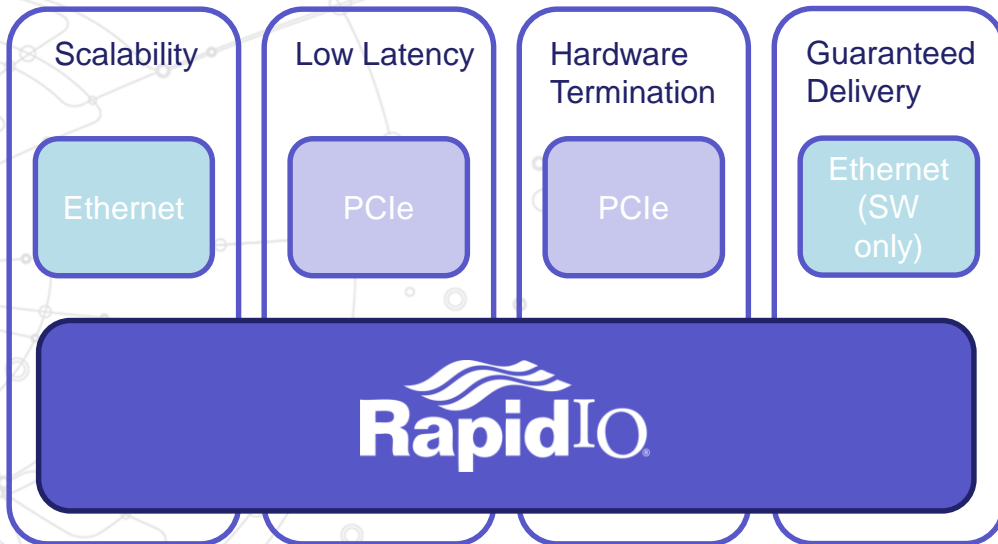


# System Level Interconnect

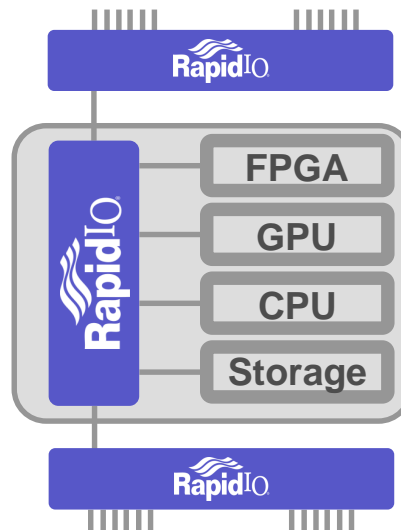


- › From front side bus to system level interconnect
- › Meets real time needs as well as scalability

# RapidIO Characteristics



- › **Combines strengths of Ethernet and PCIe**



- › **Supports heterogeneous systems**

# Project Use Cases



**Data  
Analytics**

**Data  
Acquisition**

**Real Time  
Trigger  
Systems**

- › **Explore RapidIO**
- › **Focus on different areas**
- › **Evaluate suitability**