

ATLAS Trigger and Data Acquisition Upgrades for High Luminosity LHC

- ATLAS has a TDAQ design to meet the challenges of HL-LHC
- Two-level hardware trigger based on Regions of Interest, also a single level option
- Hardware tracking in L1 and EF used to tackle high pileup
- Trend in DAQ from custom hardware to commodity hardware and software
- Baseline will be documented in a TDR, due around the end of 2017

