The ultimate ionization threshold in semiconductor detectors

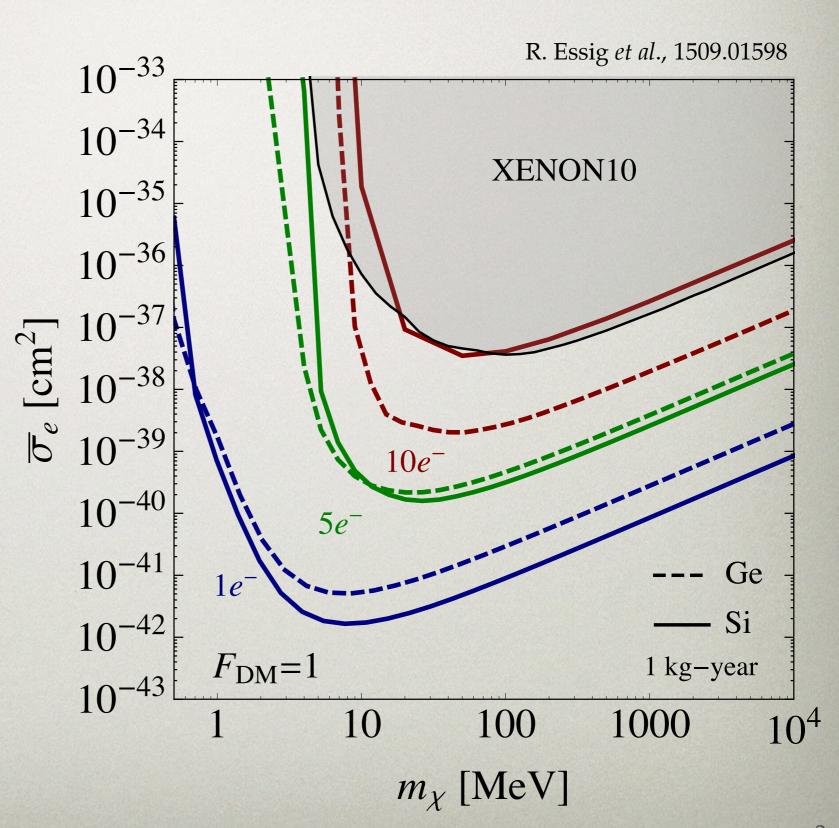
Aaron Manalaysay



SLAC Workshop on Dark Sectors 29 April, 2016

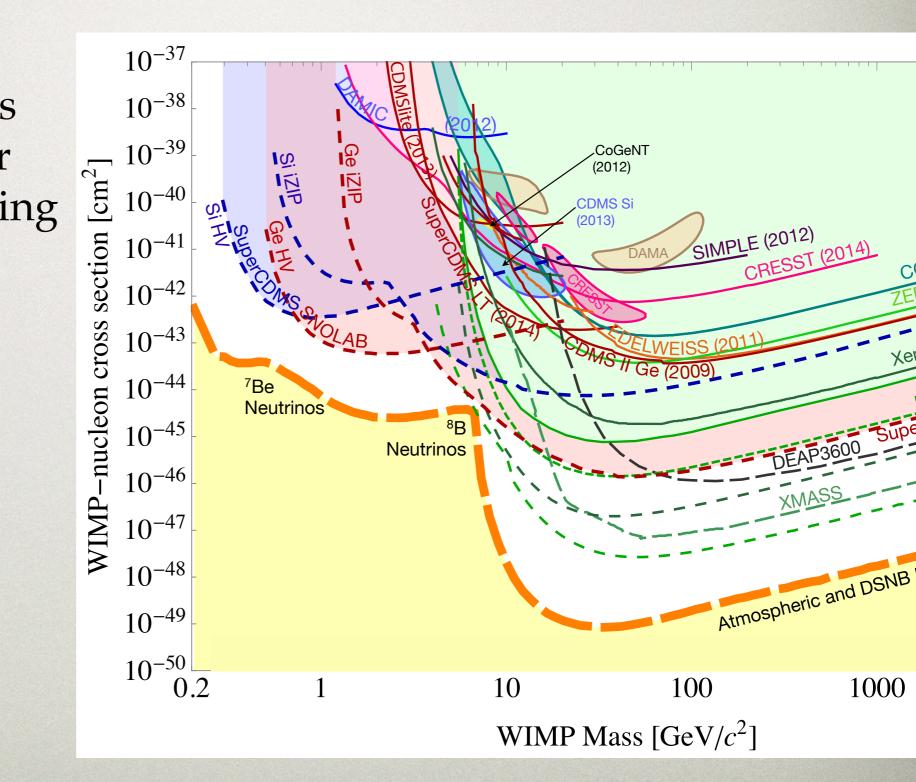
Low-threshold with semiconductors

•Strong motivation for future DM searches to push for semiconductors with very low ionization thresholds.



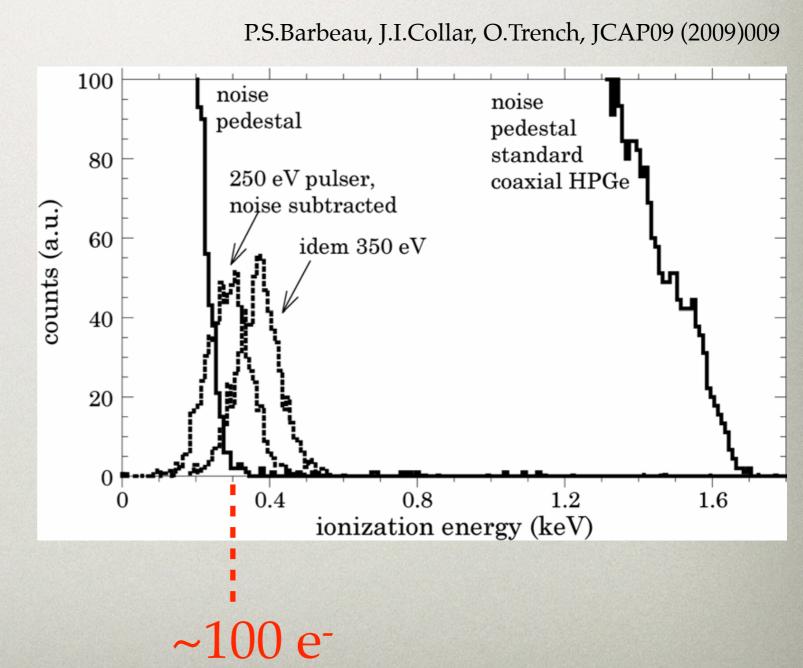
Low-threshold SuperCDMS

•Indeed, SuperCDMS is taking the prospect for integer-electron counting very seriously.



Traditional charge readout

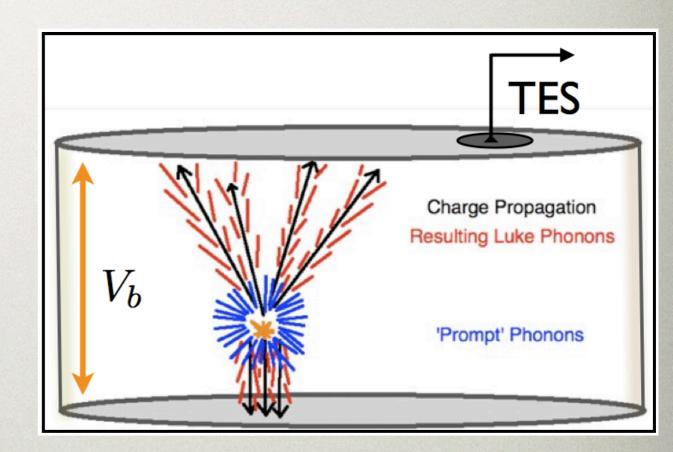
- Traditional charge readout in semiconductors, involving transistors and feedback, cannot approach thresholds of ~few electrons.
- Amplification is not the way to go.



Paths to single-e sensitivity

- The CDMS approach involves using multiplication, instead of amplification.
- Drifting electrons each produce many phonons, which are detectable via TESs. Possibility to do $n-\gamma$ discrimination.
- •They are facing some hurdles to single-e sensitivity
 - * Reducing noise requires reducing parasitic thermal power, which requires reducing T_c .
 - * Observed noise scaling differs from ideal expectation ($\propto T_c^3$) —> the devil is in the details?

From "Light Dark Matter at SuperCDMS" (J.Cooley, M.Pyle)
Beyond WIMPs (http://tomerv.wix.com/lightdm#!sessions/c3kh)



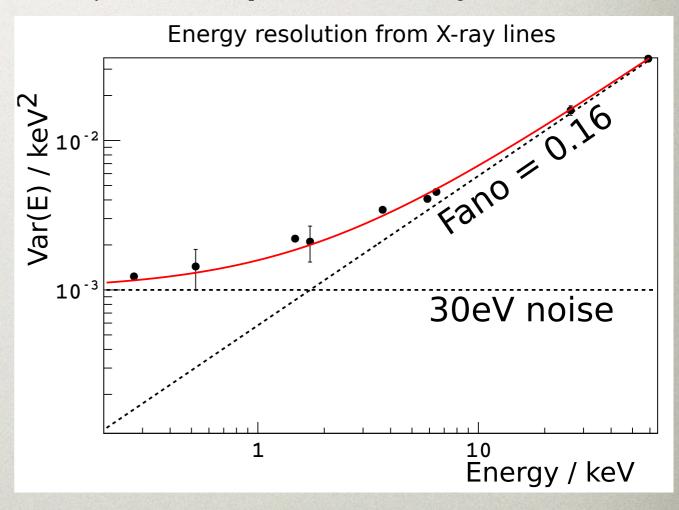
"difficult" cryogenics

See next talk in this session by Lauren Hsu

Paths to few-e sensitivity

- The DAMIC approach uses traditional CCDs
- Charge noise scales with the number of readouts, so they read out very infrequently (~8hr exposures)
- •No timing information: one sees many tracks, reminiscent of emulsion or bubble chambers.
- Difficult to scale up in mass
- •Noise floor limits sensitivity to ~10e- threshold.

"Light DM at DAMIC" (J.Tiffenberg)
Beyond WIMPs (http://tomerv.wix.com/lightdm#!sessions/c3kh)



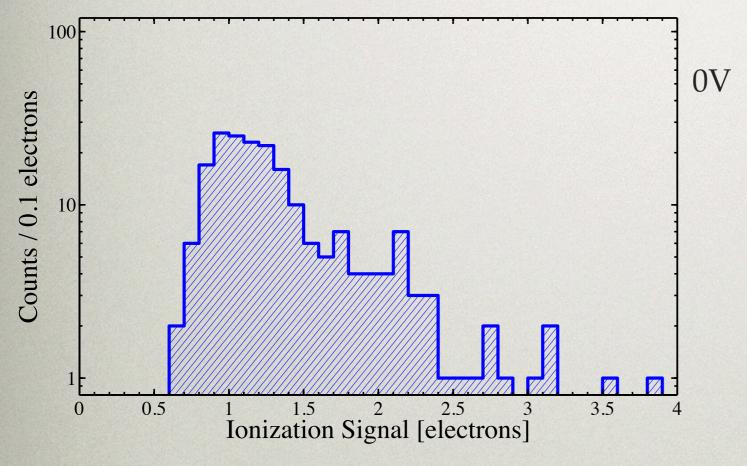
easy cryogenics

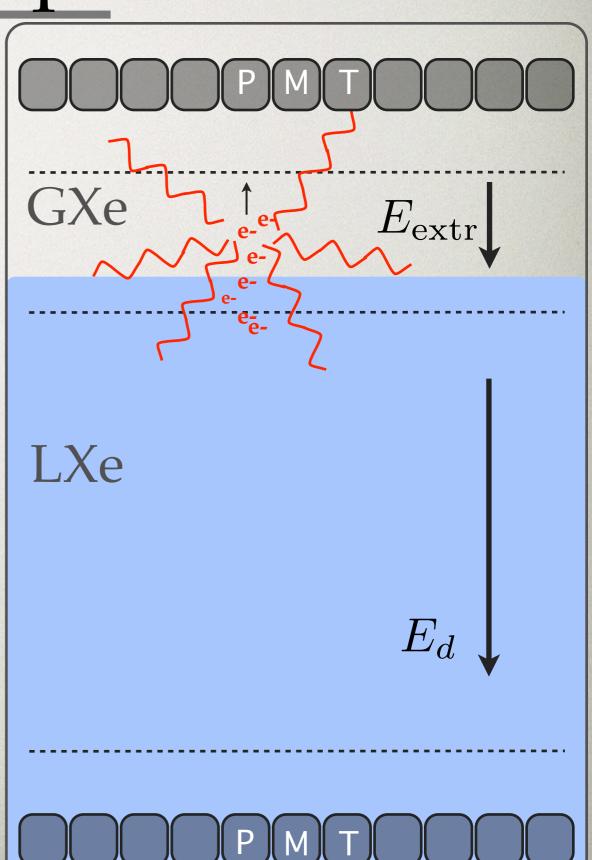
See talk by Juan Estrada

Single-e in noble liquids

-HV

Electron extraction works well for LXe and LAr: get the electrons out, then multiply them

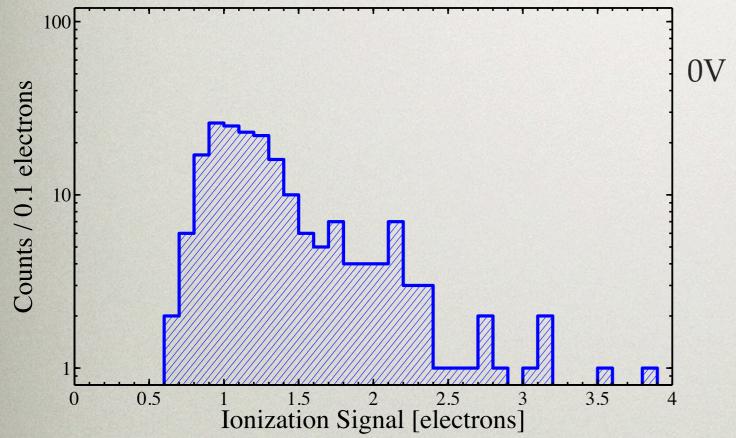




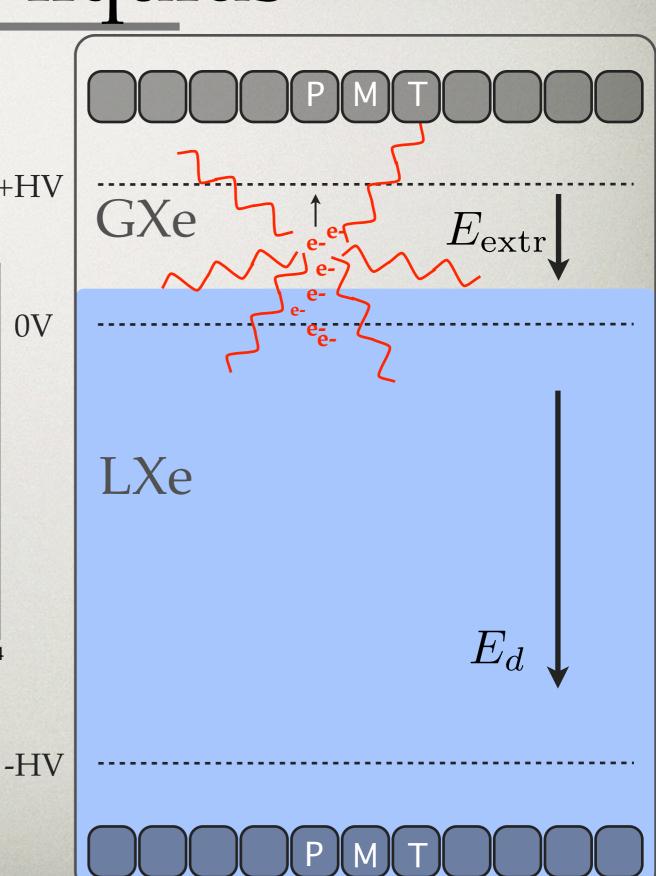
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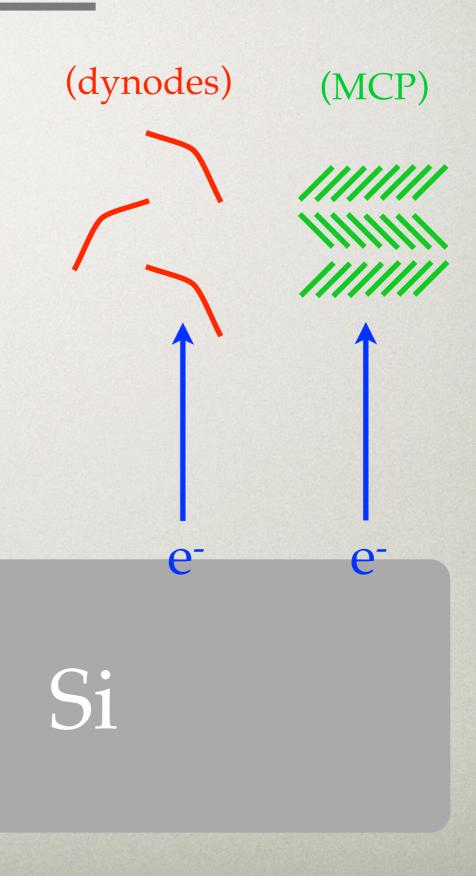


What would be possible if we could extract electrons from a semiconductor?



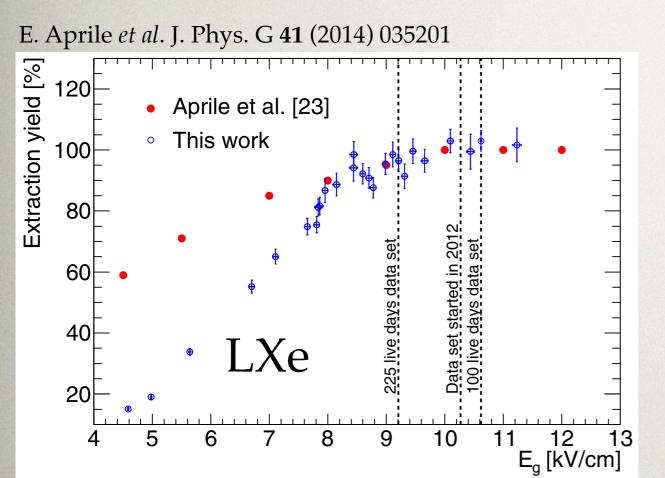
PMT without the P

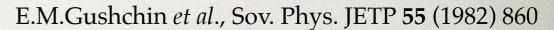
If the electrons from an interaction could be extracted, obtaining single electron sensitivity would be trivial. We do this all the time with PMTs.

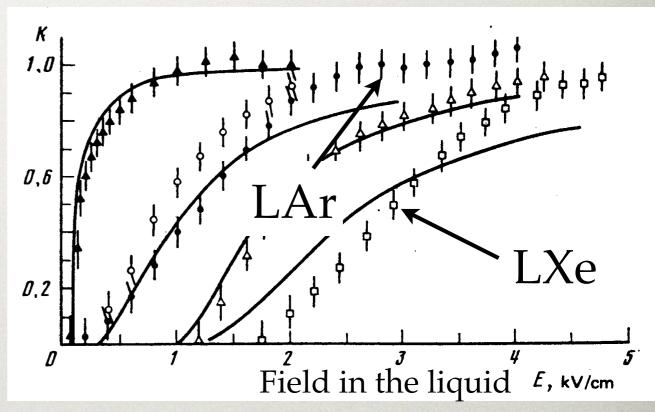


But I'm avoiding the question: How?

Extraction efficiency







Different measurements support the same picture:

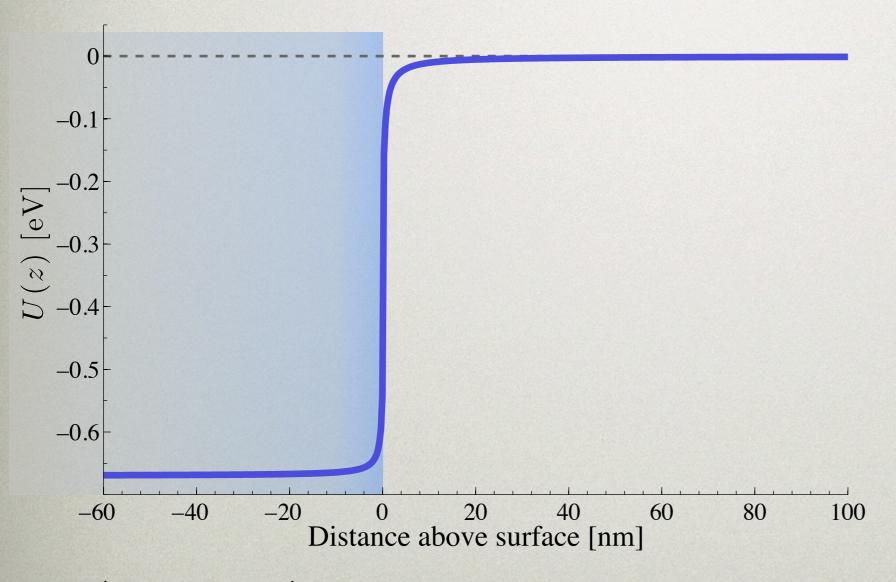
LXe: 100% efficiency for electron extraction at ~10 kV/cm (in the gas)

LAr: 100% efficiency for electron extraction at ~4 kV/cm

Can we understand these results and use them to predict what fields would be necessary in Si?

Electron potential energy:

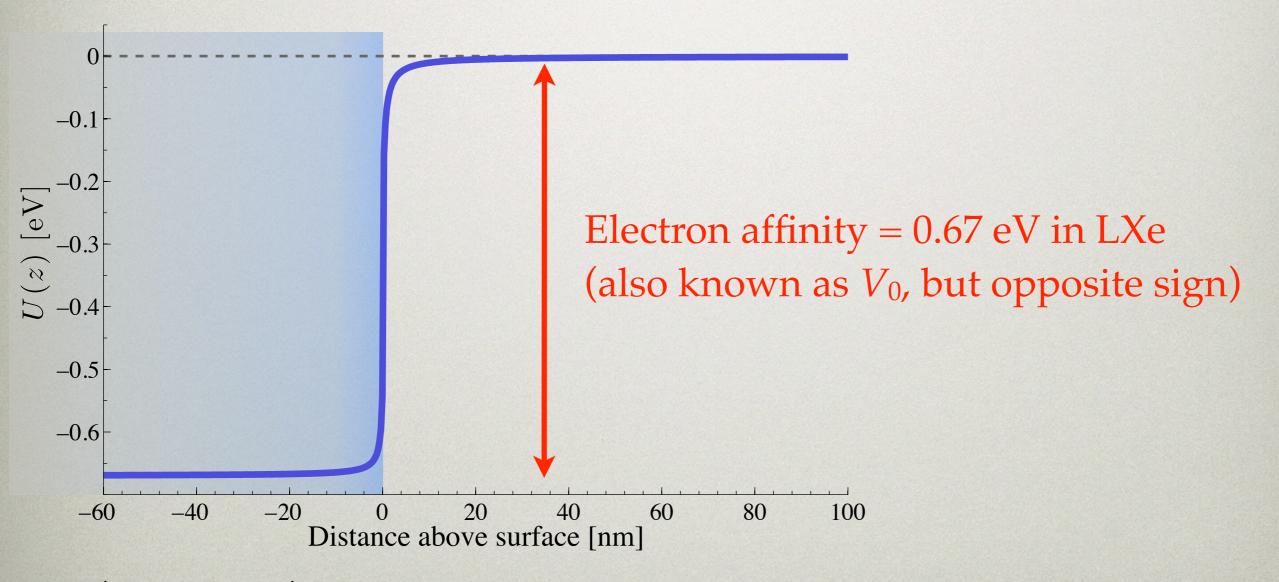
$$U(z) = \frac{1}{16\pi\epsilon_0} \frac{e^2}{z+\beta} \frac{\epsilon - \epsilon_0}{\epsilon + \epsilon_0}, \quad z > 0$$



(no field)

Electron potential energy:

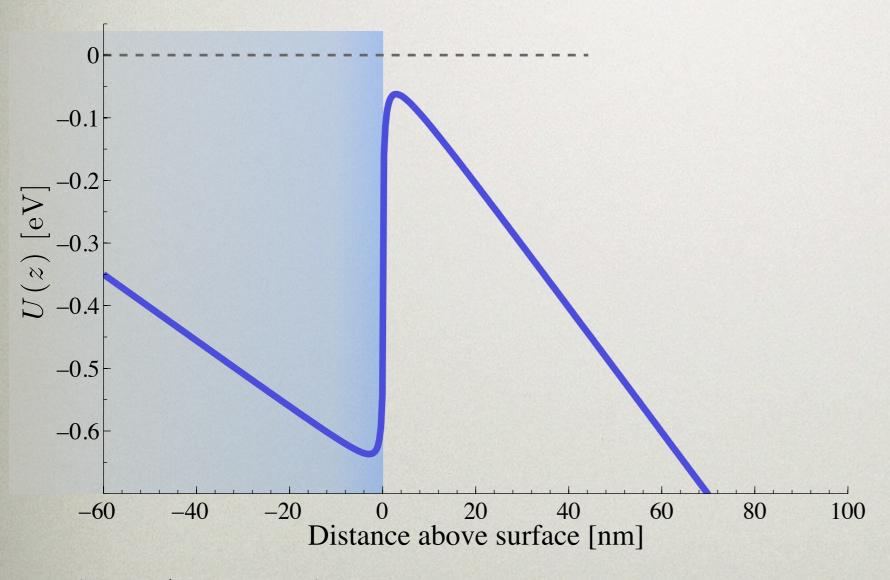
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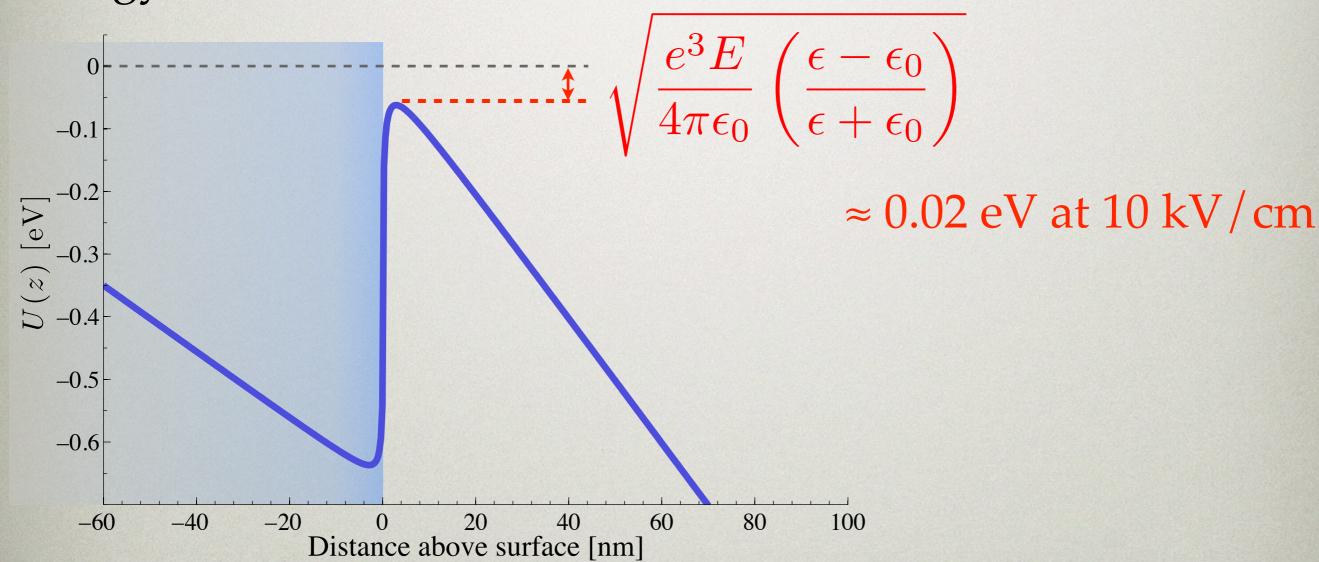
$$U(z) = \frac{1}{16\pi\epsilon_0} \frac{e^2}{z+\beta} \frac{\epsilon - \epsilon_0}{\epsilon + \epsilon_0} - eEz, \ z > 0$$



(with field)

Electron potential energy:

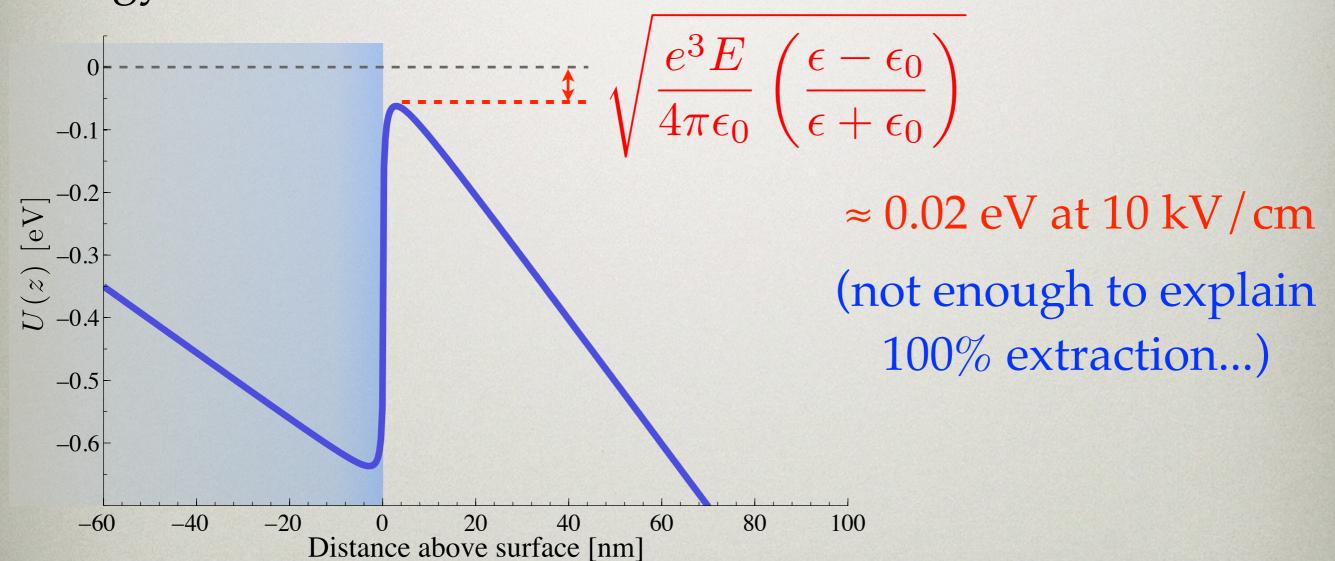
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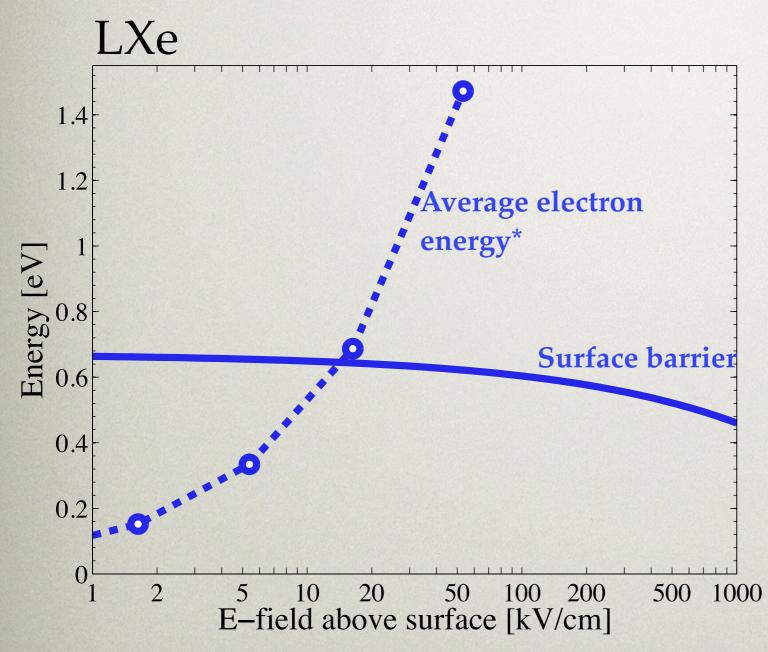
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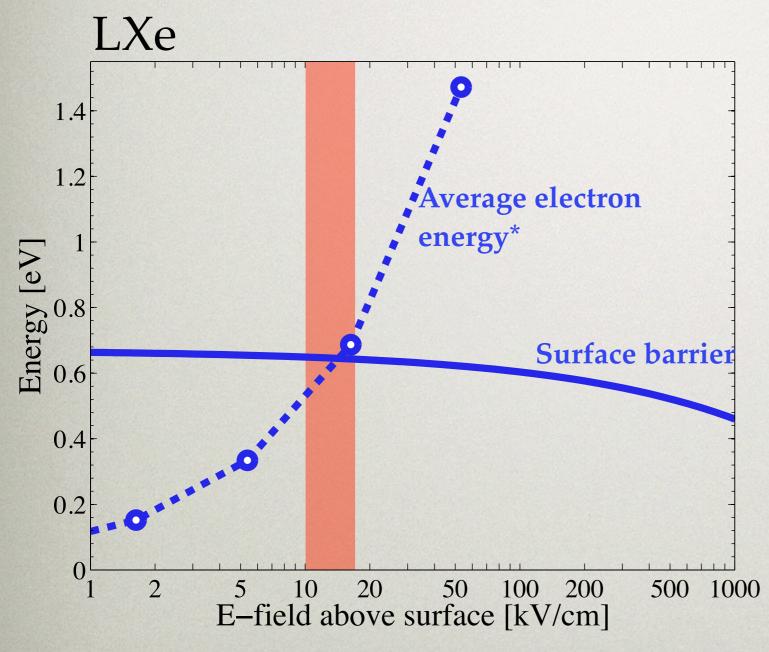


(with field)



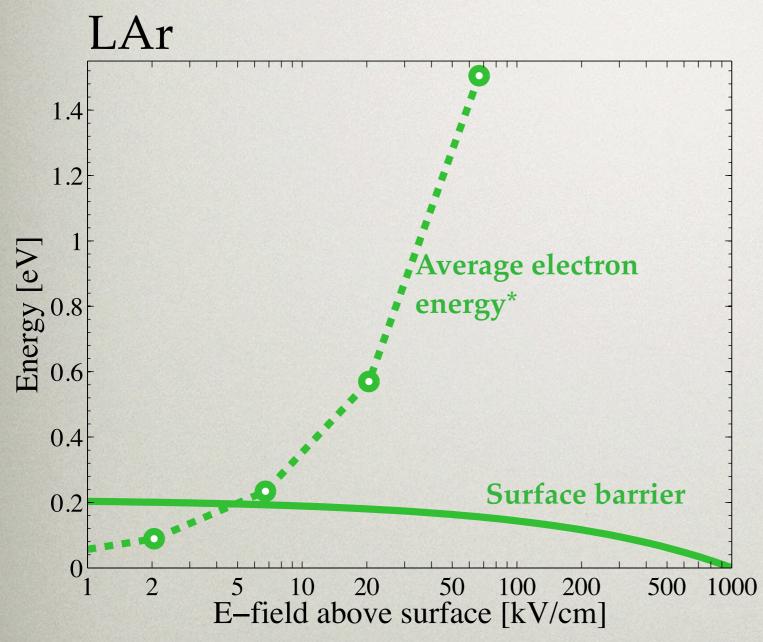
- Electron temperature increases with applied field
- We can conclude that 100% extraction occurs when the electron temperature exceeds the potential barrier.

*U. Sowada et al., Chem. Phys. Lett. 34 (1975) 466



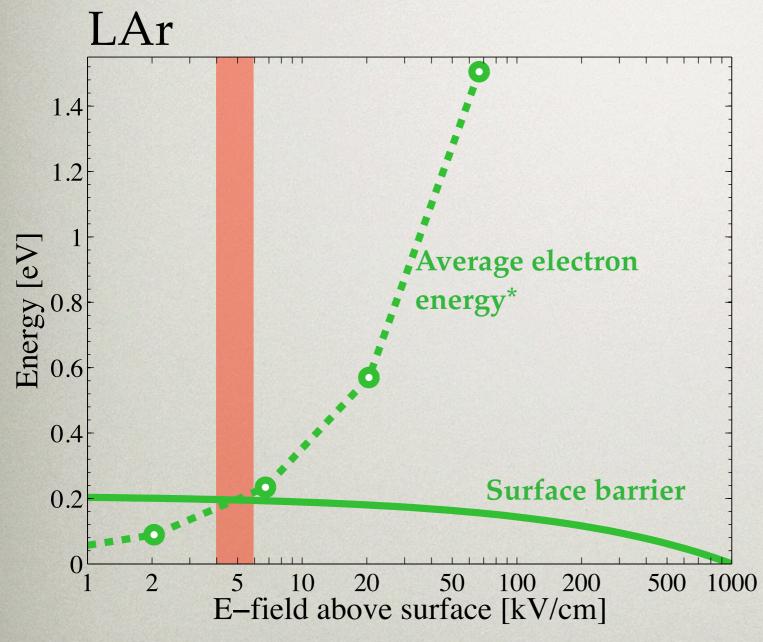
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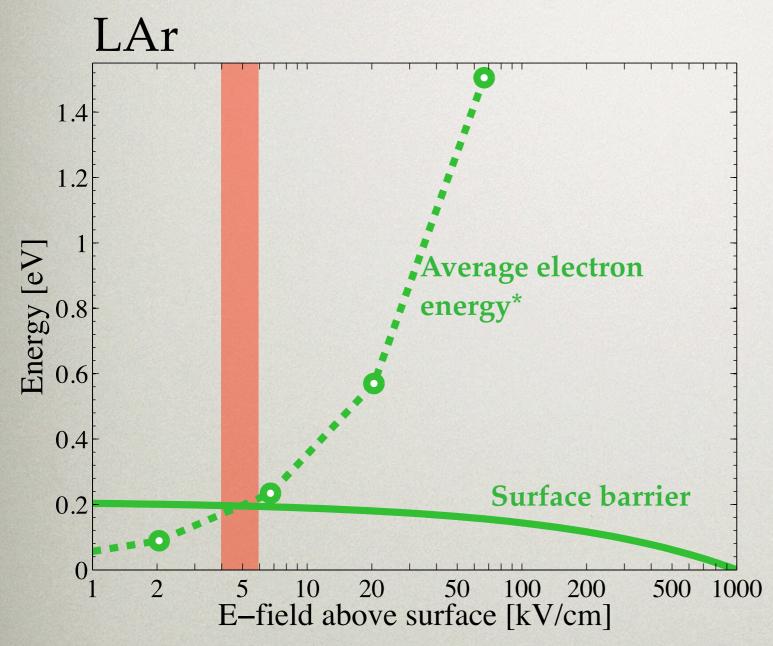
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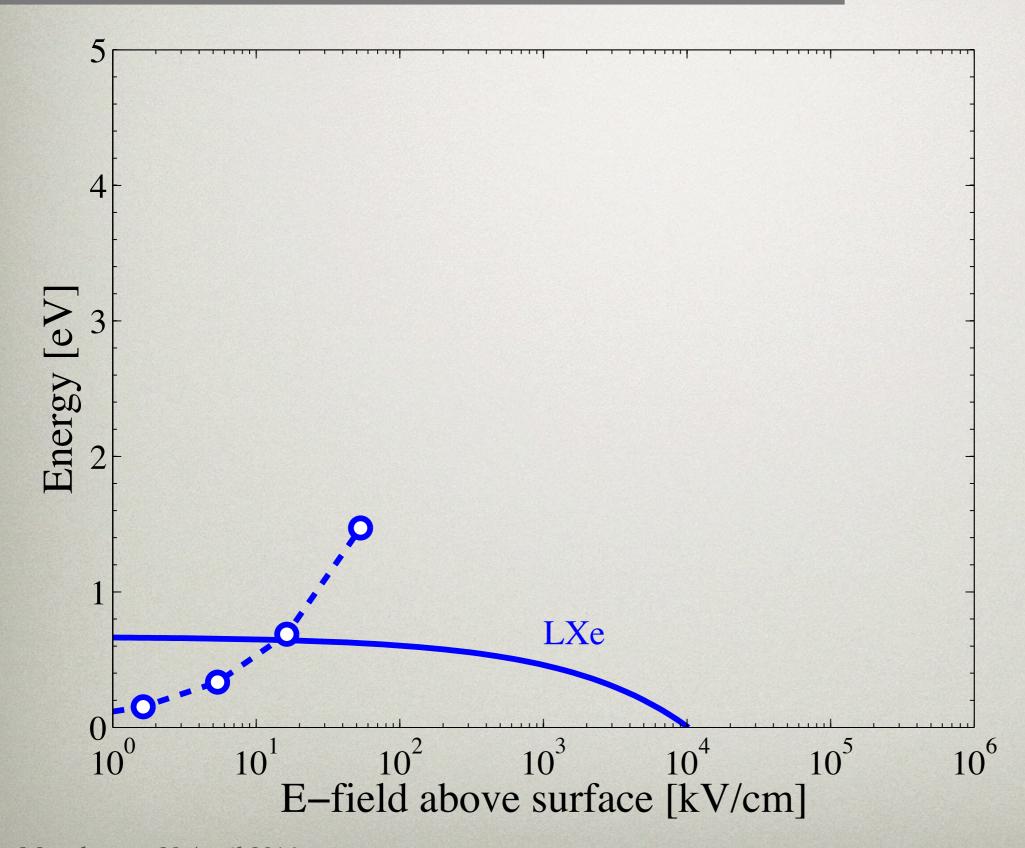
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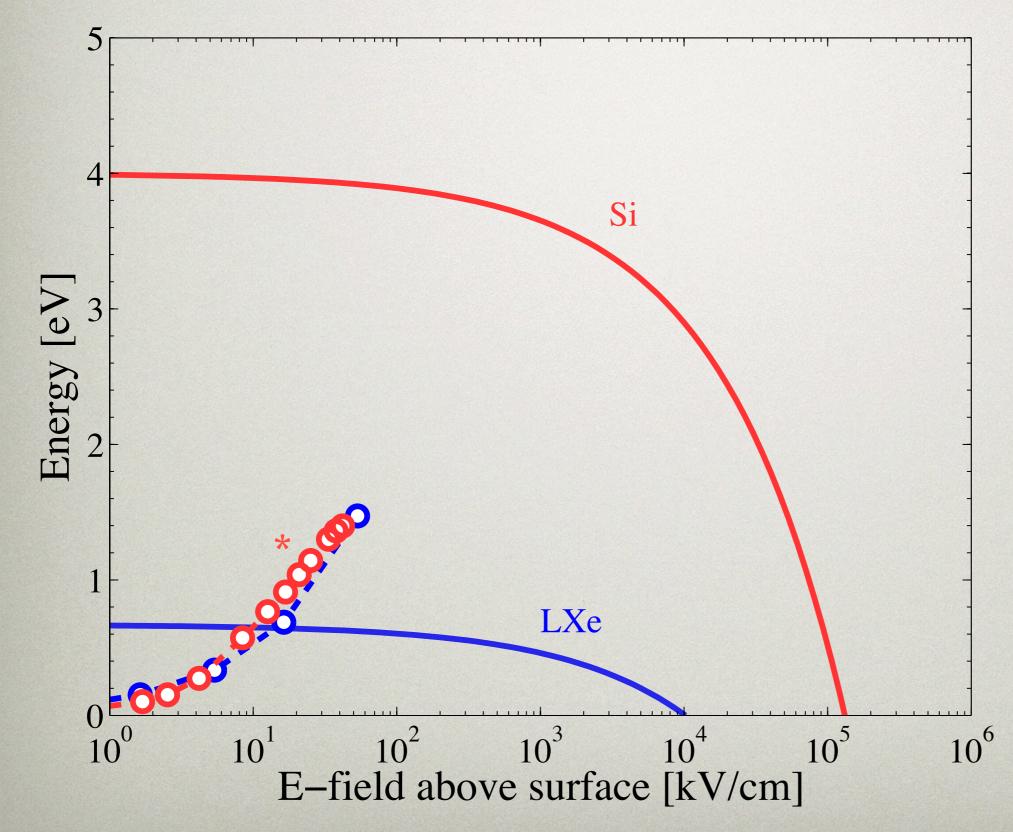


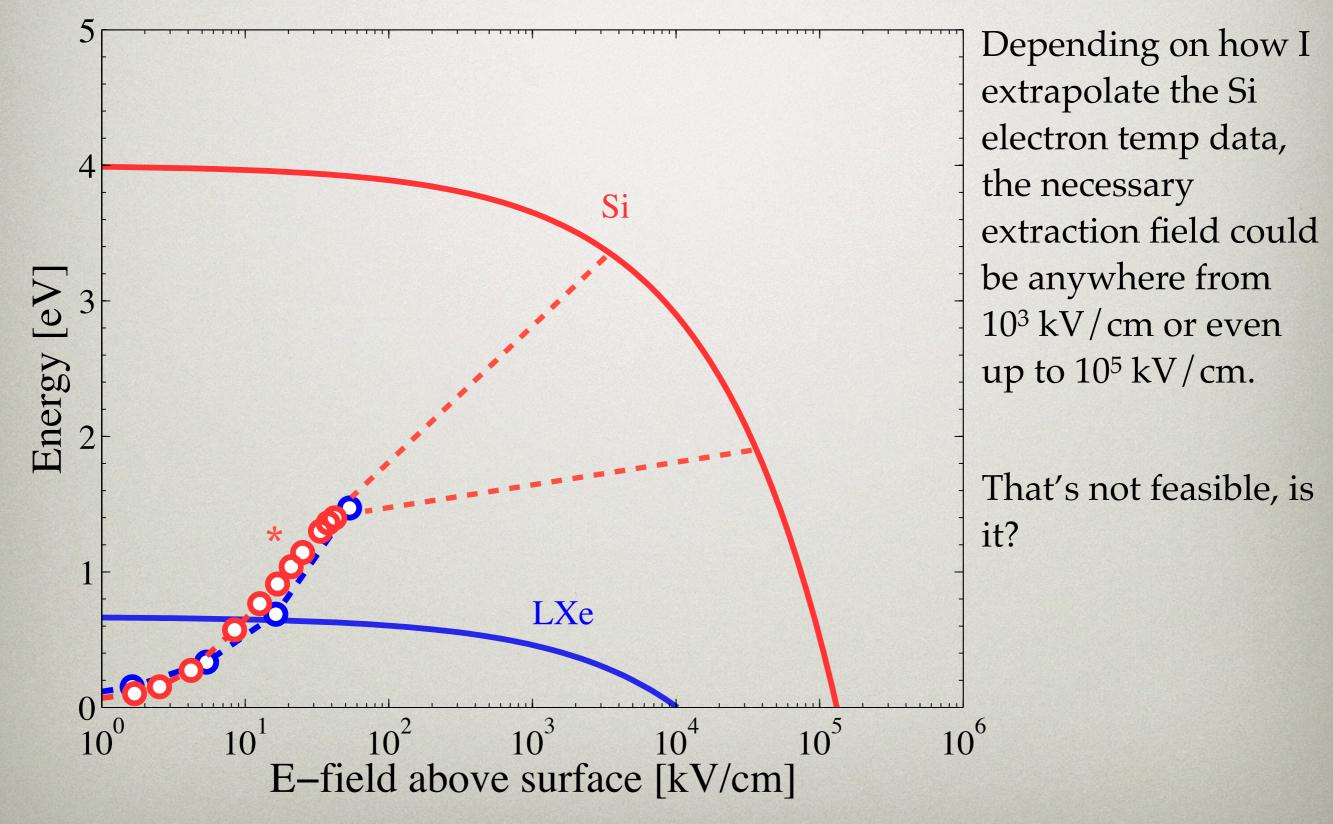
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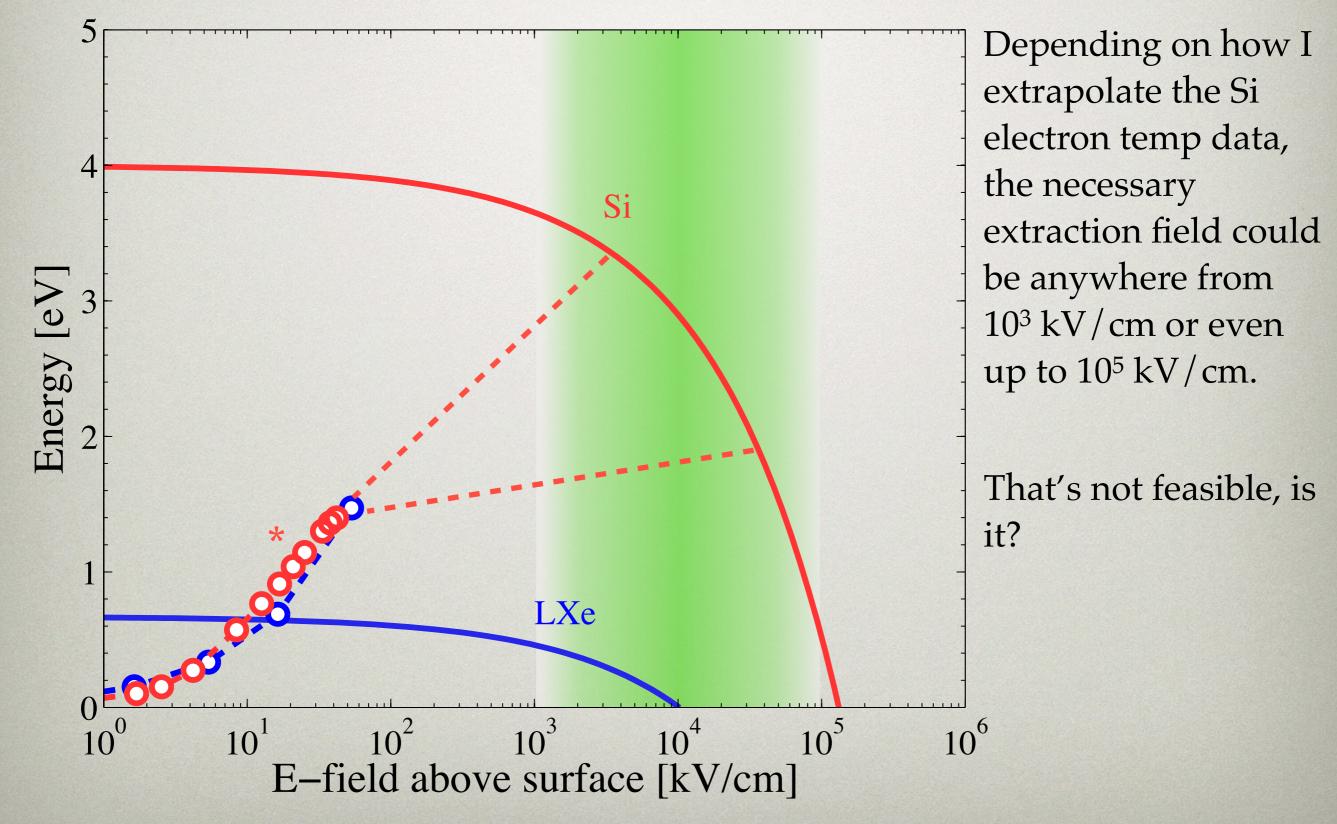
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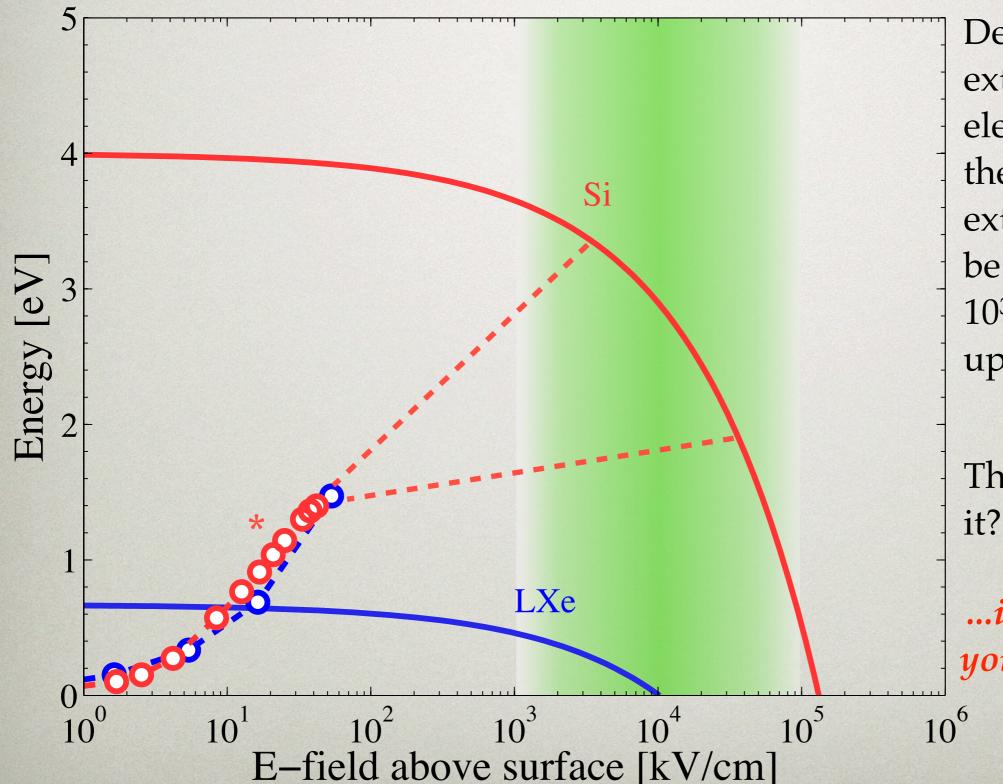
What does this plot look like for Si?









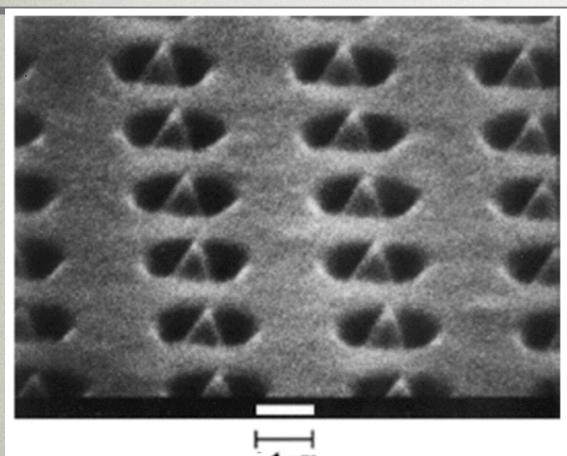


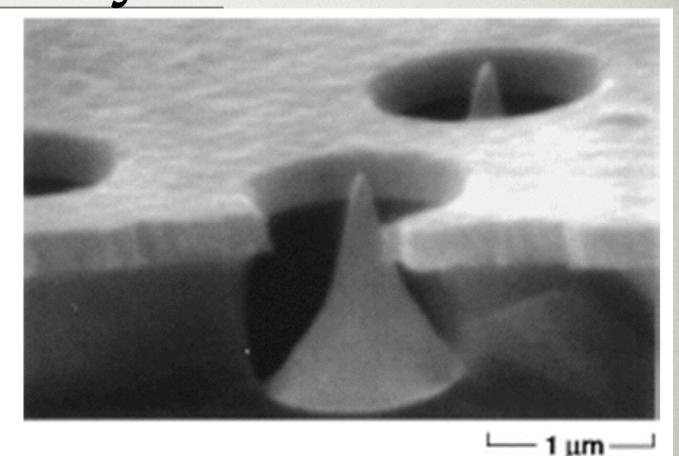
Depending on how I extrapolate the Si electron temp data, the necessary extraction field could be anywhere from 10^3 kV/cm or even up to 10^5 kV/cm.

That's not feasible, is it?

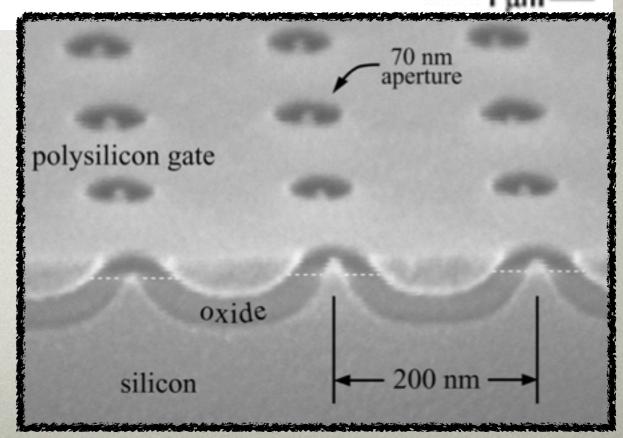
...it depends on what you do to the surface!

Field Emitter Arrays





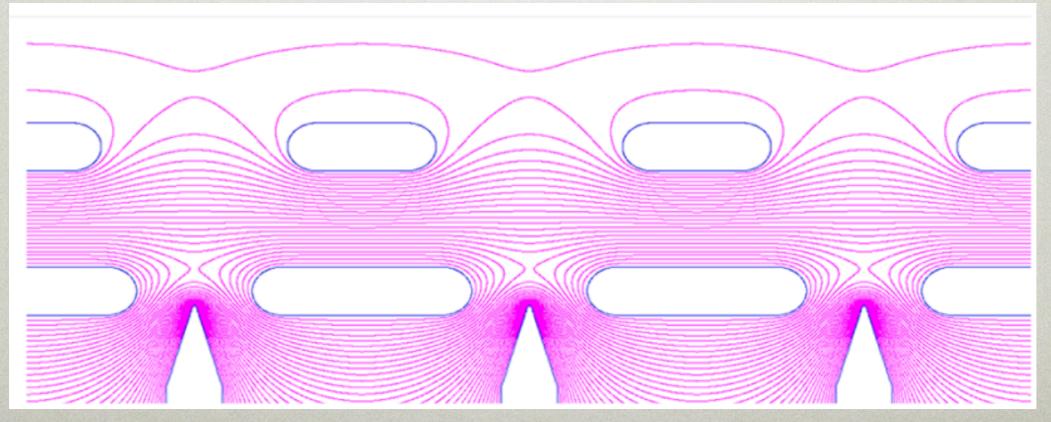
An array of microscopic tips is etched on the surface of the silicon (or other material). A conducting plate is held above the surface by an insulating layer. *Tips are* nm-sharp!!!



HUGE field densities

Field densities of ~10⁵ kV/cm at gate bias of ~30V!!





This is not new technology

An important point about this technology is that it is very mature, involving standardized techniques. Many facilities easily have the necessary capabilities.

C.A. Spindt, J. Appl. Phys. 39 (1968) 3504

A Thin-Film Field-Emission Cathode

C. A. SPINDT

Applied Physics Laboratory, Stanford Research Institute,

Menlo Park, California

(Received 19 February 1968)

Research on micron-size field-emission tubes^{1,2} has recently led to the development of a novel low-voltage, high-current, field-emission cathode and relatively simple techniques for producing such cathodes in various forms. The basic cathode consists of a molybdenum-aluminum oxide-molybdenum thin-film sandwich an a campbing substrate basing either a random or regular array.

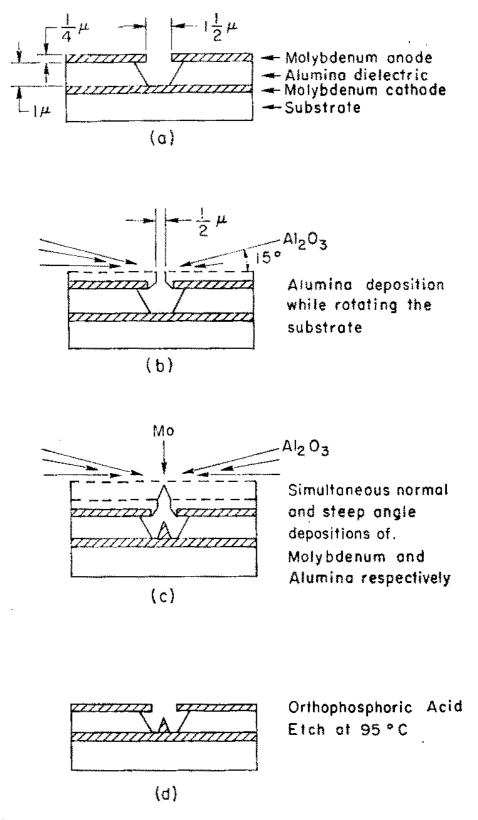
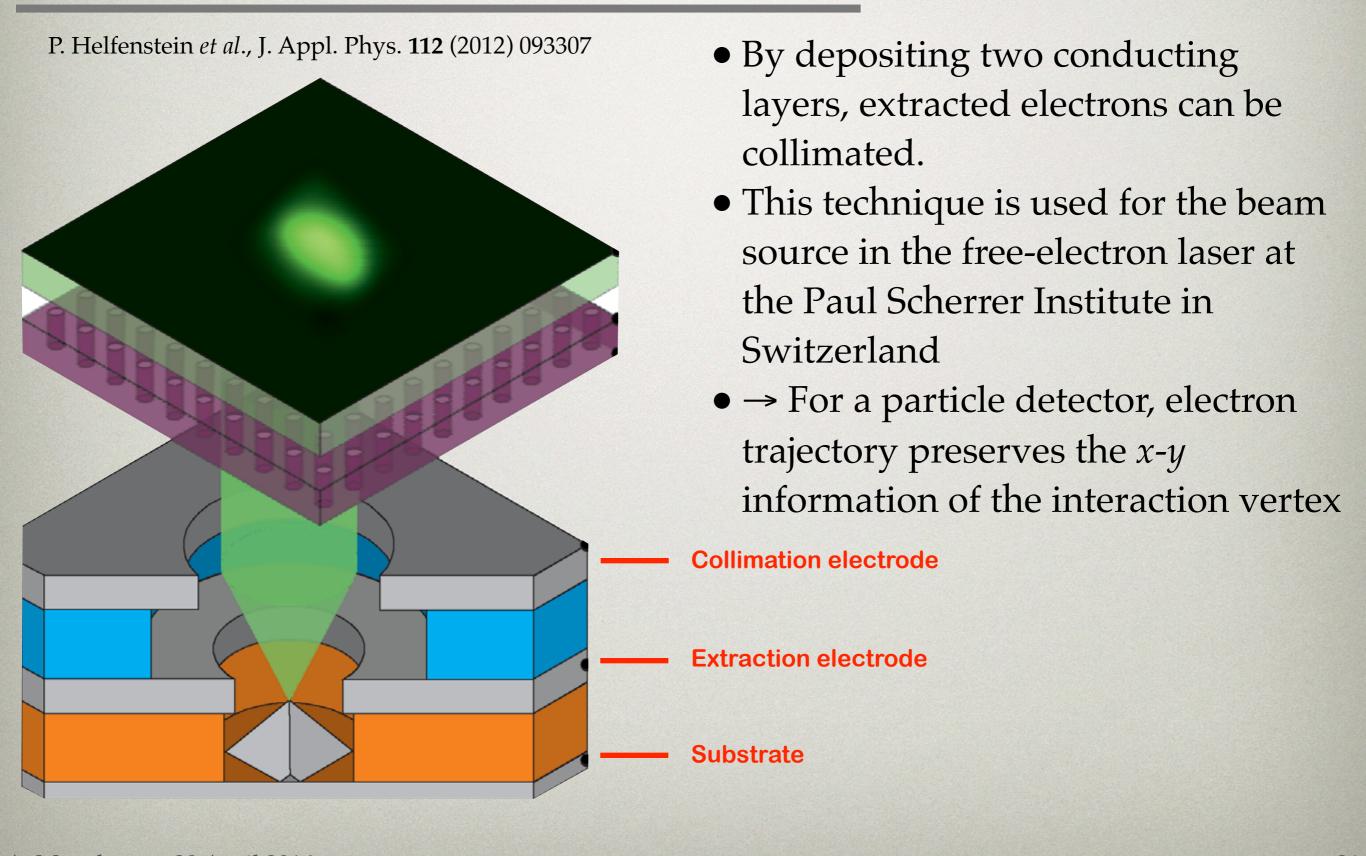


Fig. 2. Cathode formation by deposition from two sources.

Electron collimation



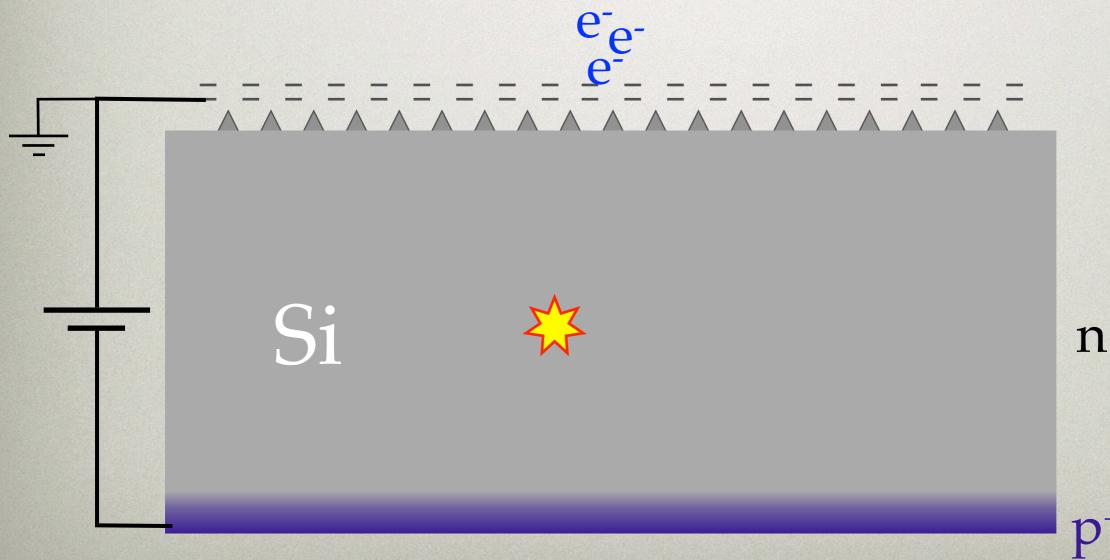
Towards a detector concept



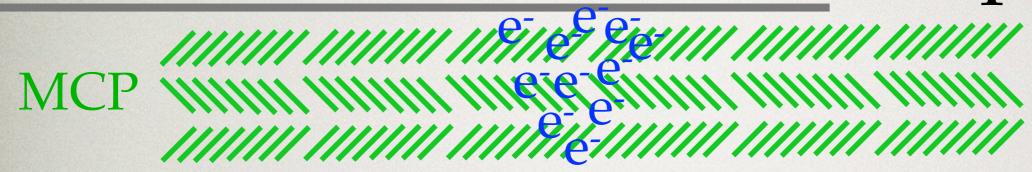


Towards a detector concept





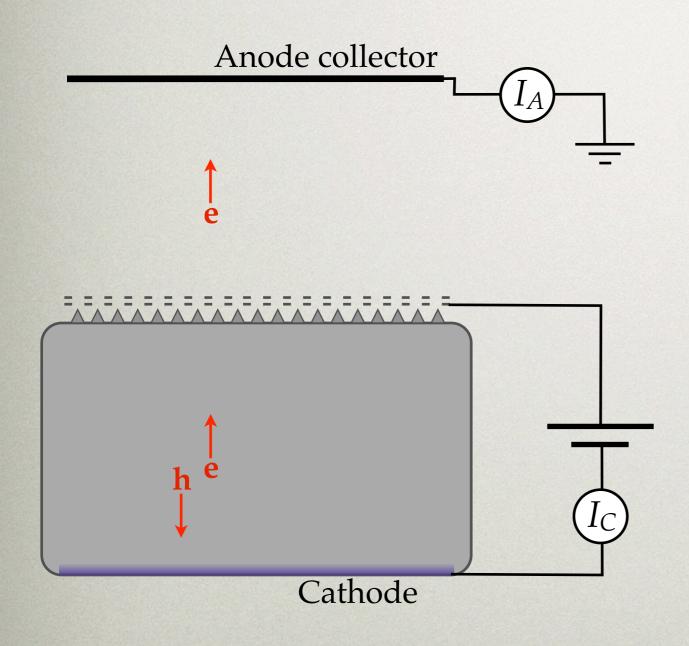
Towards a detector concept





What would be the easiest way to test the viability of this idea?

Easy proof-of-principle



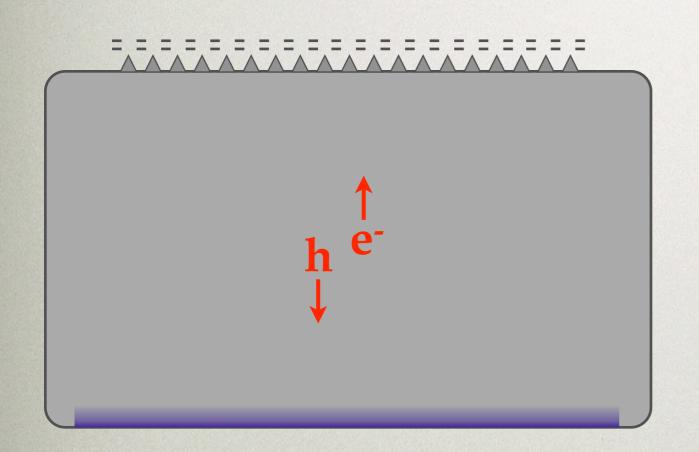
- To measure the extraction efficiency, one can measure the thermally induced current.
- The ratio of I_A to I_C should be equal to the extraction efficiency. It is essential to verify that this can be made something close to unity.
- The temp. can be varied to estimate which portion of I_C is due to thermal excitation.

$$I_{
m therm} \propto T^{3/2} \exp\left(-\frac{E_g}{2kT}\right)$$

What about backgrounds?

Thoughts on potential backgrounds

Thermally induced electrons



$$I_{
m therm} \propto T^{3/2} \exp\left(-\frac{E_g}{2kT}\right)$$

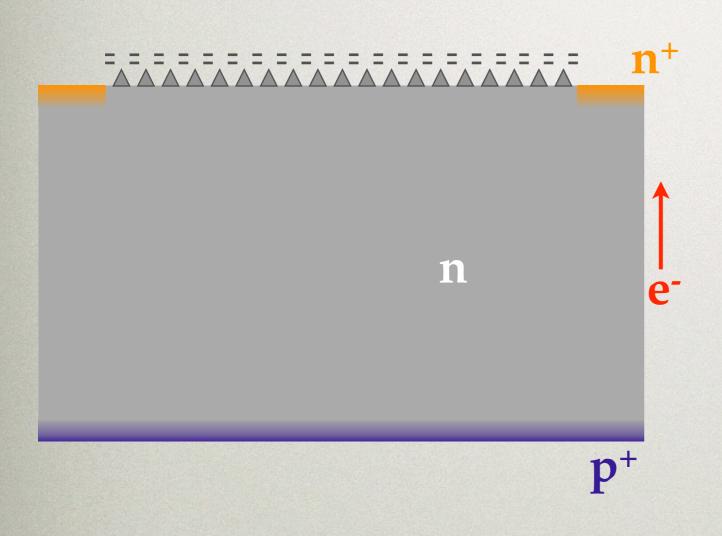
$$E_g \approx 1.2 \text{ eV (Silicon)}$$

For example going from 77K to 4K reduces the thermally induced current by over 260 orders of magnitude!!

Likely no difficult cryogenics needed (i.e. **no dilution fridge**).

Thoughts on potential backgrounds

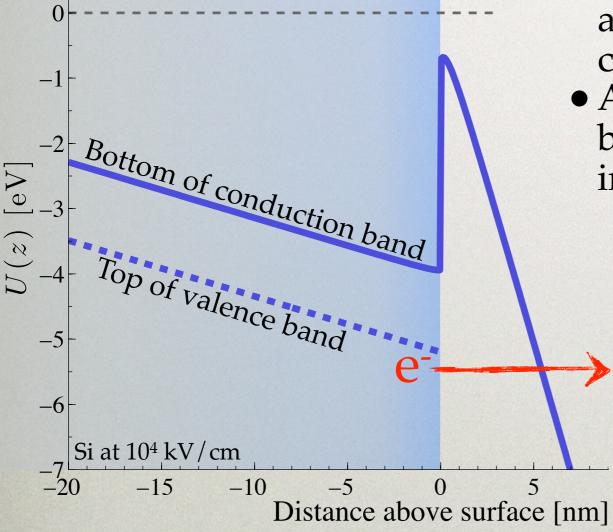
Surface currents



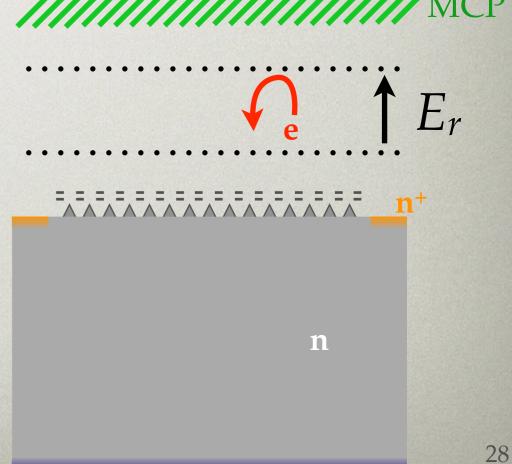
- A significant contribution to leakage current can be due to surface currents, unrelated to thermal excitation of the bulk
- These currents can be absorbed by depositing an n⁺ contact on the periphery, outside the tip array, and coupling it to ground.

Thoughts on potential backgrounds

Valence tunneling



- Valence tunneling ("field emission") could spontaneously throw electrons off the surface
- Such electrons will leave the surface with a reduced kinetic energy (compared to conduction electrons)
- \bullet A retarding field, E_r , can kill electrons below a chosen energy (commonly done in emission spectroscopy)



Someone else apparently had a similar idea...

As I recently discovered, a similar technique has been implemented in x-ray imaging.

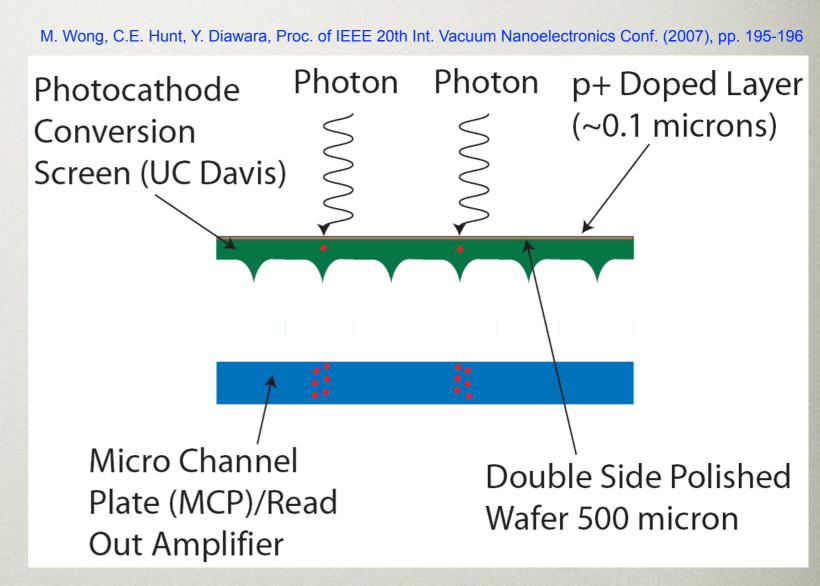


Figure 1: X-ray Imager and Energy Detector

...so stay tuned!

Summary

- Searches for sub-GeV DM provide good, well recognized motivations to build a singleelectron-threshold semiconductor calorimeter
- Extracted electrons can easily be detected with the desired sensitivity
- High fields necessary to emit conduction electrons from Si with ~100% efficiency can be produced with microscopic tip arrays.
- Such a detector would be easy to operate, using simple, mature technologies. Easy to reject/reduce many single-e backgrounds.