Silicon microstrip detectors for future tracker alignment systems







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Aim : align Si microstrip sensors using IR laser tracks

Consecutive layers of silicon sensors are traversed by IR laser beams. Si is almost transparent to IR light. Still, its absorption is enough to produce a measurable signal.



Starting point : design and technology of the detector



Technological factors of optical interest:

• 10 mm diameter optical window in the back

Geometrical factors of optical

- interest:
- strip width
- metal strip width
- readout strip pitch



- Minimum impact on system integration, **no cost in extra material budget**
- Straightforward DAQ integration => Alignment data is read out using Si DAQ
- electrode to allow the light pass through
- different materials => different refractive index
- materials thickness

First optical simulations

Tuning the thicknesses of the different material layers and the pitch/metal strip-width ratio of the sensors we optimized transmittance and reduced reflectance for a wavelength of 1085 nm.

A full optical simulation of the passage of light through a silicon microstrip detector has been developed taking in account the effects of multiple reflections and diffraction by the microstrips, with the use of a rigorous Maxwell's equation solver (RCWA) 1 .











If deposition thickness is better than 5% repeatability of results is granted



W2 -5 -4 -3 -2 -1 0 1 2 3 4 5

Measurements of SiO₂ passivation layer on the top surface of the wafers

X(wafer) (cm)

Wafer Statistics	W1	W2	W3	W4	W5	W6
Mean (nm)	949	980	986	978	955	962
Uniformity (%)	3.3%	3.4%	3.1%	3.2%	3.5%	4.4%

Measured Test structures VS simulated

Good agreement in uniform structures

•256 readout strips with 1.5 cm length •Circular window in the back metal (r=0.5 cm)

intermediate strips without metal in order to improve spatial resolution.

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5	12.5um	5um	1
6	17.5um	5um	1
7	15um	15um	no
8	15um	10um	no
9	15um	5um	no
10	15um	3um	no
11	12.5um	5um	no
12	17.5um	5um	no

2 trasparent diodes, 4 optical test structures (OTS), more test structures.

Optical results at intermediate step

We hold the run just before the deposition of the last passivation layer (Si₃N₄). We measured transmittance and reflectance at this intermediate step.

•T~70% Test structures. •No intermediate implant $\Delta T = +20\%$. •Metal width [3-5] µm: second order effect. •Metal width>10 μ m: $\Delta T \leq -5\%$

•R<30% Test structures. •Metal width>10 μ m: $\Delta R \sim 10\%$ •No intermediate implant less different than in T case.

> %R linked to Al width %T related to uniformity

A new simulation is in process to better choose the thickness of the Si₃N₄ layer which would improve the transmittance (ARC).

Conclusions

•We developed a complete study to improve transmittance of silicon microstrip detectors to IR light .

- i. Optimal layout (msw/pitch ratio =10%)
- ii. Passivation = ARC

• 5+1 wafer with multi-geometry sensor in production. Hold just before the last passivation layer deposition.

- i. Deposit tolerance at CNM is remarkable. Better than 5% in almost all layers => repeatability of results granted.
- ii. First optical measurements at intermediate step: results could improve
- iii. New simulation to choose the Si_3N_4 layer thickness to complete the ARC.

1 "Infrared-transparents microstrip detectors" M.Fernández et al. Nuclear Instruments and Methods in Physics Reserch A (2008), Pages 84-85. 2 "A Portable Readout System for Microstrip Silicon Sensors (ALIBAVA)" Marco-Hernandez, Ricardo; Nuclear Science Symposium Conference Record, 2008. NSS '08. IEEE 19-25 Oct. 2008 Page(s):3201 - 3208