

Pulse processing electronics (II)

Tuesday 12 July 2016 09:00 (1h 30m)

Analog CMOS circuit design: The charge sensitive amplifier (Noise mechanisms, ENC, noise sources, dimensioning the input transistor, the preamplifier reset, the shaper, baseline stabilization, discriminator, sample and hold, ADCs, TDCs, packaging and interconnects, examples.

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Session Classification: Introductory concepts