## Test CMS ECAL SLVRB Design by M. Hansen

## Function of the SLVR Board Integrated into the ECAL TT Electronics

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## Test Setup with CMS ECAL SLVRB



**CMS ECAL SLVRB Schema (1)** 



CMS ECAL SLVRB Schema (2)



### **Comment on installation and running**

The integration of the CMS ECAL SLVRB in the Trigger Tower test setup was without problems.

The cooling of the board is by fan, installed below the board.

Input Voltage 10.0 V (linear LV Power supply) Current drawn by all 5 VFE boards and FE card (CCU=6) is 4.01A

The setting for the original barrel LVR board at P5 is: Input Voltage 4.2 V (Wiener LV PS – Switching type) Current drawn by all 5 VEF boards and FE card is 12.8 A

No problems taking data using the ECAL DAQ and function manager code.

Pedestal and test-pulse runs were taken with original LVR board in the TT4 (CCU=6) and with the new CMS ECAL SLVRB.

**TELEDYNE** Lecroy Scope was used for Fourier Spectrum Analysis.

### **Pedestal data**

Pedestal data taken with SLVRB

Pedestal data taken with original LVRB

Ch 106 107 108 109 110 126 127 128 129 130 146 147 148 149 150 166 167 168 169 170 186 187 188 189 190	Gain 1PedRMS198.6.6194.6.6197.4.6209.8.6212.2.5189.5205.5193.9.6197.4.5191.6207.5.6203.1.6203.2.5190.9.6197.4.6203.1.6203.1.6203.1.6203.1.6203.1.6197.4.6197.4.6197.4.6193.1.5185.4.6190.5.6203.8.6	Gain6PedRMS196.4.6207.5.7206.2.7196.7.7200.4.7201.7.7201.4.7195.8.8189.7.7205.1.7209.2.7198.1.8199.7.7208.3.6195.8.7208.3.6195.8.7208.3.6195.8.7193.5.7188.8.6202.1.7190.4.7185.5.7191.6.7	Gain12PedRMS209.41203.31208.7.9192.4.9196.11205.51208.7.9210.8.9193120711981205.21205.21205.21205.31196.5.9193.41195.6.9211.21195.1.9197.9.9196.81186.2.9197.21182.91	St. 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Ch 106 107 108 109 110 126 127 128 129 130 146 147 148 149 150 166 167 168 169 170 186 187 188 189 190	Gain 1PedRMS196.5.6192.2.6195.1.5207.9.6210.6190.7.6206.4.6195.7.6194.6195.7.6193.2.6193.2.6208.8.5204.8.6202.6204.4.6207.5.5192.8.5192.8.5192.8.6210.5.6197.7.6190.3.6195.2.6209.7.6	Gain 6PedRMS194.1.7205.8.7204.7195.4.7199.3.7208.9.6212.3.7211.7204.6.7194.9.7198.2.8204.5.7208.9.7198.6.7198.1.7196.2.7200.6.7192.8.7192.9.7207.9.8194.7.7197.4.7	Gain 12 Ped RMS 208.5 1 202.6 1 207.7 1 192.1 1 195.7 1 203.8 1 206.9 .9 209.8 1 195.3 1 205.1 .9 196 1 208.2 .9 203.5 1 205.8 1 192.6 1 190.8 1 195.9 .9 211.5 .9 191.9 1 197.6 1 200.9 1 192.3 1 190.5 1 203.6 .9 191.4 1	
Meam:	198.3 0.58	199.7 0.70	199.5 0.9 <b>@</b>		Mean:	199.8 0.58	200.5 0.70	200.0 0.98	[

The G12 mean value of the noise for the TT, powered by SLVRB is even better than the noise of the TT, powered by the standard LVRB. The RMS = 0.96 ADC counts correspond to energy resolution of ~35 MeV.

## **Pedestal TT with CCU ID=6**

#### new SLVR

#### old LVR



# **Pedestal TT with CCU ID=6**

Pedestal data using Magnus SLVRB



### **Test-pulse TT with CCU ID=6**

new SLVR

old LVR



# **Test-pulse TT with CCU ID=6**

#### TT Test\_pulse data using Magnus SLVRB



# **Noise Spectrum from FEAST Chips 1 and 2**



### **Noise Spectrum from FEAST Chips 3 and 4**



