

CMS ECAL VFE phase II upgrade workshop

Report of Contributions

Contribution ID: 1

Type: **not specified**

Welcome and Practical Details

Thursday, 12 May 2016 09:00 (20 minutes)

Presenter: BESANCON, marc (cea-saclay/dapnia/spp)

Session Classification: Introduction

Contribution ID: 2

Type: **not specified**

Goals of the workshop

Thursday, 12 May 2016 09:20 (20 minutes)

Presenter: JESSOP, Colin (University of Notre Dame (US))

Session Classification: Introduction

Contribution ID: 3

Type: **not specified**

Overall Design and technical questions to answer

Thursday, 12 May 2016 09:40 (30 minutes)

Presenter: DEJARDIN, Marc (CEA/IRFU,Centre d'étude de Saclay Gif-sur-Yvette (FR))

Session Classification: Introduction

Contribution ID: 4

Type: **not specified**

Engineering considerations

Thursday, 12 May 2016 10:10 (20 minutes)

Presenter: HANSEN, Magnus (CERN)

Session Classification: Introduction

Contribution ID: 5

Type: **not specified**

VFE ASIC technical requirements

Thursday, 12 May 2016 14:00 (1 hour)

Presenter: DEJARDIN, Marc (CEA/IRFU,Centre d'étude de Saclay Gif-sur-Yvette (FR))

Session Classification: VFE ASIC Design

Contribution ID: 6

Type: **not specified**

TIA

Thursday, 12 May 2016 15:00 (40 minutes)

Summary

Presenter: BARON, Pascal (CEA/IRFU,Centre d'etude de Saclay Gif-sur-Yvette (FR))

Session Classification: VFE ASIC Design

Contribution ID: 8

Type: **not specified**

QIE

Thursday, 12 May 2016 16:50 (40 minutes)

Presenter: HIRSCHAUER, Jim (Fermi National Accelerator Lab. (US))

Session Classification: VFE ASIC Design

Contribution ID: 9

Type: **not specified**

Discussion

Thursday, 12 May 2016 17:30 (30 minutes)

Session Classification: VFE ASIC Design

Contribution ID: **11**

Type: **not specified**

LVR Overview and interface to VFE

Friday, 13 May 2016 09:45 (25 minutes)

Presenter: LUSTERMAN, Werner (Eidgenoessische Tech. Hochschule Zuerich (CH))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: **12**

Type: **not specified**

VFE Board Design and Layout considerations

Friday, 13 May 2016 11:00 (20 minutes)

Presenter: LUSTERMAN, Werner (Eidgenoessische Tech. Hochschule Zuerich (CH))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: 13

Type: **not specified**

VFE interface to FE considerations

Friday, 13 May 2016 11:20 (20 minutes)

Presenters: SINGOVSKI, Alexander (University of Minnesota (US)); DOLGOPOLOV, Alexandre (Fermi National Accelerator Lab. (US))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: 14

Type: **not specified**

Implications of fast timing to VFE board design

Friday, 13 May 2016 12:00 (20 minutes)

Summary

Presenter: MICHELI, Francesco (Eidgenoessische Tech. Hochschule Zuerich (ETH))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: 15

Type: **not specified**

Testbeam status and planning

Friday, 13 May 2016 12:20 (30 minutes)

Presenter: SINGOVSKI, Alexander (University of Minnesota (US))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: **16**

Type: **not specified**

Tests of DC-DC converters

Friday, 13 May 2016 10:10 (20 minutes)

Presenter: BARRIA, Patrizia (University of Virginia (US))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: 17

Type: **not specified**

ADC requirements and plans

Thursday, 12 May 2016 15:40 (40 minutes)

Presenter: VARELA, Joao (LIP Laboratorio de Instrumentacao e Fisica Experimental de Part)

Session Classification: VFE ASIC Design

Contribution ID: **18**

Type: **not specified**

Off detector electronics considerations

Friday, 13 May 2016 11:40 (20 minutes)

Presenter: HIROSKY, Bob (University of Virginia (US))

Session Classification: Other VFE design aspects (ADC/LVR/FE)

Contribution ID: **20**

Type: **not specified**

MPGA++

Presenter: THOMAS, Stephen (STFC - Rutherford Appleton Lab. (GB))

Session Classification: VFE ASIC Design

Contribution ID: **21**

Type: **not specified**

MPGA++

Friday, 13 May 2016 09:00 (40 minutes)

Presenter: THOMAS, Stephen (STFC - Rutherford Appleton Lab. (GB))