

**Session Program**

**11-13 May 2016**

**CMS ECAL VFE phase II upgrade workshop**

***Other VFE design aspects (ADC/LVR/FE)***

CEA-Saclay INSTN, 112

# Friday 13 May

09:00

## Other VFE design aspects (ADC/LVR/FE)

**Session** | **Location:** CEA-Saclay INSTN, 112 | **Convener:** Werner Lustermann

### 09:45–10:10 LVR Overview and interface to VFE

**Speaker**

Werner Lustermann

### 10:10–10:30 Tests of DC-DC converters

**Speaker**

Patrizia Barria

### 10:30–11:00 Coffee/Discussion

### 11:00–11:20 VFE Board Design and Layout considerations

**Speaker**

Werner Lustermann

### 11:20–11:40 VFE interface to FE considerations

**Speakers**

Alexander Singovski, Alexandre Dolgopolo

### 11:40–12:00 Off detector electronics considerations

**Speaker**

Bob Hirosky

### 12:00–12:20 Implications of fast timing to VFE board design

**Speaker**

Francesco Micheli

### 12:20–12:50 Testbeam status and planning

**Speaker**

Alexander Singovski

12:50