

List of Reviewers of Schematic Design of FEC Rev. 0

Pedro Miguel Vicente Leitao (GBTx)

Alessandro Caratelli (GBT-SCA)

Lauri Juhani Olantera (VTXx)

Marcel Rossewijn, Utrecht University (overall design)

Valerie Chambert (overall design)

Arild Velure (SAMPA digital, overall design)

Alex Kluge (ALICE Electronics)

Torsten Alt (TPC Electronics)