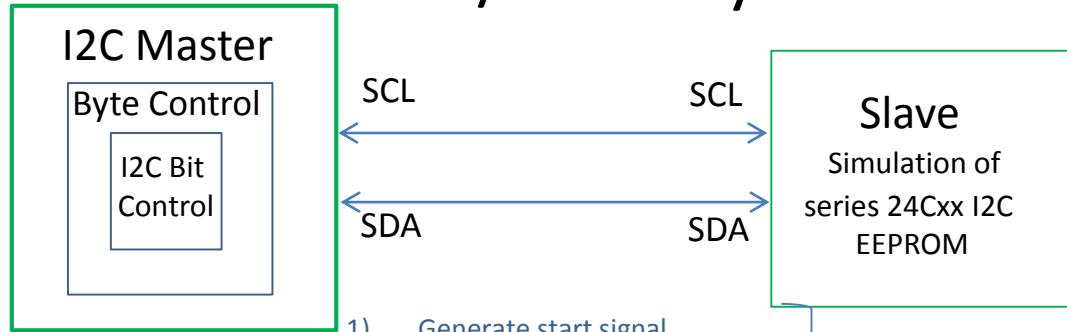
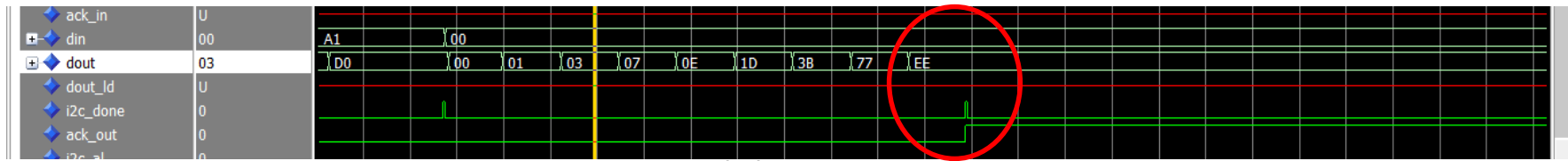
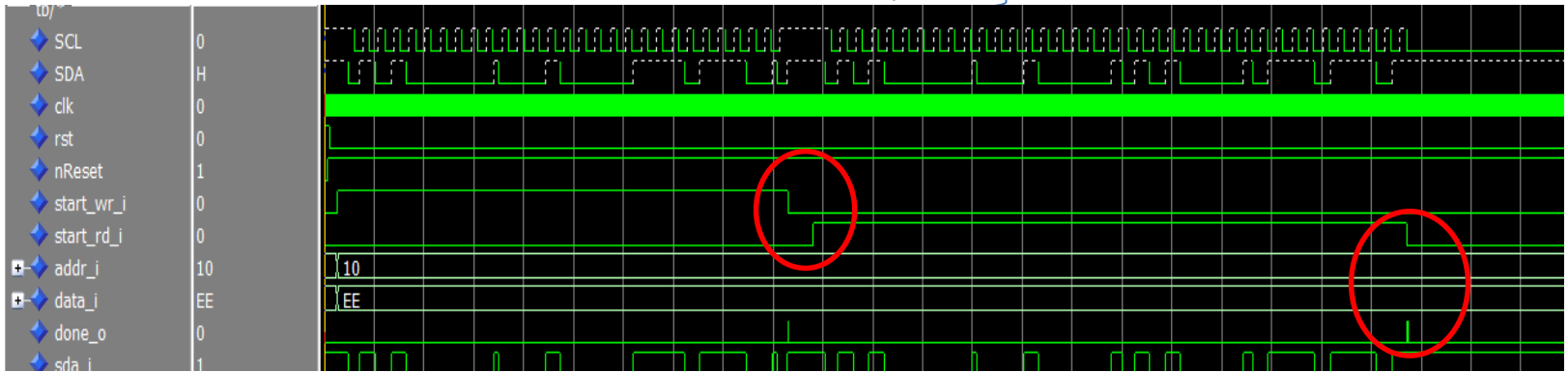


# I2C Read/Write cycle Simulation



- 1) Generate start signal
  - 2) Write Slave address + write bit.
  - 3) Write memory location
  - 4) stop
  - 5) Generate start signal
  - 6) Write Slave address + read bit
  - 7) Write memory location
  - 8) Read from the memory
- } WR
- } RD



Model Sim Waveform

Instance Manager: Ready to acquire

Probe read interval  
Current interval: 9 samples per second  
 Automatic  
 Manual 1 s

Event log  
Maximum size: 8  
 Save data to event log  
Write source data: Continuously

JTAG Chain Configuration: JTAG ready  
Hardware: USB-Blaster [USB-0] Setup...  
Device: @1: EP3C25/EP4CE22 (0) Scan Chain  
File: ...

Index	Instance ID	Status	Sources: 32	Probes: 32	Name
0	alma	Unloading data	32	32	alma

Index	Type	Alias	Name	Data	-8	-7	-6	-5	-4	-3	-2	-1	0
P14	✖		probe[14]	0									
P13	✖		probe[13]	0									
P12	✖		probe[12]	0									
P11	✖		probe[11]	0									
P10	✖		probe[10]	0									
P9	✖		probe[9]	0									
P8	✖		probe[8]	1									
P7	✖		probe[7]	1									
P6	✖		probe[6]	1									
P5	✖		probe[5]	1									
P4	✖		probe[4]	1									
P3	✖		probe[3]	1									
P2	✖		probe[2]	0									
P1	✖		probe[1]	1									
P0	✖		probe[0]	0									
S[31..0]	✖		source[31..0]	0									0

## Monitoring in ISSP