



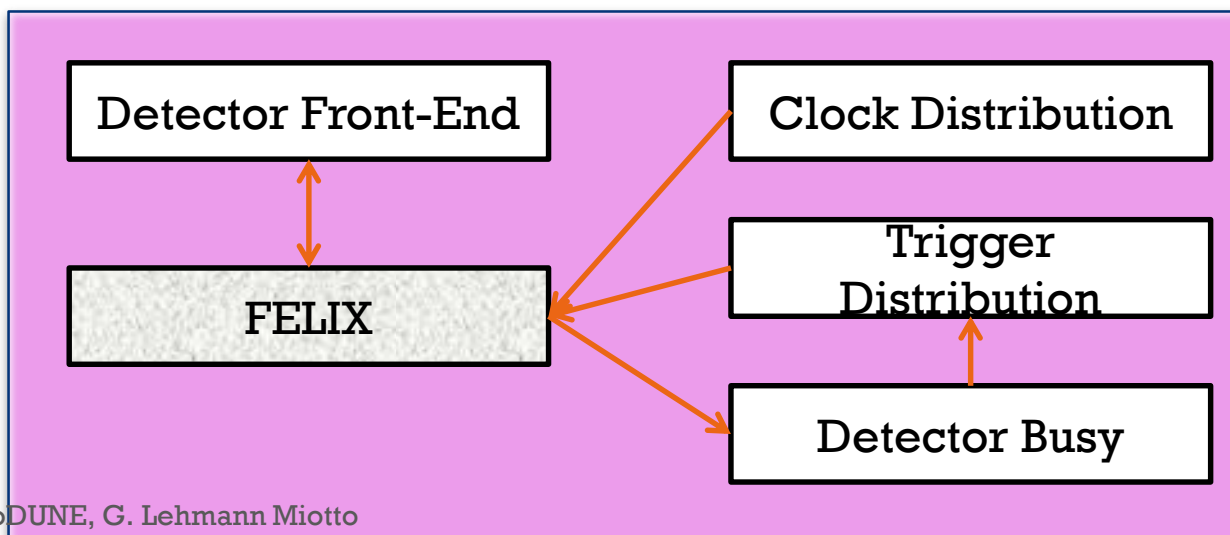
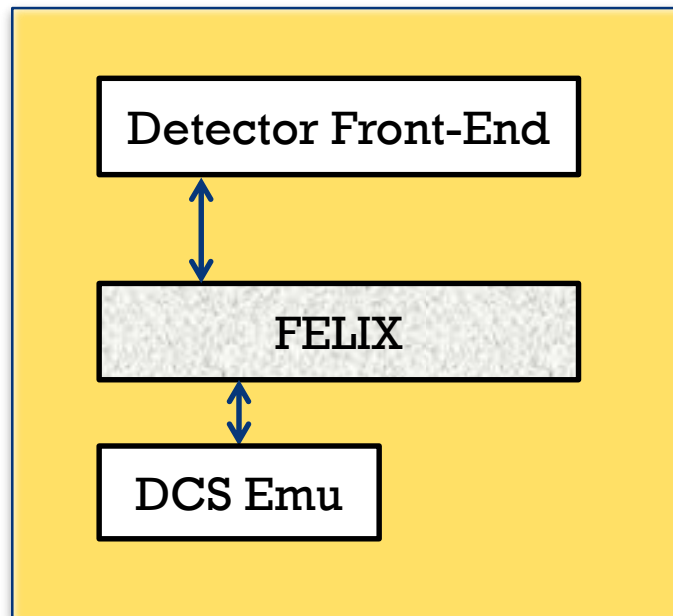
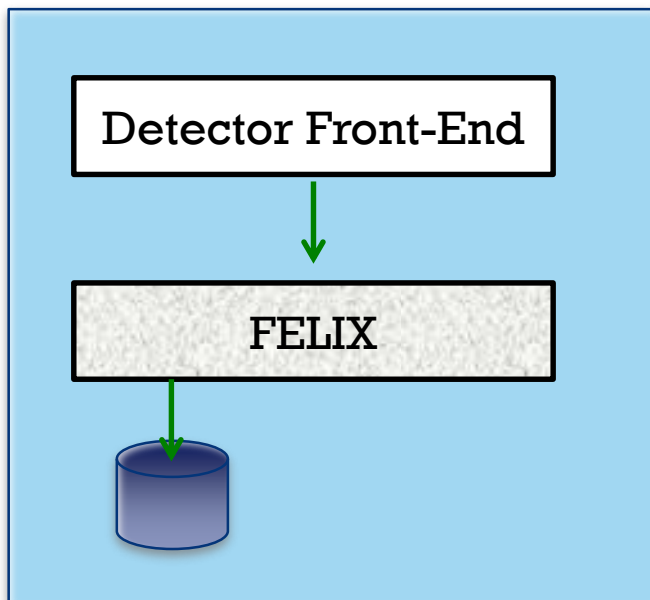
Tasks Breakdown

Giovanna Lehmann Miotto

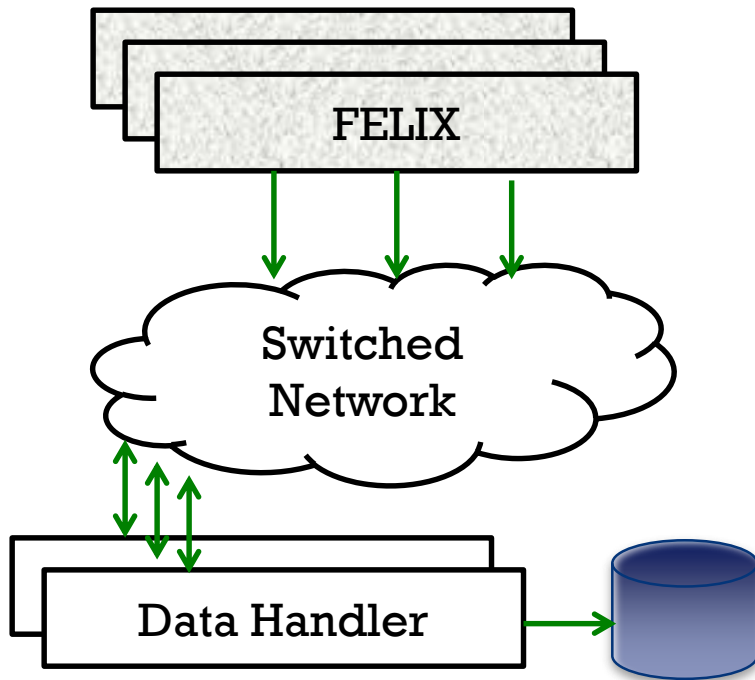
+ Introduction

- Draft subdivision into functional blocks
- Development needs
 - Hardware
 - Emulators
 - Data samples
- Many tasks can be worked on in parallel and independently
- Regular integration steps should be planned early on, to allow experts getting together and making fast progress

+ WIB - FELIX



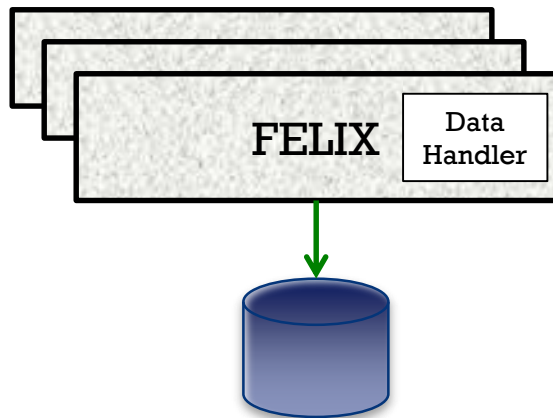
+ FELIX – Data Handler



- Implement Data Handler receiver and building into artDAQ super-fragment
- Evaluate data compression algorithms in Data Handler
- Evaluate possibility of doing data selection in Data Handler
- Fully debug FELIX output path

Can start NOW

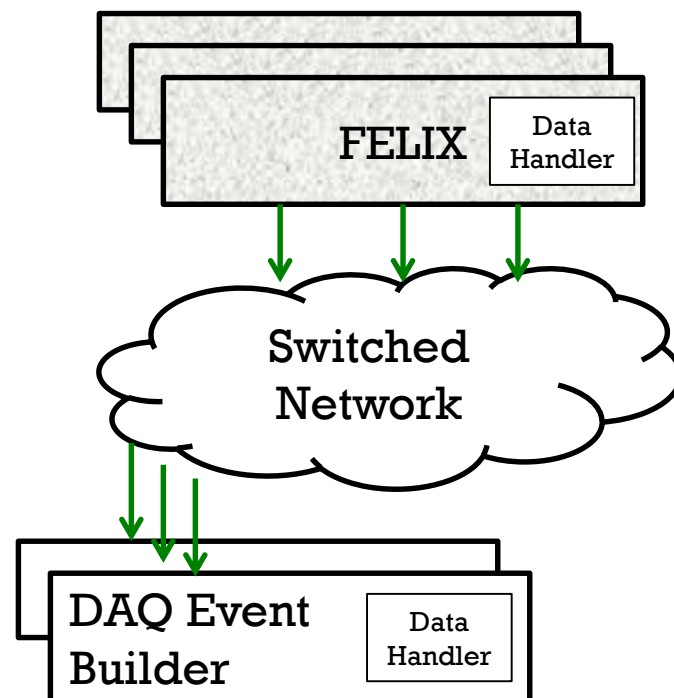
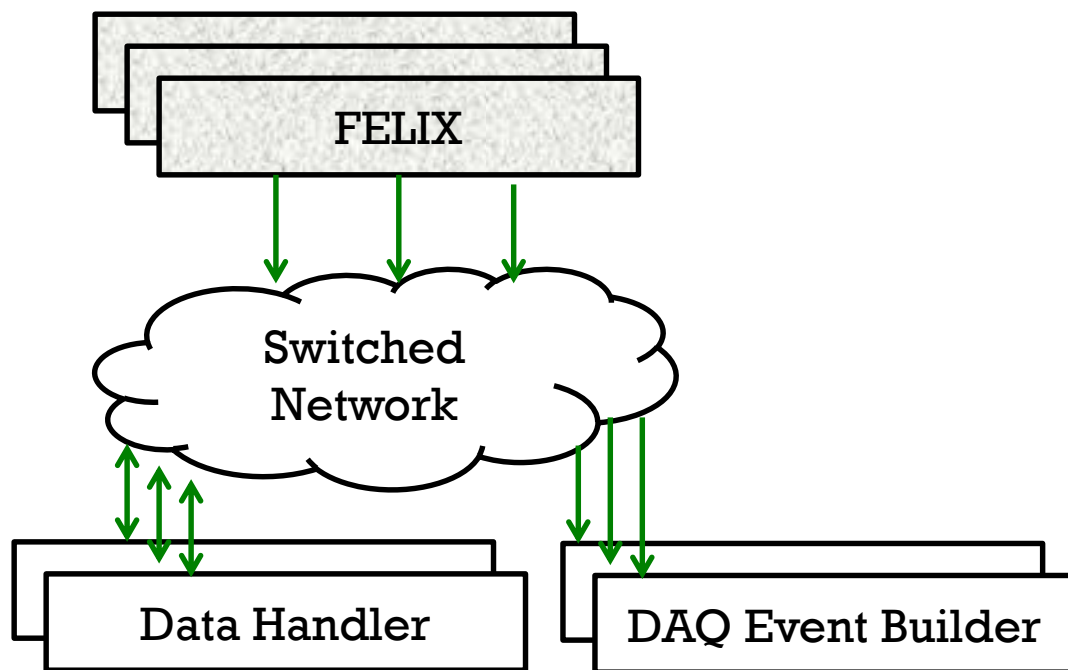
+ FELIX – Data Handler (2)



Requires careful thinking!

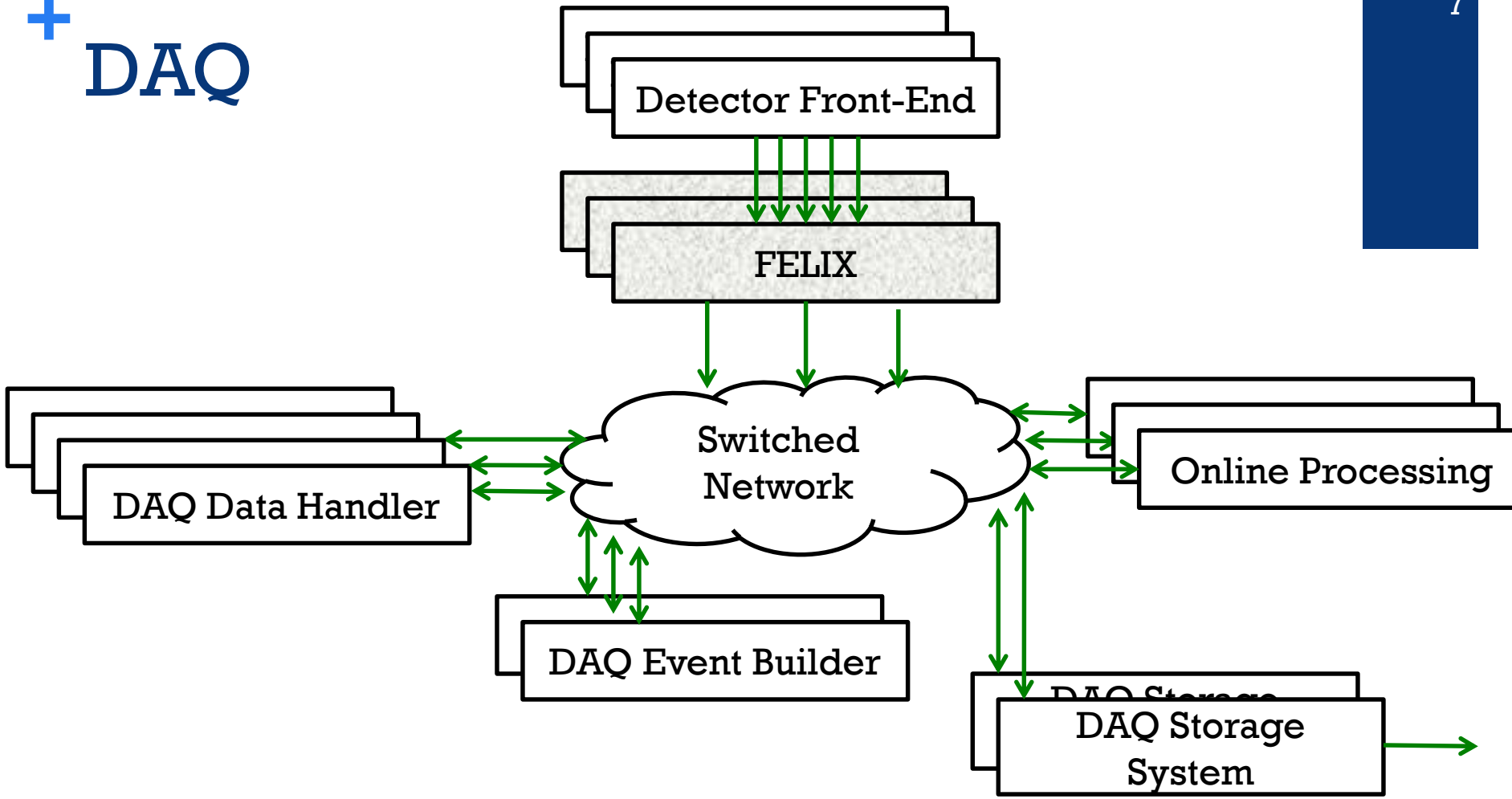
- Implement Data Handler receiver and building into artDAQ super-fragment
- Evaluate data compression algorithms in FELIX FPGA
 - Requires discussion with FELIX FPGA developers
- Evaluate possibility integrating Data Handler functionality into the FELIX server
 - May require modification of the routing FELIX application
- Assess whether this scenario is compatible with the other functions of FELIX

+ FELIX – Event Builder

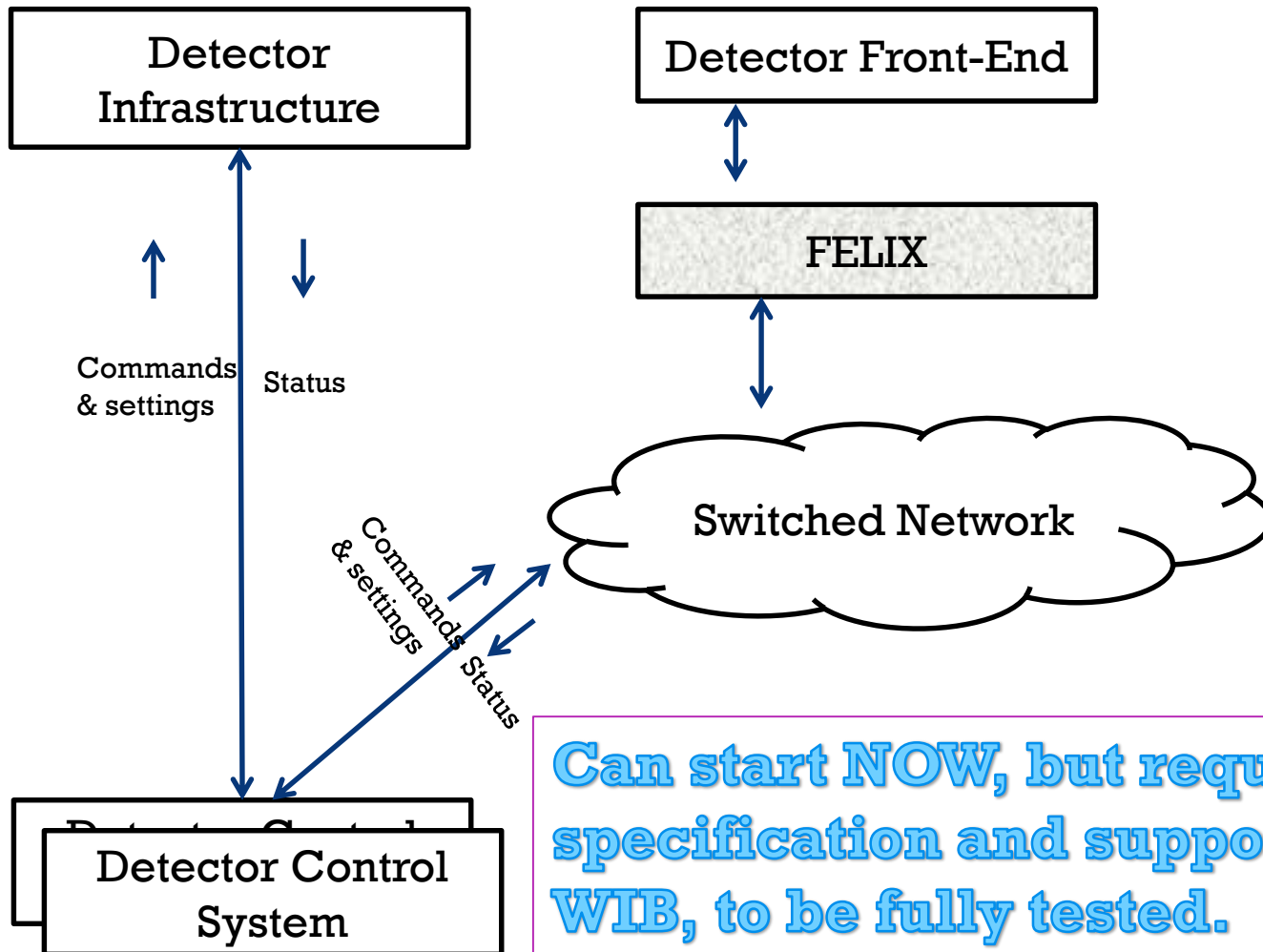


Working prototype summer 2016

+ DAQ

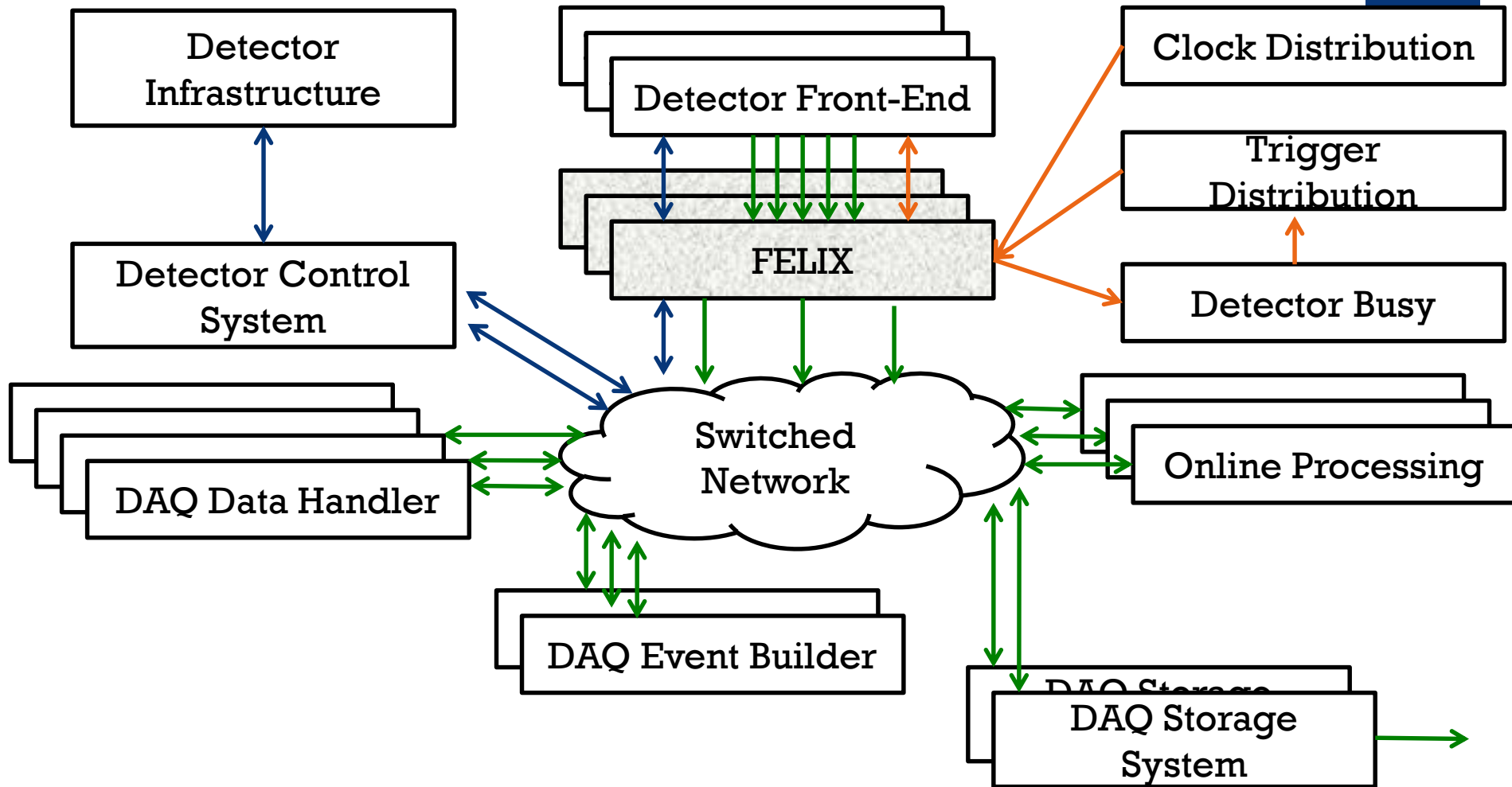


**Depends on overall DAQ status;
proof of principle may be done based other systems.**



Can start NOW, but requires DCS specification and support from WIB, to be fully tested.

+ Complete System



+ Summary

- Ready to start development effort now
- Good communication with the DAQ community needed in order to agree on data formats, protocols, etc.
- Collaboration with WIB developers essential
 - Establish which functions may be tested when
- FELIX development boards can be purchased (one already available at CERN for us)
- This is the right time to build a developers team with all interested parties and split the work among ourselves