# The SESAME XAFS fluorescence detector project

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XVI International Conference on Science, Arts and Culture International Conference on SESAME - in honour of Paolo Budinich

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#### Outline

#### Introduction

XAFS and the fluorescence detector

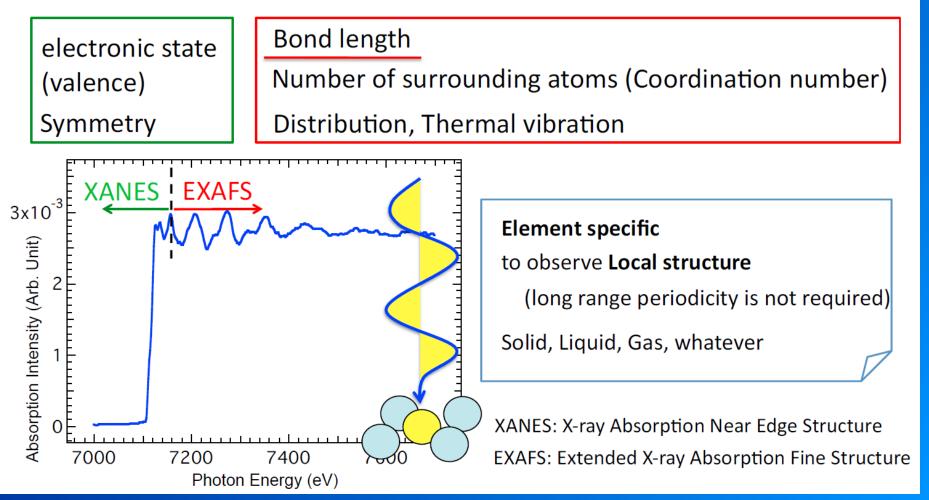
#### The SESAME fluorescence detector

- The detector design
- Results from a parallel development: TwinMic at Elettra

#### Conclusions

#### What is XAFS?

#### XAFS: X-ray Absorption Fine Structure



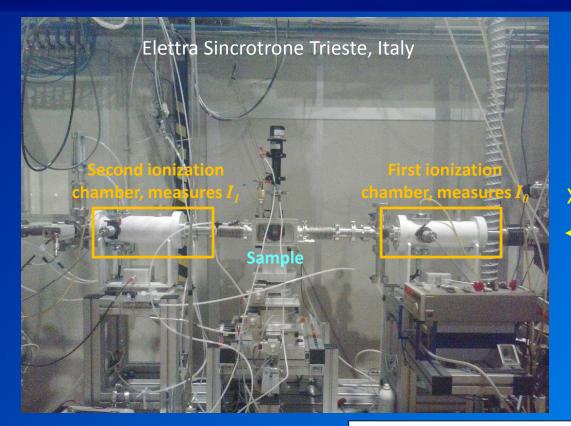
Source: Hitoshi Abe, *A Brief Introduction to XAFS* http://www.sesame.org.jo/sesame/images/News/Users\_meeting\_and\_JSPS\_School/jsps-lectures/Lec04\_Abe.pdf

## A typical XAFS beamline



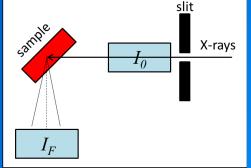
- The energy of the output X-ray beam is precisely set by a double crystal monochromator that provides an energy resolution  $\Delta E/E \approx 10^{-4}$
- The beam size at the sample is defined by the exit slits
- The X-ray flux is  $\approx 10^{11} 10^{12}$  ph/s·mm<sup>2</sup>

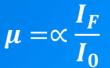
## A typical XAFS experimental station



X-ray beam  $\checkmark$  M Absorption coefficient  $\mu = ln \left( \frac{I_0}{I_1} \right)$ 

When the sample is thick but diluted or thin but concentrated, the measurement can be carried out in fluorescence mode





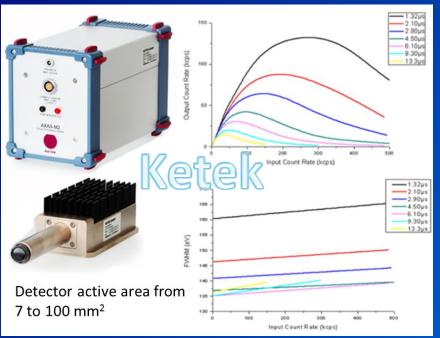
#### **XAFS fluorescence detector specs?**

In our context, a XAFS measurement is the measurement of the fluorescence flux from the sample during a very fine energy scan around the absorption edge of the material under examination, hence:

- The detector needs to have a good sensitivity in the whole energy range of the beamline → 3 - 30 keV for SESAME
- The energy resolution of the detector should be good enough to allow for rejection of undesired signals, i.e. scattered beam or other fluorescence lines close to the investigated one  $\rightarrow E_{RFS} < 150 \text{ eV FWHM} @ 5.9 \text{ keV}$
- The detector needs to cope with large fluxes of photons

   → small detection units to reduce pileup probability
- The geometric acceptance of the detector should be as large as possible to reduce the measurement time → multi-channel systems

### Some commercial systems





- Top performances SDD with active area from 5mm<sup>2</sup> to 100mm<sup>2</sup>.
- Equipped with the state-of-the-art Pulsed-reset Charge-preamplifier XGL-CPA-2100.
- Optionally includes the XGL-MCSH-3100 and the XGL POWER SUPPLY to set-up a complete stand-alone X-ray spectroscopic system.



- A range of silicon drift detector sizes, from 150 mm<sup>2</sup> to 20 mm<sup>2</sup>
- ${\bf X}\mbox{-}{\bf Max}^{N}$  provides a superb resolution that is independent of sensor size
- Excellent low energy analysis, including Be detection guaranteed on all sensor sizes
- Up to four X-Max^N can run in parallel on one microscope to create a system with a total active area of 600  $\rm mm^2$



- Available in thickness of 0.5 and 1 mm
- Available single detectors in 30, 40, 50, 65, 70, 80 mm<sup>2</sup>
- Large area, four-element silicon drift detector (total area of 120 mm<sup>2</sup> 200 mm<sup>2</sup>)

### **Multi-channel commercial solution**

	AND	nso:		sh		
Sensor	# of channels			1 to 19		
	Sensor type			SDD		
	Active area (mm²)	10	40	65	100	170
	Thickness			450µm		
	Collimated area (mm²)	7	30	50	80	150
	Resolution (eV)	<133	<133	<133	136	139
	P/B ratio			>10,000 : 1		
	FET or 'CUBE <sup>(2)</sup> ' primary stage			Option		
	Peltier cooled cryostat			Standard		
	Temperature stabilisation			Standard		

Interesting solution, but few drawbacks:

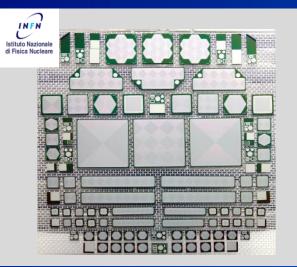
- large area easily obtainable with large sensors, difficult with smaller ones
- made up of single detector systems, each one requires a power supply and a MCA and a lot of cables
- bulky (not necessarily bad)

#### **SESAME detector concept**

The detector concept we proposed to SESAME is the result of a thorough discussion with the XAFS beamline scientists at Elettra, for which we are developing the same exact detector:

- a multi-channel system with cells as small as feasible to reduce pileup, the limit is due to the constraints on the hybrid layout
   → 9 mm<sup>2</sup> a good compromise
- the total sensitive area should allow at least an order of magnitude gain on the measurement time → 64 cells, 576 mm<sup>2</sup> (currently 80 mm<sup>2</sup>)
- the system should be segmented, rather then monolithic, to allow for redundancy → 8 units, each comprising a detection head and a backend PCB
- the system should be compact and should contain the whole acquisition chain including the MCAs → On board digital signal processing

#### Heritage – detector R&Ds



#### INFN (Trieste, Bologna and TIFPA)

- Silicon detector design: device simulation, layout, characterization, and assembly
- Expertise in optimizing large-area SDDs for applications on different research fields
- Expertise in design and construction of complex multi-channel detector systems
- Expertise in digital signal processing

#### FBK - Center for Materials and Microsystems

- Expertise in development of advanced solid-state sensors in dedicated technology processes
- Sensor design and simulations, physical design, tuning of the fabrication process, electrical and optical characterization
- 6" wafer microfabrication facility with a 500 m<sup>2</sup>, class 10/100 Clean Room and a 0.8 μm CMOS pilot line



### Heritage – ASICs and DAQ systems



#### Politecnico di Milano – DEIB

- Characterization of GaAs, CdTe, CZT, Si and SiC radiation detectors
- Design of ASICs for radiation detectors
- Low-noise Low-Power Electronics



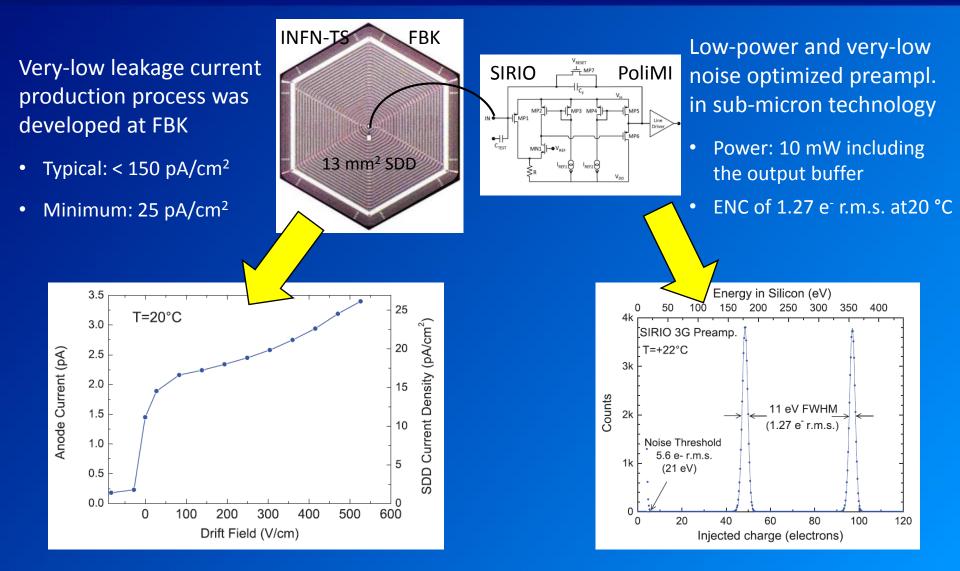
#### *Elettra – Instrumentation and Detectors Laboratory*

- Development of special instrumentation and boards for many applications: FAST STM scanning and acquisition electronics, LLRF systems and bipolar HV power supplies, picoammeters, electron energy analyzers and fast feedback electronics
- Deep knowledge of Synchrotron beamlines requirements

#### ICTP-INFN Microprocessor laboratory

- Expertise in design of FPGA based acquisition systems and digital signal processing for scientific instrumentation
- Expertise in training and education of researchers coming from developing countries

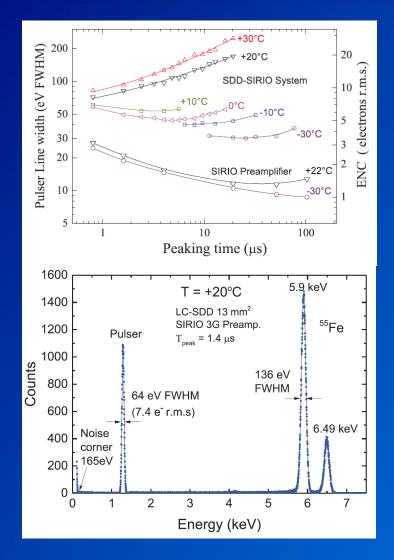
#### Starting point: state-of-the-art technology



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#### Room temperature spectroscopy in sight



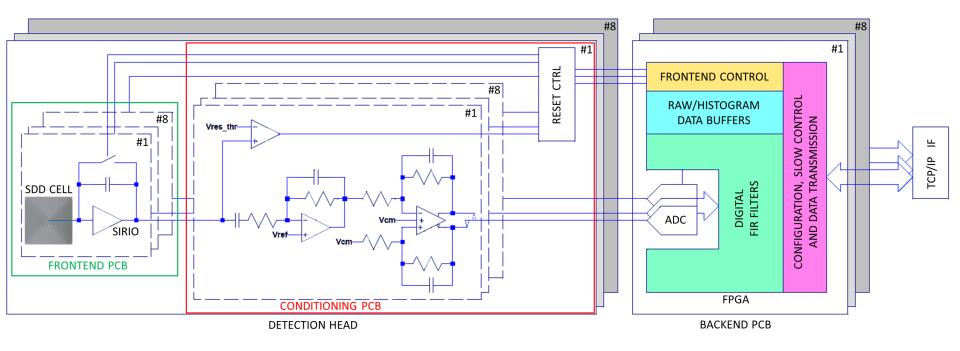
T (°C)	<sup>55</sup> Fe 5.9 keV LINE WIDTH (eV FWHM)	Pulser line width (eV FWHM)	ENC (e- r.m.s.)	lime			
+30	148	82	9.4	0.8			
+20	136	64	7.4	1.4			
+10	133	53	6.1	2.4			
0	129	44	5.0	4.8			
-10	129	41	4.7	9.6			
-30	123.7	29	3.3	19.2			
READOUT ELECTRONICS							
+22		11	1.27	51.2			
-30		8.7	1.0	102.4			

• Very-low noise preamplifiers allow the reduction of the shaping time, hence

- ✓ increased throughput
- ✓ less sensitivity to leakage current
- Very-low leakage detectors then allow to avoid bulky and power-hungry cooling systems

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#### **SESAME detector block scheme**

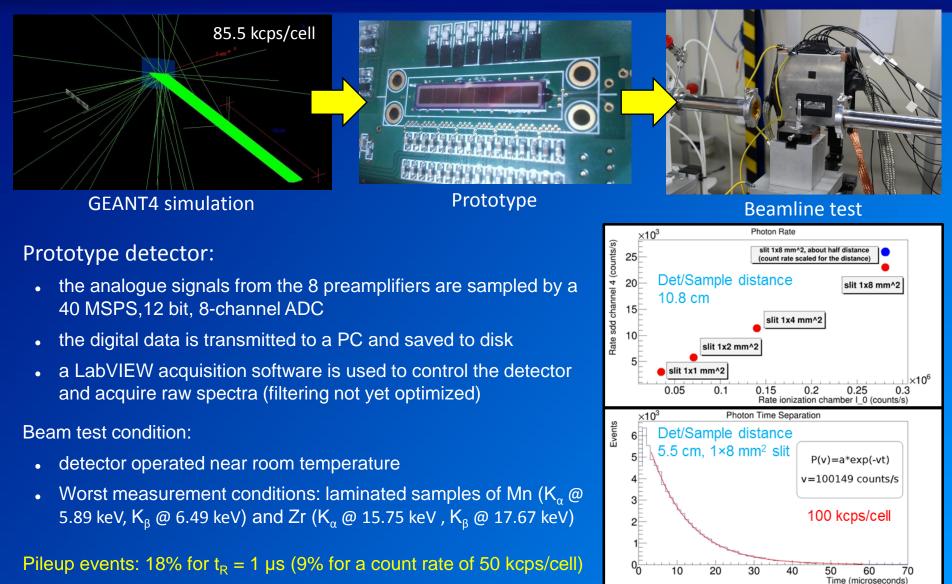


- Each unit comprises a detection head and a backend section
- The detection head hosts the frontend PCB and signal conditioning circuitry (CR-RC<sup>2</sup>)
- The detector is controlled by a PC through a TCP/IP connection

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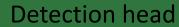
### **Detection head: sensor cell sizing**



### **Detection head design**



- Improved detector layout:
  - better defined sensitive area
  - on-board thermistors for temperature control
- Tungsten collimator to minimize "split" events
- New SIRIO preamplifier prototypes optimized for SESAME now under test at Politecnico di Milano
  - A low frontend PCB profile allows for compact stacking of detection heads
- Thermoelectric cooling is employed to reduce the leakage current



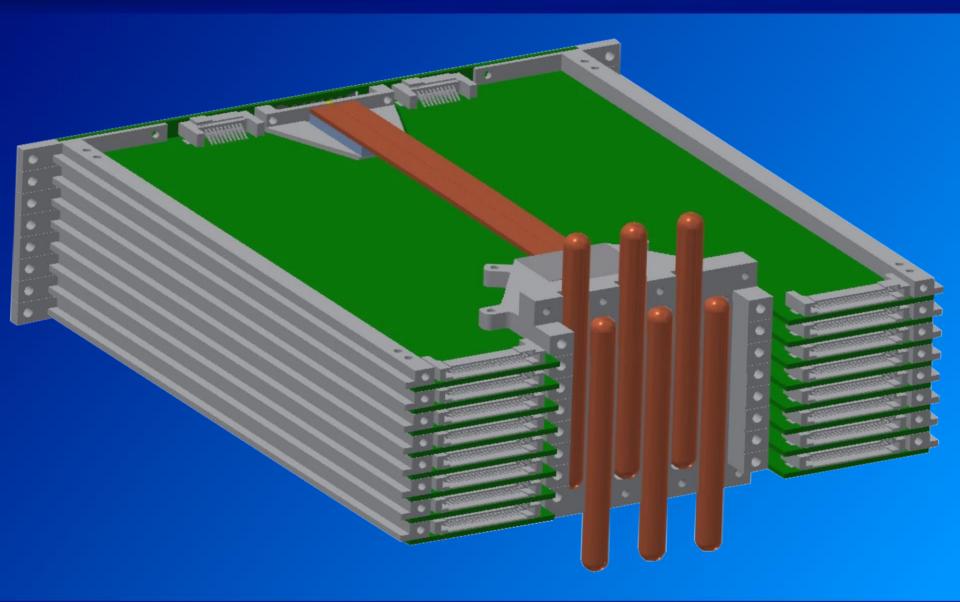
Peltier cell

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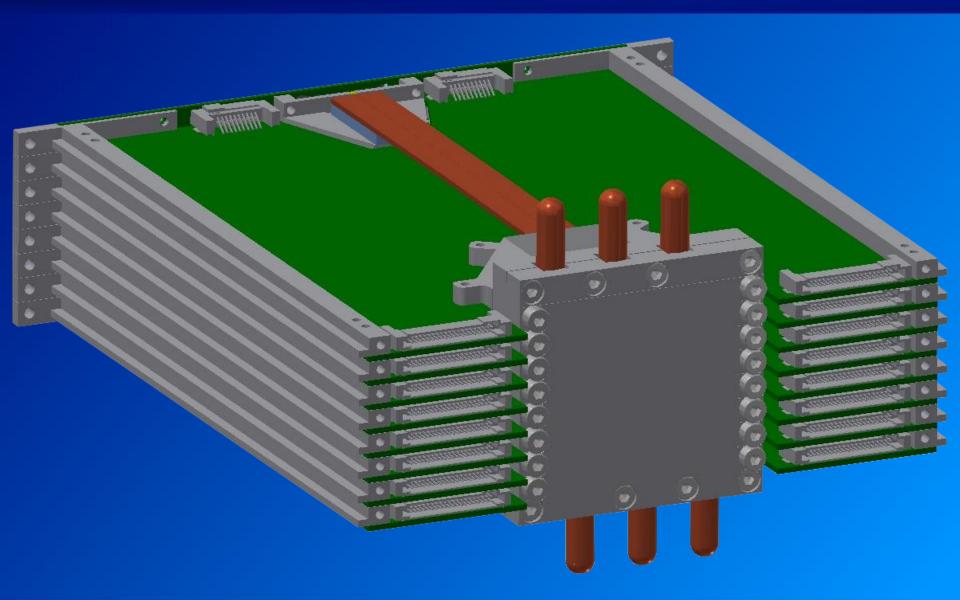
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Heat pipes

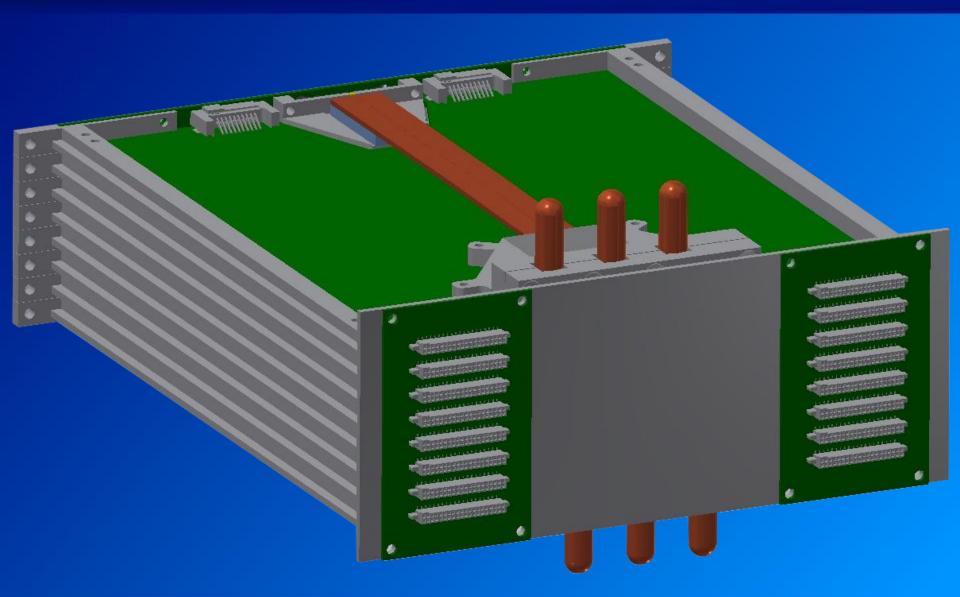
### **Detection head stack**



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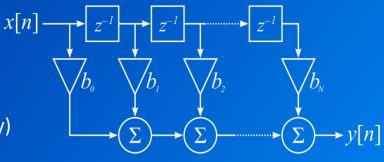
#### **Backend electronics**

#### • Its purposes are

- the configuration of the instrument
- the digitization and elaboration of the analog signals
- the acquisition of histograms or raw data for system analysis
- the communication with the controlling computer
- It features an 8 channel, 40 MSPS, 12 bit ADC. Depending on the conditioning circuit bandwidth it can allow for shaper rise times as fast as 250 ns
- Noise filtering is accomplished by means of a low-power, high-performance ALTERA Cyclone 5 FPGA, which implements also the data buffers and the configuration and control logic
- It features an Ethernet port (possibly in a separated board) for network communication with a host computer

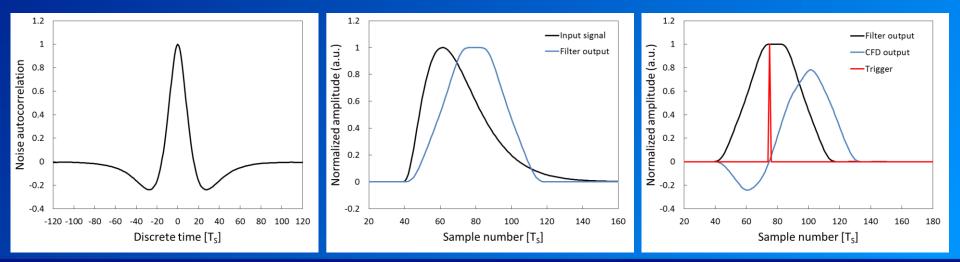
# **Digital filtering**

- Several options available, we selected the Finite Impulse Response (FIR) class because of its flexibility
- Possible to constrain the filter synthesis
  - accounting for the input signal noise
  - optimizing the output shape (duration, flat top, symmetry)
  - requiring rejection of low frequency pedestal variations
  - minimize the ADC quantization noise



 $y(n) = b_0 \cdot x(n) + b_1 \cdot x(n-1) + \cdots + b_N \cdot x(n-N)$ 

• Digital filtering allows for exotic trigger functions, e.g. constant fraction discrimination

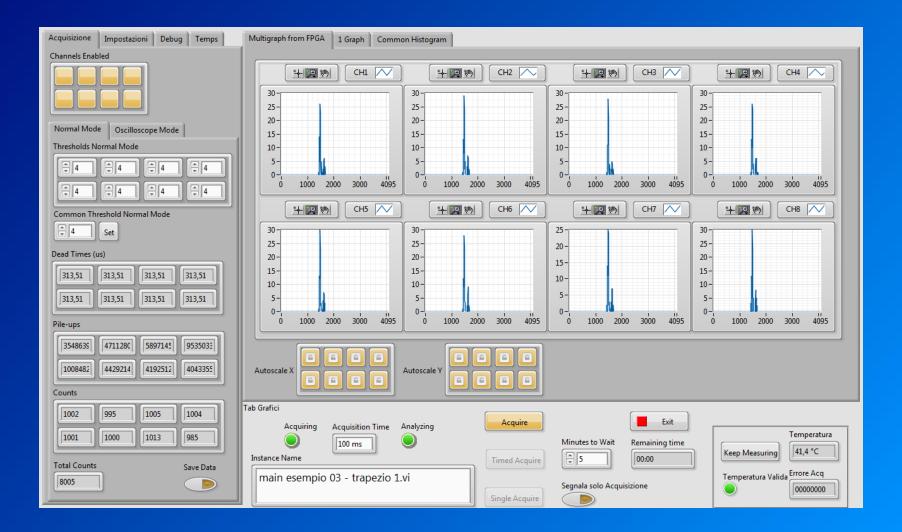


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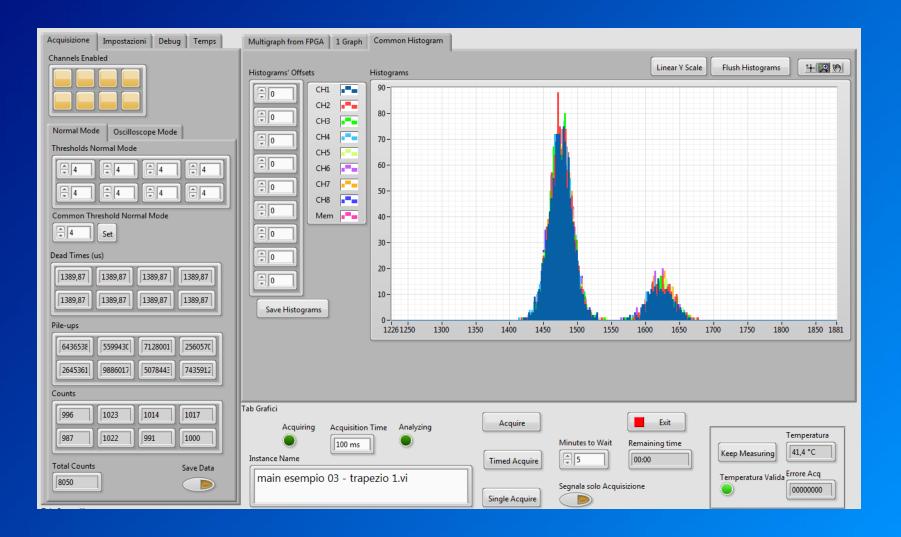
### LabVIEW DAQ – detector setup

Acquisizione Impostazioni Debug Temps
Address
127.0.0.1 Set Save File
Port
Disconnect VTEST Double VTEST Set Save Folder PD
CFD Delay
CFD Baseline
Reset-Frequency Reset-Durata Reset-Attesa
500 Hz 0,5 us 0,5 us
Reset from External Trigger
Filter Binary Cut Attesa per Reiezione Pileup
Save Settings Load Settings Change Filter

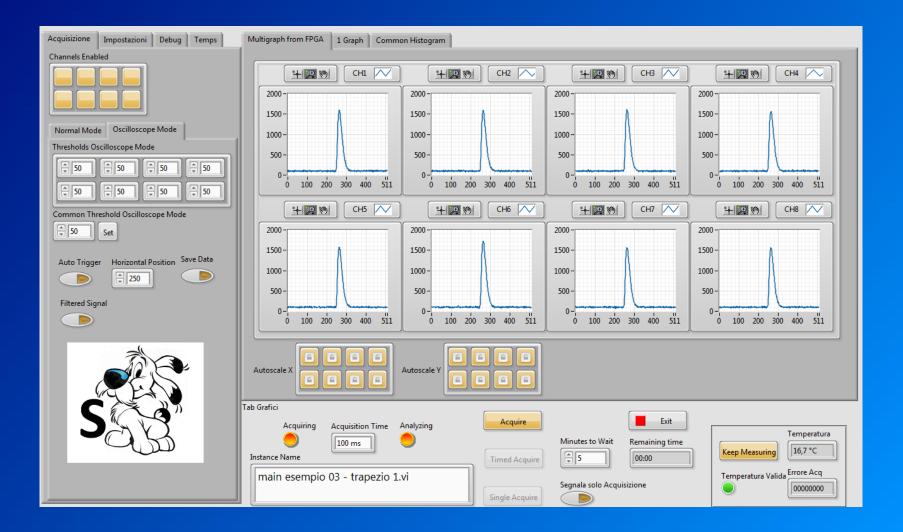
#### LabVIEW DAQ – normal mode



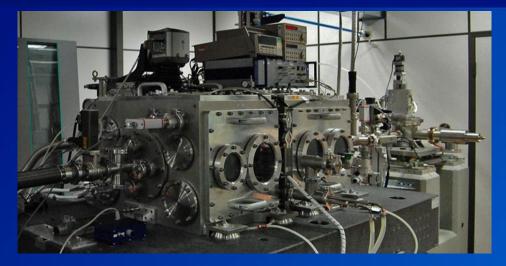
## LabVIEW DAQ – histogram mode



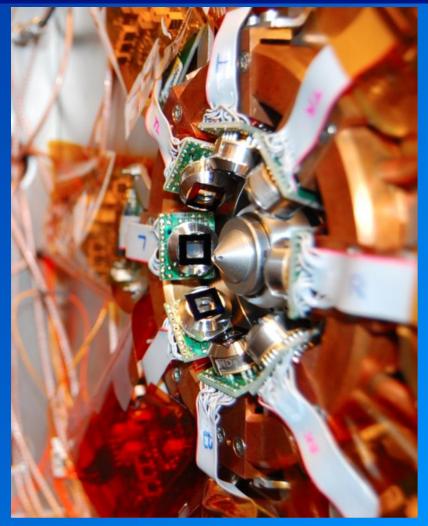
#### LabVIEW DAQ – oscilloscope mode



## **Development for Elettra/TwinMic**



- Combination of scanning and full-field imaging in a single instrument
- Energy range 400 2200 eV, lateral resolution of 30 nm – 1 mm
- Goal of the project: an order of magnitude reduction of the measurement time
  - Improved measurement throughput
  - Reduced damage to the sample
  - Alternatively, acquire larger maps



8× PNSensor SDDs (collimated 20 – 30 mm<sup>2</sup>) arranged on a copper ring used for cooling

#### The new TwinMic detector system



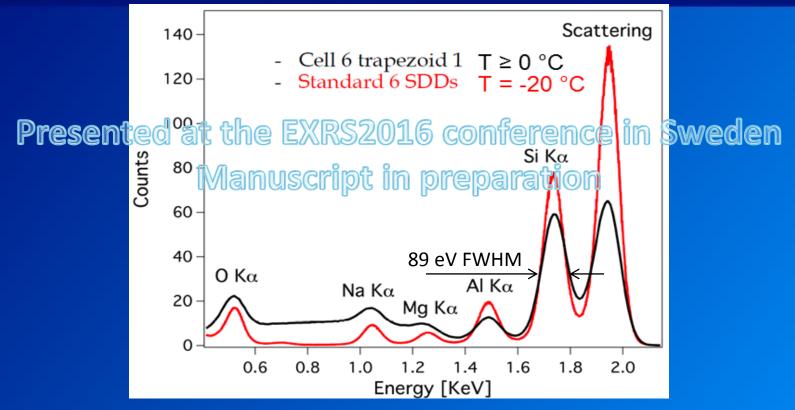
 6 <u>not collimated</u> hexagonal pixels (total area of 182 cm<sup>2</sup>) read out by SIRIO preamplifiers

- 8× analog CR-RC<sup>2</sup> filters
- 8× ADC 12 bit 40 MSPS
- FPGA Cyclone 5

# concept of SESA

Four detectors mounted in the experimental chamber in vacuum conditions; for the test the distance from the sample was not yet optimal

#### **Beamline measurements**



Si-Al-Mg-Na-O spectra of a *euphorbia pityusa* plant section acquired with one cell of the new TwinMic detector and with 6 SDDs of the standard system

- Optimized digital filtering not yet available at that time,  $t_R = 0.95 \ \mu s$
- Relevant background below 1.4 keV (electrons extracted from the sample?)
- Successive test with new mechanical support showed a doubling of the rate

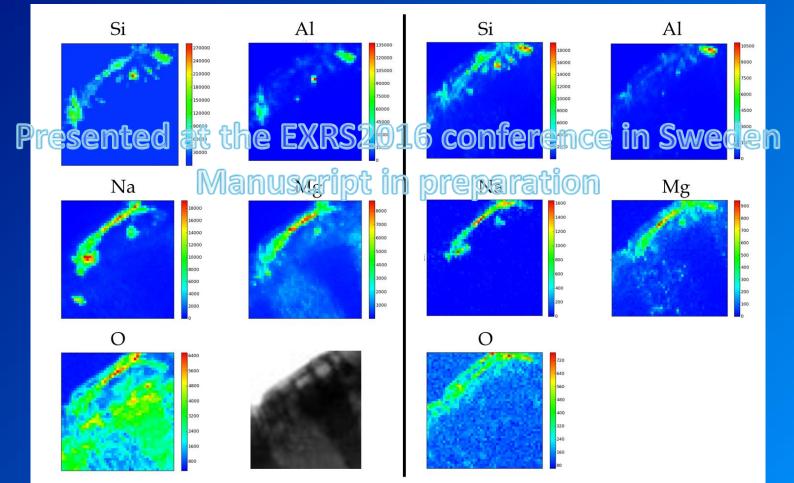
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## **Beamline measurements**

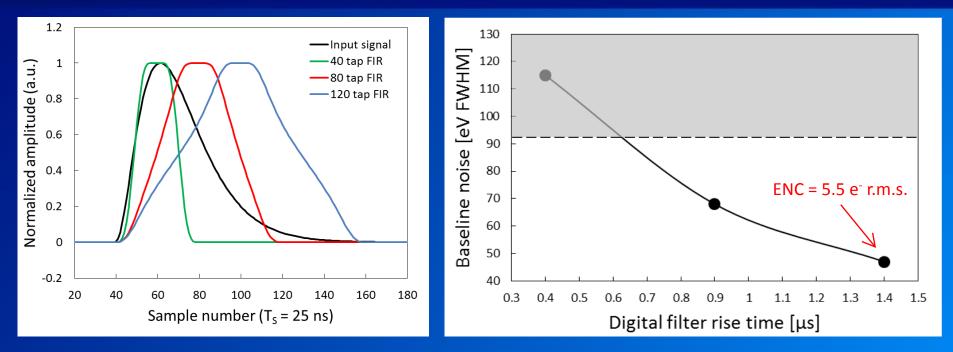
#### 6 cells of the standard detector

#### Cell #1 of trapezoid #1



Elemental maps of the *euphorbia pityusa* plant section together with the corresponding absorption image

### Laboratory measurements at 0 °C



- Energy calibration with a <sup>55</sup>Fe radioactive source
- Digital filter coefficients optimized considering the noise autocorrelation function
- Distortion present on the analog signal  $\rightarrow$  noise evaluated from the baseline fluctuations
- Fast filter worsens the resolution of the analog pre-shaper because of the unmatched frequency bands, the other two filter performances are within SESAME specifications

#### Conclusions

- The SESAME XAFS fluorescence detector under development is a state-ofthe-art instrument:
  - it will sustain a rate of at least 50 kcps/cell (≈ 560 kcps/cm<sup>2</sup>)
  - It will have a total sensitive area of 576 mm<sup>2</sup> allowing for a total rate of at least 3.2 Mcps
  - It will have an energy resolution < 150 eV FWHM @ 5.9 keV, 0 °C with a peaking time  $\leq 1~\mu s$
- The detector concept is being tested and optimized by way of two parallel developments for Elettra Sincrotrone Trieste (XAFS and TwinMic beamlines) with good results
- The timeframe set for this project forced us to consider conservative solutions, nonetheless further optimizations and improvements of such detector are possible and will be pursued in future developments