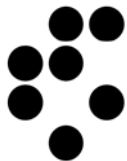




FPGA acceleration of Model Predictive Control for ITER Plasma current and shape control

Samo Gerkič, Boštjan Pregelj, Matija Perne
Jožef Stefan Institute, Ljubljana, Slovenia



SLOVENIAN RESEARCH AGENCY

Eurofusion AWP15-ENR-01/JSI-02 "FMPCFMPC"



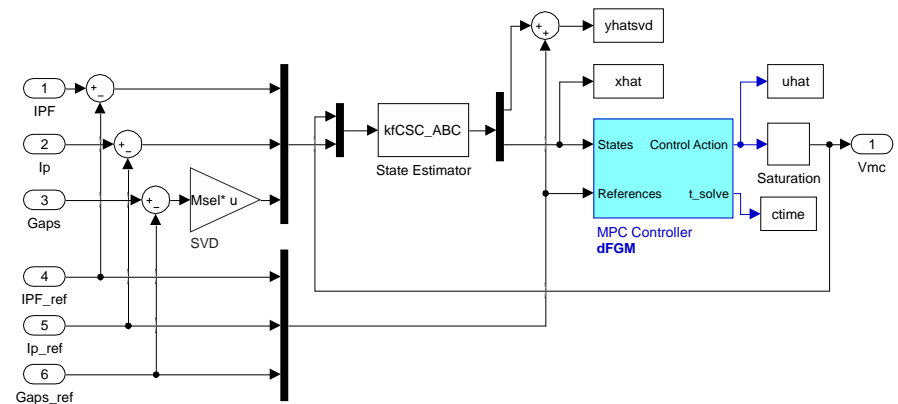
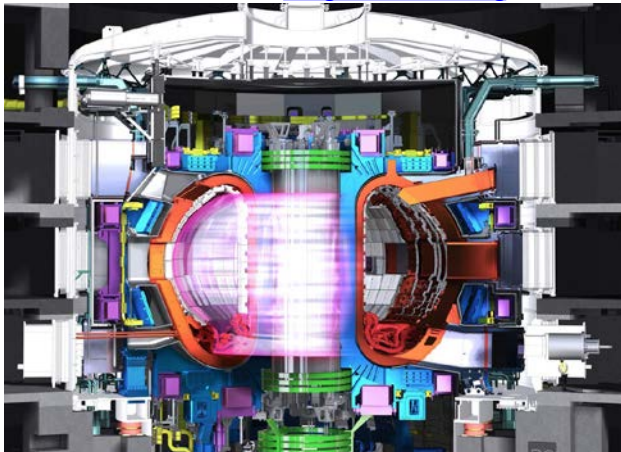
This work has been carried out within the framework of the EUROfusion Consortium and has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement number 633053. The views and opinions expressed herein do not necessarily reflect those of the European Commission.

Overview



- Plasma Current and Shape Controller (PCSC) for ITER
- Singular Value Decomposition (SVD) -based Model Predictive Control (MPC) using
- a dual Fast Gradient Method (dFGM) quadratic programming (QP) solver
- FPGA acceleration with a High-Level Synthesis (HLS) approach

Image: www.iter.org





- **MPC Quadratic Program** – hard input and soft state constraints

$$\min_{\mathbf{z}, \mathbf{s}} \frac{1}{2} \mathbf{z}^T \mathbf{H} \mathbf{z} + \mathbf{c}^T \mathbf{z} + \frac{1}{2} \mathbf{s}^T \mathbf{W} \mathbf{s} + \mathbf{w}^T \mathbf{s}$$

$$\text{subject to} \quad \begin{aligned} \mathbf{C}_x \mathbf{z} &\leq \mathbf{b}_x + \mathbf{s} \\ \mathbf{C}_u \mathbf{z} &\leq \mathbf{b}_u \\ \mathbf{s} &\leq \mathbf{0} \end{aligned}$$

- **Dual Fast Gradient Method**

$$\begin{aligned} \mathbf{v}^k &= \mathbf{v}^k + \beta^k (\mathbf{v}^k - \mathbf{v}^{k-1}) \\ \mathbf{y}^k &= -\mathbf{H}^{-1} (\mathbf{C}^T \mathbf{v}^k + \mathbf{c}) \\ \mathbf{v}^{k+1} &= \mathbf{v}^k + \mathbf{C} \mathbf{y}^k - \widehat{\text{prox}}_{h, \mathbf{W}, \mathbf{w}} (\mathbf{v}^k + \mathbf{C} \mathbf{y}^k) \end{aligned}$$

$$\widehat{\text{prox}}_{h, \mathbf{W}, \mathbf{w}}(\mathbf{t})_i = \begin{cases} t_i & \text{if } t_i \leq b_i \\ b_i & \text{if } t_i > b_i \text{ and } i \text{ hard} \\ b_i & \text{if } b_i + w_i \geq t_i > b_i \text{ and } i \text{ soft} \\ \frac{t_i + W_{ii} b_i - w_i}{W_{ii} + 1} & \text{if } b_i + w_i < t_i \text{ and } i \text{ soft} \end{cases}$$

Iterative algorithm

Microparallelization

(matrix-vector multiplication within iterations)

Several approaches to speeding up $\mathbf{M} \cdot \mathbf{v}$ multiplication

TL;DR: Speed-up via HLS achieved but not automatically