Overview and performance of the ATLAS Level-1 Topological Trigger





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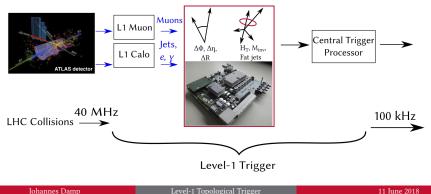


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- ATLAS Trigger system: Selection of interesting events
- Hardware-based first Level-1 Trigger
- L1 Topological Processor: trigger decision based on topological algorithms



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### L1 Topological Processor



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### Overview and performance of the ATLAS Level-1 **Topological Trigger**



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### ATLAS trigger: selects 1 kHz of collision data events for offline analysi Level-1 tripper: latency of 2.5 us, maximum output rate of 100 kHz. Level-1 Topological Processor (L1Topo): part of the Level-1 trigger. developed to deal with increasing luminosity and energy. -2018 LHC: Collisions (40 MHz, 13 TeV, Lumi s 2.14-39"cm

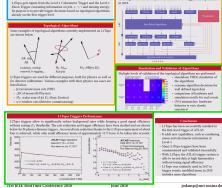


1 Thits-1 of input data with a fixed latency of ~200 ns. The imput data are received via optical fibres, transformed into electrical signals and then directed into the processor the topological algorithms are

Two algorithm types are applied to reduce the number of input objects without losing important event information

· A sort algorithm creates a list of the six leading particles

· A select algorithm creates a list of 10 particles above an E<sub>2</sub> threshold These shortened lists are then flexibly combined into various topological dein 2018.



[1] L Rin, The ATLAS Level-1 Topological Trigger performance in Run 2, ATL-DAQ-PROC-2017-002(2017)

[2] ATLAS Collaboration, https://cern.ch/twiki/bin/view/AtlasPublic/TriggerOperationPublicResults

### L1 Topo Functionality and Hardware

## **Topological Algorithms**

## L1Topo in Action