Single Photon Source Driver Designed in ASIC

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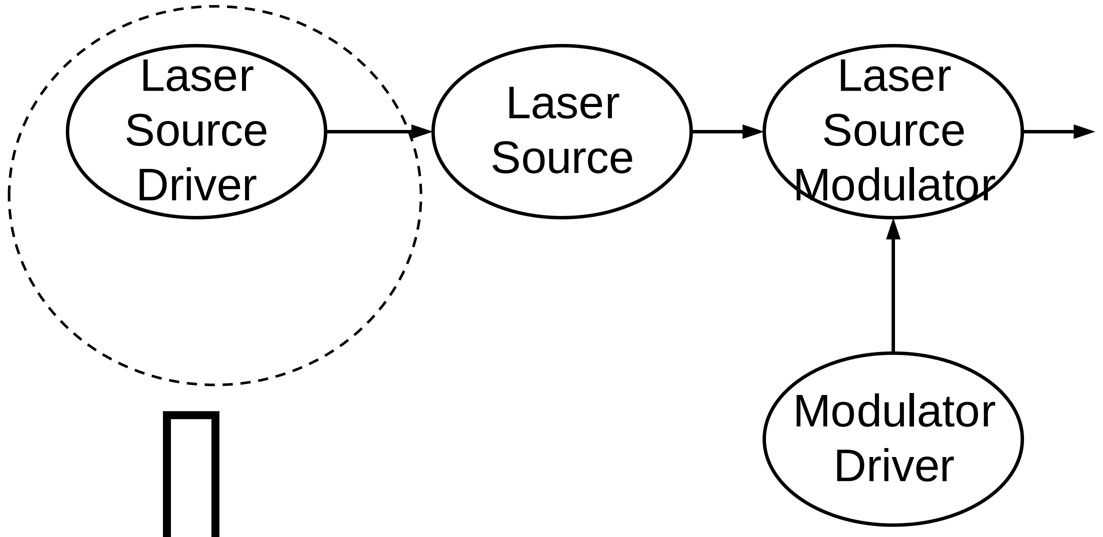
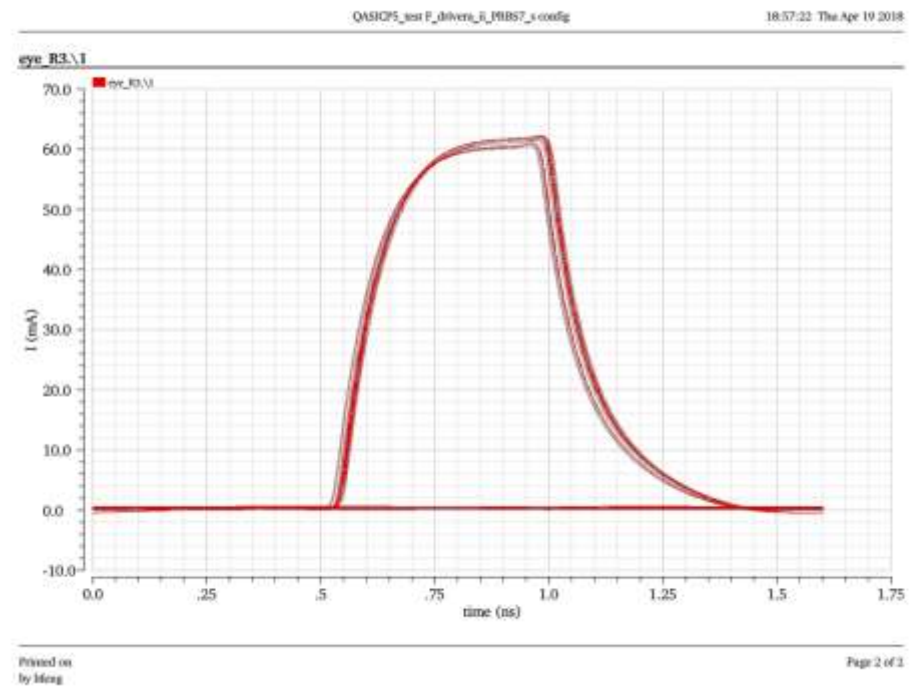


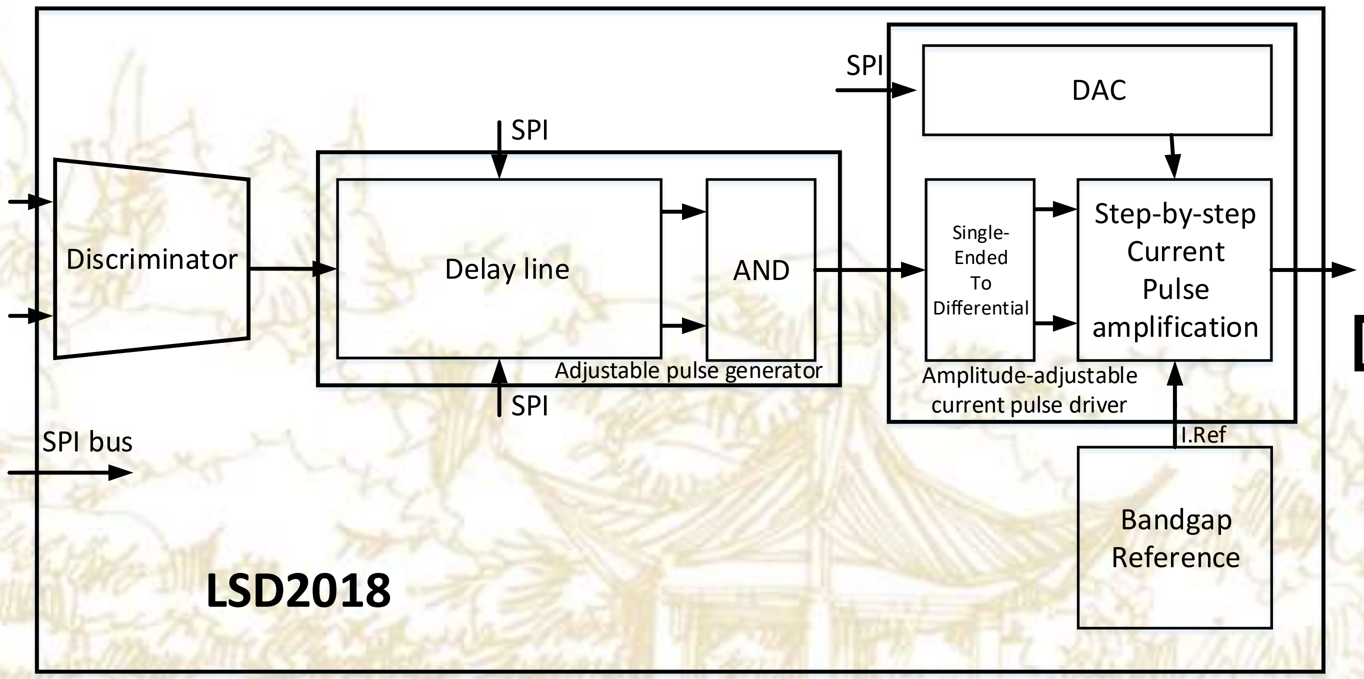
The structure of Alice`s front-end electronic in the QKD system

	Design indicators	Simulation results
Maximum output frequency	**625MHz**	**625MHz**
Minimum output pulse width	**400ps**	**400ps**
Output current amplitude	**20mA-100mA**	**20mA-120mA**

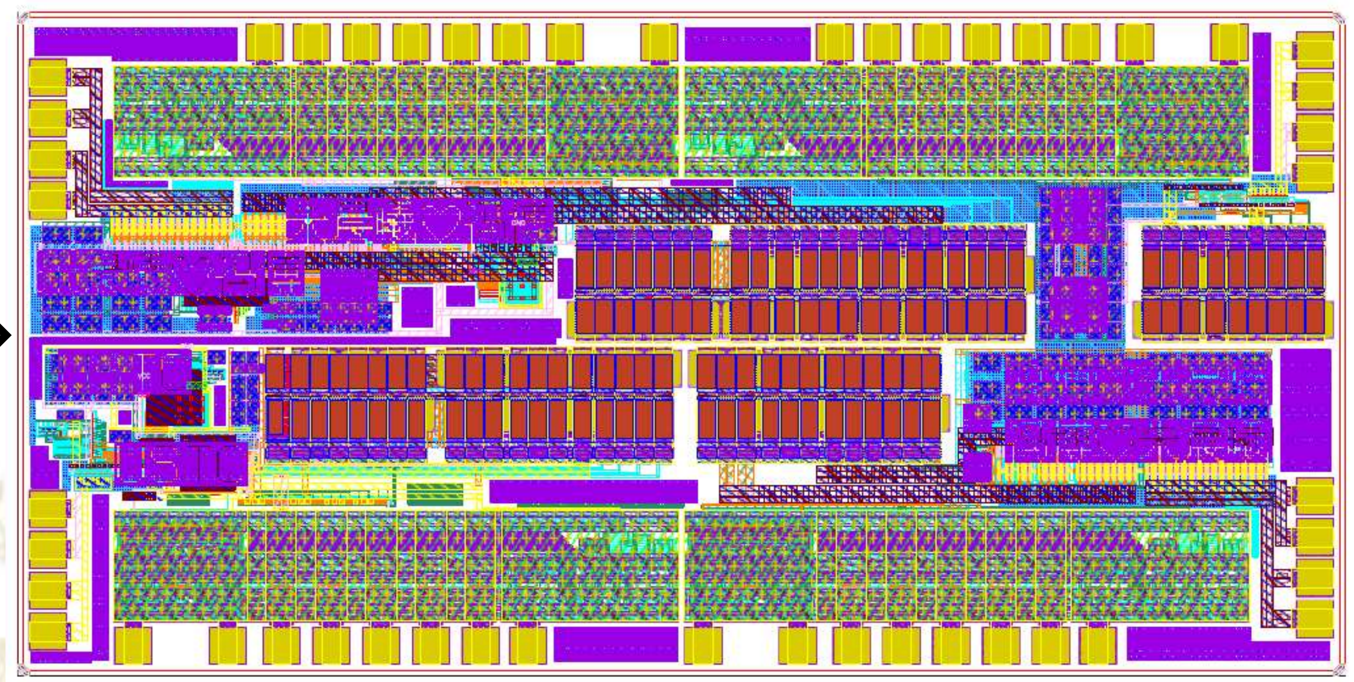
The project indicators and the simulation results of the LSD2018



An eye diagram of the simulation of the LSD2018

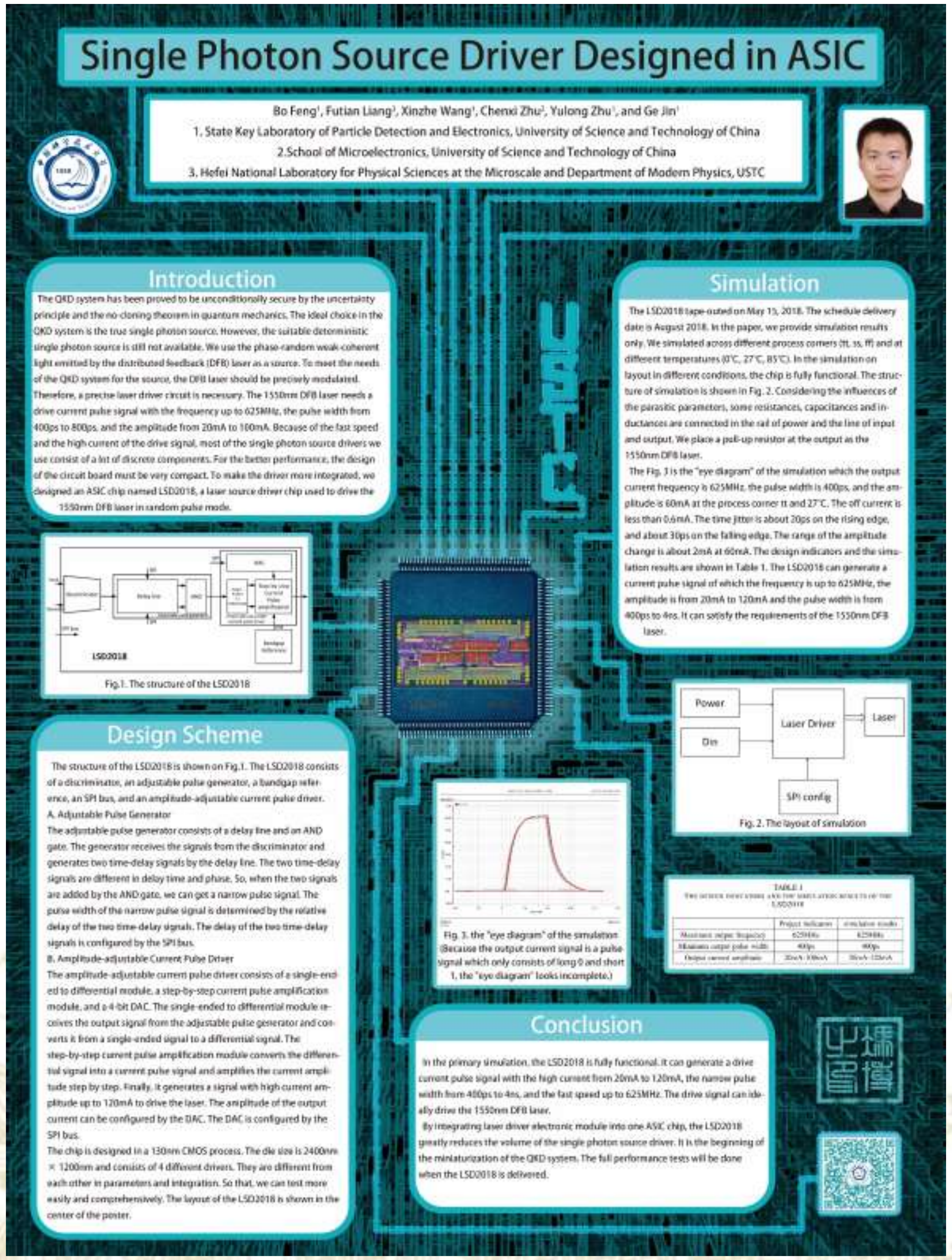


The structure of the LSD2018



The layout of the LSD2018

Poster



Introduction

Simulation

Design Scheme

Conclusion