

21st IEEE Real Time Conference - Colonial Williamsburg

Monday 11 June 2018

Minioral 1 (15:00-16:10)

-Conveners: Martin Grossmann; Rejean Fontaine

time	[id] title	presenter
15:00	[616] 388 Web-based Real Time Monitoring with HTML5	RITT, Stefan
15:02	[615] 387 A programmable clock generator for automatic Quality Assurance of LOCx2	Mr FENG, Yi
15:04	[617] 396 Design of Voltage Pulse Control Module for Free Space Measurement-Device-Independent Quantum Key Distribution	ZHANG, Sijie
15:06	[619] 403 Application of FPGA Acceleration in ADC Performance Calibration	YUAN, guangyuan
15:08	[620] 407 The Electronics Design of Error Field Feedback Control System in KTX	Mr XU, Tianbo
15:10	[621] 408 The Proton Beam Realtime Monitor System in CSNS	Dr ZHUANG, Jian
15:12	[622] 422 Real-Time Betatron Tune Correction with the Precise Measurement of Magnet Current	KURIMOTO, Yoshinori
15:14	[623] 425 Overview and performance of the ATLAS Level-1 Topological Trigger	DAMP, Johannes Frederic
15:16	[630] 455 A new high speed, Ultrascale+ based, board for the ATLAS jet calorimeter trigger system	DAMP, Johannes Frederic
15:18	[625] 442 The Phase-1 Upgrade for the Level-1 Muon Barrel Trigger of the ATLAS Experiment at LHC	IZZO, Vincenzo
15:20	[626] 443 Scanning Test System of p/sFEB for the ATLAS Phase-I sTGC Trigger Upgrade	Ms WANG, Xinxin
15:22	[628] 450 Longitudinal Mode-by-Mode Feedback System for The J-PARC Main Ring	SUGIYAMA, Yasuyuki
15:24	[629] 453 The design and performance of the ATLAS Inner Detector trigger in high pileup collisions at 13 TeV at the Large Hadron Collider	BEAUCHEMIN, Pierre-Hugues
15:26	[636] 473 Real time data analysis with the ATLAS trigger at the LHC in Run-2	BEAUCHEMIN, Pierre-Hugues
15:28	[632] 467 A readout method based on 10 gigabit Ethernet for silicon pixel detector	LI, HangXu
15:30	[633] 468 Technique of active phase stabilization for the interferometer with 128 actively selectable paths	XU, Yu
15:32	[634] 470 Single photon source driver designed in ASIC	FENG, Bo
15:34	[631] 458 The Design and Testing of the Address in Real Time Data Driver Card for the Micromegas Detector of the ATLAS New Small Wheel Upgrade	YAO, Lin
15:36	[637] 477 Control and Readout Software in Superconducting Quantum Computing	Dr LIN, Jin
15:38	[642] 496 Scalable Self-Adaptive Synchronous Triggering System in Superconducting Quantum Computing	Dr LIN, Jin

15:40	[638] 478 Clock Distribution and Readout Architecture for the ATLAS Tile Calorimeter at the HL-LHC	CARRIO ARGOS, Fernando
15:42	[641] 492 OpenCL implementation of an adaptive disruption predictor based on a probabilistic Venn classifier	Mr BERNAL, Enrique
15:44	[643] 504 Real-time data compression for data acquisition systems applied to the ITER Radial Neutron Camera	SANTOS, Bruno