

21st IEEE Real Time Conference - Colonial Williamsburg

Wednesday, June 13, 2018

Minioral 3 (11:10 AM - 12:20 PM)

-Conveners: **Pierre-Andre Amaudruz; Martin Grossmann**

time	[id] title	presenter
11:10	407 [389] Design of a Non-vacuum-cooling compact scientific CCD camera	Mr FENG, Yi
11:12	408 [397] Design of 32-channel TDC Based on Single FPGA for μ SR Spectrometer at CSNS	DENG, Fanshui
11:14	409 [398] The Readout Supervisor firmware for controlling the upgraded LHCb detector and readout system.	ALESSIO, Federico
11:16	417 [426] Timing and clocking scheme in the upgraded LHCb detector	ALESSIO, Federico
11:18	411 [412] Design of Front End Electronics for direct dark matter detection based on LAr	Mr ZHU, Xing
11:20	412 [415] Design of Readout Electronics for CEPC Semi-Digital Hadronic Calorimeter Pre-research	WANG, Yu
11:22	413 [416] A new all-digital background calibration technique for time-interleaved ADC using first order approximation FIR filters	Mr HU, Jiadong
11:24	414 [418] The Detector System Design for the Grating-based Phase Contrast Imaging CT Prototype	Dr SUN, Rongqi
11:26	415 [419] A data transmission system for the phase contrast X-ray human computed tomography prototype	Dr SUN, Rongqi
11:28	416 [423] JUNO DAQ Readout and Event Building Research	Dr ZENG, Tingxuan
11:30	410 [410] An ultra-sensitive balanced detector with low noise for continuous-variable quantum key distribution	LU, qiming
11:32	418 [429] A Front-end Signal Digital Acquisition System In Intensive Electromagnetic Field Circumstance	Dr ZHOU, He
11:34	419 [430] Design of readout electronics of scintillators and SiPMs for CEPC ECAL prereseach	ZHAO, Shensen
11:36	410 [431] An SOA based Design of JUNO DAQ Online Software	LI, Jin
11:38	411 [432] Nuclear Pulse Charge Measurement with a Method of Time over Linear Threshold	Mr SONG, Zhengqi
11:40	412 [436] A Design of FPGA Based Small Animal PET Real Time Digital Signal Processing and Correction Logic	Mr LU, Jiaming
11:42	413 [446] A General Purpose FPGA-based Programmable Digital Patter Generator	RUSSO, stefano
11:44	415 [457] Design of Mesh-Signal Readout Electronics for PandaX-III prototype TPC	Mr CHEN, Zhen
11:46	416 [461] Prototype of Front-end Electronics for PandaX-4ton Experiment	Mr WANG, Shuwen
11:48	417 [465] Quality Evaluation Electronics for CBM-TOF Super Module	LI, Chao
11:50	418 [466] Development of a 256-channel Time-of-flight Electronics System For Neutron Beam Profiling	Mr CHEN, Haolei

11:52	[479] 474 The customization of White Rabbit for different applications	Dr GONG, Guanghua
11:54	[480] 483 Time of flight Measurement Electronics for Back-n at CSNS	YU, Tao
11:56	[481] 487 A Zynq – based flexible ADC architecture combining real-time data streaming and transient recording	RIGONI, Andrea
11:58	[482] 500 Design of TDC ASIC based on Temperature Compensation	YICHAO, Ma