PCIexpress in MicroTCA.4 System at XFEL 150 NAT-MCH-PHYS and 50 NAT-MCH-PHYS80





PCIexpress in MicroTCA Systems Agenda



- PCIe: Topology, device tree, root complex
- Sub-dividing a system with N root complexes
- Hot-swap

lspci –s bb:dev.func Configuration Space





slide 3 I © 2018 N.A.T. GmbH I All trademarks and logos are property of their respective holders

PCIexpress devices & endpoints root@nat: #lspci



Ispci

00:00.0 Host bridge: Intel Corporation 2nd Generation Core Processor Family DRAM Controller (rev 09) 00:01.0 PCI bridge: Intel Corporation Xeon E3-1200/2nd Generation Core Processor Family PCI Express Root Port (rev 09)

00:01.1 PCI bridge: Intel Corporation Xeon E3-1200/2nd Generation Core Processor Family PCI Express Root Port (rev 09)

00:02.0 VGA compatible controller: Intel Corporation 2nd Generation Core Processor Family Integrated Graphics Controller (rev 09)

00:16.0 Communication controller: Intel Corporation 6 Series Chipset Family MEI Controller #1 (rev 04) 00:19.0 Ethernet controller: Intel Corporation 82579LM Gigabit Network Connection (rev 04)

00:1a.0 USB Controller: Intel Corporation 6 Series Chipset Family USB Enhanced Host Controller #2 (rev 04)

00:1c.0 PCI bridge: Intel Corporation 6 Series Chipset Family PCI Express Root Port 1 (rev b4) 00:1d.0 USB Controller: Intel Corporation 6 Series Chipset Family USB Enhanced Host Controller #1 (rev 04)

00:1f.0 ISA bridge: Intel Corporation QM67 Express Chipset Family LPC Controller (rev 04)

00:1f.2 IDE interface: Intel Corporation 6 Series Chipset Family 4 port **SATA** IDE Controller (rev 04)

00:1f.3 SMBus: Intel Corporation 6 Series Chipset Family SMBus Controller (rev 04)

00:1f.5 IDE interface: Intel Corporation 6 Series Chipset Family 2 port SATA IDE Controller (rev 04)

01:00.0 PCI bridge: Integrated Device Technology Inc. Device 808f

01:00.2 System peripheral: Integrated Device Technology Inc. Device 808f

02:08.0 PCI bridge: Integrated Device Technology Inc. Device 808f

03:00,0 PCI bridge: PLX Technology Inc. Device 8748 (rev ba)

04:00.0 PCI bridge: PLX Technology Inc. Device 8748 (rev ba)

Star Topology Any Slot can be Root Complex





slide 5 I © 2018 N.A.T. GmbH I All trademarks and logos are property of their respective holders

Application Example: MPI Greifswald 12 * 4 PCIe lanes 2 * 16 PCIe lanes



7.1.



- Opens the N.A.T. webpage in a new browser window.
- Shows this page.

Home:



PEX8748 Multi-Host Configuration: up to 6 Cluster





slide 8 I © 2018 N.A.T. GmbH I All trademarks and logos are property of their respective holders





NAT-MCH by N.A.T.



Setup

Base Configuration Switch HASE 1966 Age Time Port on/off Port VLAN 802.10 VLAN 802.1X 802.1p Port Mirroring **Jumbo Frame** Link Aggregation **Rapid Spanning Tree** Link Status BCM5396 counters Configure PCIe Virtual Switches

Maintenance

Board Information System Information Reboot NAT-MCH Update MCH Change Password N.A.T. Webpage Home

PCIe Virtual Switch configuration

Select Host AMCs (Upstream) for each virtual switch that shall be enabled first. Select Host AMCs (Non-Transparent Upstream) for each virtual switch that shall be enabled afterwards. Select which AMCs shall be connected to each virtual switch as downstream in the end.

Virtual Switch	Upstream AMC	NT- Upstream AMC	A M C 1 47	A M C 2 47	A M C 3 47	A M C 4 47	A M C 5 47	A M C 6 47	A M C 7 47
none			0	C	0	0	0	0	0
Virtual Switch 0	ETM 🙃	- 110118 - 🔁	0	0	0	o	0	0	0
Virtual Switch 1	AMO 6_4 👩		0	o	0	0	C	۲	C
Virtual Switch 2	- none - 🛛 📀		0	0	0	0	C	0	0
Virtual Switch 3	[-nons0]		0	0	0	0	0	0	0
Virtual Switch 4	- none 0		0	0	0	0	0	0	0
Virtual Switch 5	- nono ©		0	0	0	0	0	0	0
Max. Link Speed			SD GTA 😳	80.01/4 0	80 01/8 0	(8.0 GT/s G	8.0 GT/a 💈	S.O GTA 😳	8.0 61/4 😳
Apply									

Note: You need to click apply before you can save your changes to EEPROM.

PCIexpress Requirements Hot Plug and not Hotswap Solution





slide 11 I © 2018 N.A.T. GmbH I All trademarks and logos are property of their respective holders

Hot Removal of AMC with PCIe Hotswap handle pulled out: standard procedure



slide 12 I © 2018 N.A.T. GmbH I All trademarks and logos are property of their respective holders

Testing HotPlug Prepair MCH and OS

- The PCI Express Hot-Swap in MTCA depends on:
 - Linux OS Hotplug Driver (pciehp)
 - PCI Express Switch with the Hot-Plug controller on the MCH
- We have to ...
- Configure the MCH (PCI Express Switch)
 - Enable Hot-Plug Controller on the PCI Express Switch

PCIe parameter	Current Configuration						
configuration flags:							
upstream slot power up delay	25	sec <					
power up delay for AMC	15	80C					
100 MHz spread spectrum	disabl	ed c					
hot plug support	enabl	ed :) 🔶					
PCIe early ekey (before payload)	disabl	ed c					
Use PCIe on MCH-RTM(disable AMC12)	no	2					

Thank you very much! **Questions?**





Vollrath Dirksen vollrath@nateurope.com

N.A.T. GmbH Konrad-Zuse-Platz 9 53227 Bonn, Germany

www.nateurope.com

MTCA.4 Training:

techlab.desy.de/support/training

www.nateurope.com/services/support.html