

	Start	Author	Title
1	09:00	Ray Larsen	Introduction: 2007 was the first MicroTCA workshop @ RealTime ...
2	09:10	Ralf Waldt	ATCA, AMC, MicroTCA: the form factors
3	09:20	Ralf Waldt/ Vollrath Dirksen	Crates and power
4	09:30	Heiko Körte/ Vollrath Dirksen	System management topology MCH: states, power, alerts, FRU information
5	09:50	Vollrath Dirksen	Debugging a managed system
6	10:00	Kay Rehlich	Communication options: Ethernet, PCIe, SATA, Clocks, Triggers, interlocks, Point-to-point
7	10:17	Vollrath Dirksen	Status of the system communication links
	10:30		Coffee
8	11:00	Vollrath Dirksen	PCIe: Topology, device tree, root complex Hot-swap, Sub-dividing a system with N root complexes
9	11:25	Vollrath Dirksen	Debugging, life demo
10	11:35	Kay Rehlich	MTCA.4.1: 2nd backplane: Advantage and Use Case Rear PS, Protective covers, Compatibility Classes
11	11:50	Vollrath Dirksen	Demo/Video RF with backplane in operation
12	11:55	Jan Marjanovic	AMC, MMC, RTM development aspects
13	12:10	Jan Marjanovic	DESY MicroTCA TechLab Intoduction
	12:30		Lunch
14	13:30	Heiko Körte for PICMG	PICMG Specifications for Advanced Research Projects – A Brief History in Time
15	13:30	Dariusz Makowski, DMCS	Extension of MMC for diagnostics of high Power Piezo Driver
16	13:30	Zhen-An Lui, IHEP	xTCA based backend data processor development at IHEP
17	13:30	Ralf Waldt, nVent	Requirements for a MTCA crate and product overview
18	13:30	Kay Rehlich, DESY	MicroTCA @ European XFEL
	13:30		Coffee
19	13:30	Yifan Yang, University Libre de Bruxelles	uTCA-based readout system for CMS Triple-GEM detectors
20	13:30	Vollrath Dirksen, N.A.T.	High Bandwidth Data Aquisition
21	13:30	Carsten Watolla, Teledyne/ SP Devices	Latest MTCA high performance Digitizer technology
22	13:30	Jan Marjanovic, Desy TechLab	Data acquisition in MTCA with Xilinx FPGA
	13:30		End